

# UNISONIC TECHNOLOGIES CO., LTD

# **TIP112**

### NPN SILICON TRANSISTOR

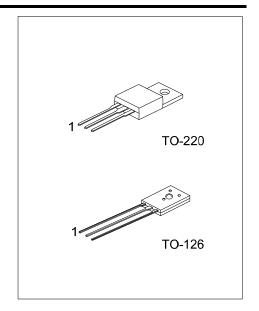
# NPN EPITAXIAL SILICON DARLINGTON TRANSISTOR

#### ■ DESCRIPTION

The UTC **TIP112** is designed for such applications as: DC/DC converters supply line switching, battery charger, LCD backlighting, peripheral drivers, Driver in low supply voltage applications (e.g. lamps and LEDs) and inductive load driver (e.g. relays, buzzers and motors).

#### ■ FEATURES

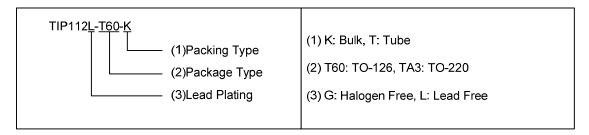
- \* High DC current gain :  $h_{FE}$  = 1000 @  $V_{CE}$  =4V,  $I_{C}$ =1A (Min)
- \* Low collector-emitter saturation voltage
- EQUIVALENT TEST (R<sub>1</sub>≈10kΩ, R<sub>2</sub>≈0.6kΩ)





#### ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
TIP112L-T60-K	TIP112G-T60-K	TO-126	Е	С	В	Bulk	
TIP112L-TA3-T	TIP112G-TA3-T	TO-220	В	С	Е	Tube	



### ■ ABSOLUTE MAXIMUM RATING (Ta=25°C)

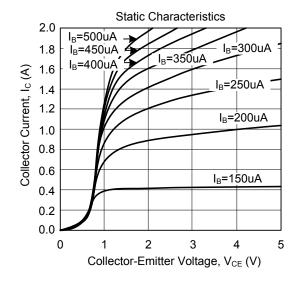
PARAMETER		SYMBOL	RATINGS	UNIT	
Collector to Base Voltage		$V_{CBO}$	100	V	
Collector to Emitter Voltage		$V_{CEO}$	100	V	
Emitter to Base Voltage		$V_{EBO}$	5	V	
Collector Current	DC	Ic	2	Α	
Collector Current	Peak	I <sub>CM</sub>	4		
Base Current (DC)		$I_{B}$	50	mA	
Callastar Dissination	Ta=25°C	D	2	W	
Collector Dissipation	T <sub>C</sub> =25°C	Pc	50		
Junction Temperature		TJ	150	°C	
Storage Temperature		$T_{STG}$	-65~+150	°C	

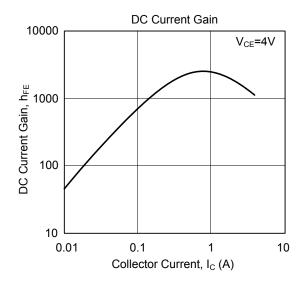
Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

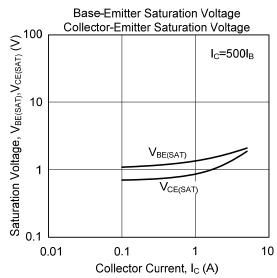
## ■ ELECTRICAL CHARACTERISTICS (Ta=25°C)

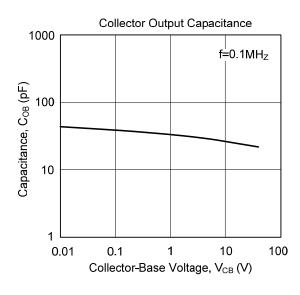
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Emitter Breakdown Voltage	V <sub>CEO(SUS)</sub>	I <sub>C</sub> =30mA, I <sub>B</sub> =0A	100			V
Collector-Emitter Saturation Voltage	$V_{CE(SAT)}$	I <sub>C</sub> =2A, I <sub>B</sub> =8mA			2.5	V
Base-Emitter Turn-On Voltage	$V_{BE(ON)}$	V <sub>CE</sub> =4V, I <sub>C</sub> =2A			2.8	
Collector-Base Cut-Off Current	I <sub>CBO</sub>	V <sub>CB</sub> =100V, I <sub>E</sub> =0A			1	mA
Collector-Emitter Cut-Off Current	I <sub>CEO</sub>	$V_{CE}$ =50V, $V_{B}$ =0A			2	mA
Emitter-Base Cut-Off Current	I <sub>EBO</sub>	V <sub>EB</sub> =5V, I <sub>C</sub> =0A			2	mA
DC Current Gain	h <sub>FE</sub>	V <sub>CE</sub> =4V, I <sub>C</sub> =1A	1000			
		$V_{CE}$ =4V, $I_{C}$ =2A	500			
Collector Capacitance	Сов	V <sub>CB</sub> =10V, I <sub>E</sub> =0A, f=0.1MHz			100	pF

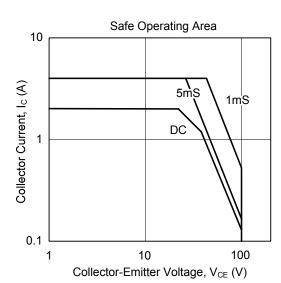
#### ■ TYPICAL CHARACTERISTICS

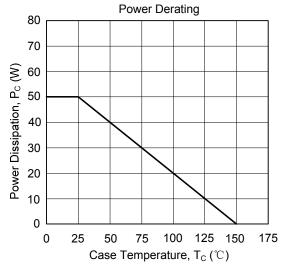












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