



Pulse
A TECHNITROL COMPANY

LAN Product Guide – PoE Applications



Available for Gigabit and 100Base-T data rates.



SMT (surface mount) discrete and THT (through hole) connector modules with integrated magnetics, available with or without LED's.



Meets IEEE 802.3af specification (350 mA, 48 V).



Available for Extended Temp., Low-Profile, PSE, and PD applications.

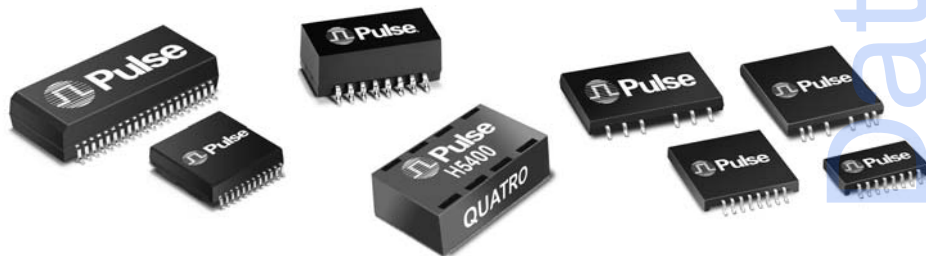


X = extended temp, ^A = AutoMDIX, **NL** = RoHS compliant.



Contact Pulse for PoE+.

DISCRETE MAGNETICS		CONNECTORS WITH INTEGRATED MAGNETICS	
10/100	Gigabit	10/100	Gigabit
SINGLE:	SINGLE:	1X1:	1X1:
H2019NL ^A H2260NL ^A HX2019NL ^{A, X} HX1393NL ^{A, X}	H5084NL ^A H6062NL ^A	JK0-0020NL ^A JXK0-0138NL ^{A, X} JK0-0025NL ^A JK0-0044NL ^A	JK0-0161NL ^A JK0-0133NL ^A
DUAL:	DUAL:	1XN:	
H2005ANL H2006ANL ^A H2009NL ^A	H6080NL ^A HX5200NL ^{A, X} H6020NL ^A HX5201NL ^{A, X}	JG0-0031NL ^A (1X2) JG0-0032NL ^A (1X4) JG0-0045NL ^A (1X6) JG0-0051NL ^A (1X8)	
QUAD:	QUAD:	2XN:	2XN:
H2014NL H1259NL ^A H2017NL ^A HX1259NL ^{A, X}	H5400NL ^A HX5400NL ^{A, X} H5401NL ^A HX5401NL ^{A, X}	J20-0017NL ^A (2X4) J20-0019NL ^A (2X6)	J0B-0182NL ^A (2X4) J0B-0183NL ^A (2X6) J0B-0170NL ^A (2X8)





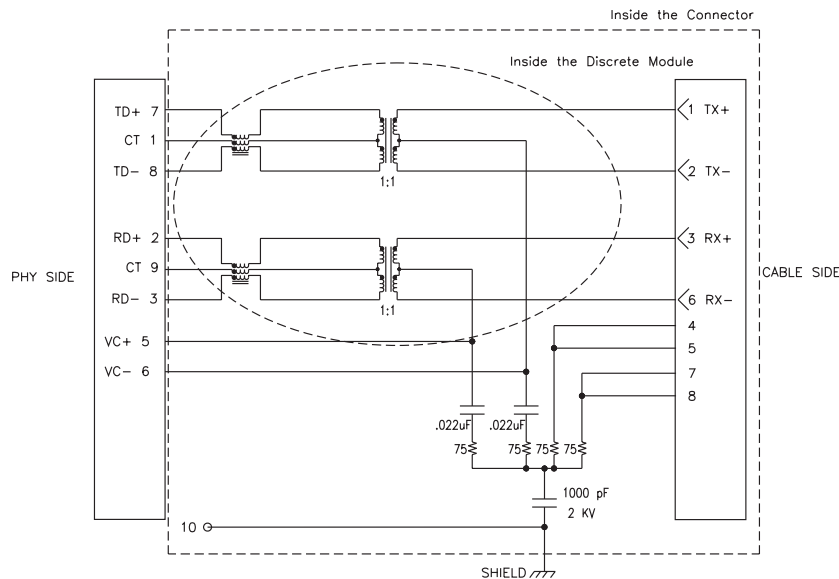
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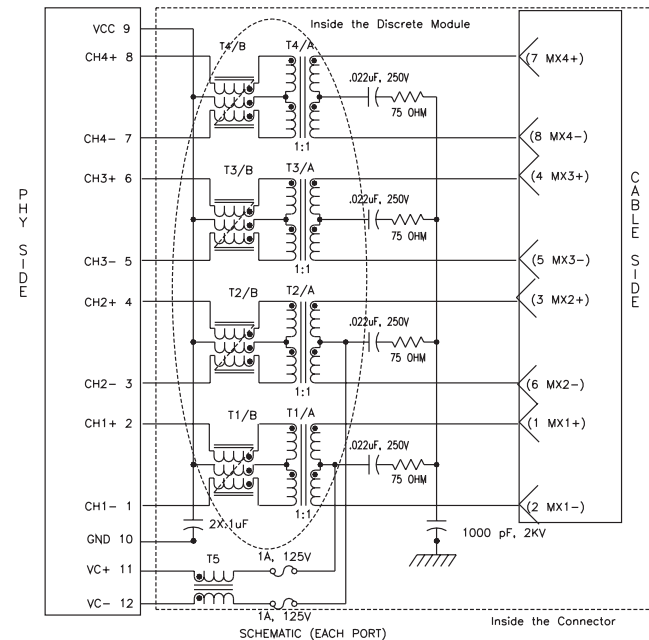
Application Notes

- Layering ground planes is advisable. Route connector/discrete module ground pins to chassis/analog ground if possible.
- Keep signal traces from PHY to connector/discrete module as short as possible. If traces exceed 3-4 inches, pay close attention to line impedance imbalance.
- Using BST (75Ω resistors and high voltage cap to chassis ground) to terminate Cable side CTs is advisable for best EMI performance (included in most connector solutions).
- Follow PHY manufacturer's application notes for further layout considerations.
- Common applications include: VoIP phones, WLAN APs, Security Cameras, POS Terminals, Keycard Access/Security.

10/100 APPLICATION CIRCUIT



GIGABIT APPLICATION CIRCUIT



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