

# MCR703A Series

Preferred Device

## Sensitive Gate Silicon Controlled Rectifiers

### Reverse Blocking Thyristors

PNPN devices designed for high volume, low cost consumer applications such as temperature, light and speed control; process and remote control; and warning systems where reliability of operation is critical.

#### Features

- Pb-Free Package is Available
- Small Size
- Passivated Die Surface for Reliability and Uniformity
- Low Level Triggering and Holding Characteristics
- Recommend Electrical Replacement for C106
- Surface Mount Package – Case 369C
- To Obtain “DPAK” in Straight Lead Version (Shipped in Sleeves):  
– Add '1' Suffix to Device Number, i.e., MCR706A1
- Epoxy Meets UL 94, V-0 @ 0.125 in
- ESD Ratings: Human Body Model, 3B > 8000 V  
Machine Model, C > 400 V



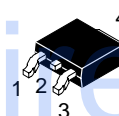
**ON Semiconductor®**

<http://onsemi.com>

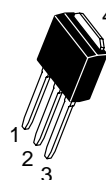
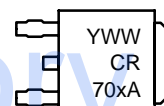
**SCRs**  
**4.0 AMPERES RMS**  
**100 – 600 VOLTS**



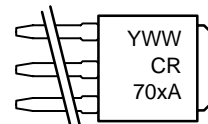
#### MARKING DIAGRAMS



**DPAK**  
**CASE 369C**  
**STYLE 2**



**DPAK-3**  
**CASE 369D**  
**STYLE 2**



Y = Year  
WW = Work Week  
x = 3, 6, or 8

#### PIN ASSIGNMENT

PIN ASSIGNMENT	
1	Gate
2	Anode
3	Cathode
4	Anode

#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

**Preferred** devices are recommended choices for future use and best overall value.

## MCR703A Series

### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

Rating	Symbol	Max	Unit
Peak Repetitive Off-State Voltage (Note 1) (T <sub>C</sub> = -40 to +110°C, Sine Wave, 50 to 60 Hz, Gate Open)	V <sub>DRM</sub> , V <sub>RRM</sub>	100 400 600	V
Peak Non-Repetitive Off-State Voltage (Sine Wave, 50 to 60 Hz, Gate Open, T <sub>C</sub> = -40 to +110°C)	V <sub>RSM</sub>	150 450 650	V
On-State RMS Current (180° Conduction Angles; T <sub>C</sub> = 90°C)	I <sub>T(RMS)</sub>	4.0	A
Average On-State Current (180° Conduction Angles) T <sub>C</sub> = -40 to +90°C T <sub>C</sub> = +100°C	I <sub>T(AV)</sub>	2.6 1.6	A
Non-Repetitive Surge Current (1/2 Sine Wave, 60 Hz, T <sub>J</sub> = 110°C) (1/2 Sine Wave, 1.5 ms, T <sub>J</sub> = 110°C)	I <sub>TSM</sub>	25 35	A
Circuit Fusing (t = 8.3 msec)	I <sup>2</sup> t	2.6	A <sup>2</sup> sec
Forward Peak Gate Power (Pulse Width ≤ 1.0 μsec, T <sub>C</sub> = 90°C)	P <sub>GM</sub>	0.5	W
Forward Average Gate Power (t = 8.3 msec, T <sub>C</sub> = 90°C)	P <sub>G(AV)</sub>	0.1	W
Forward Peak Gate Current (Pulse Width ≤ 1.0 μsec, T <sub>C</sub> = 90°C)	I <sub>GM</sub>	0.2	A
Operating Junction Temperature Range	T <sub>J</sub>	-40 to +110	°C
Storage Temperature Range	T <sub>stg</sub>	-40 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. V<sub>DRM</sub> and V<sub>RRM</sub> for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	R <sub>θJC</sub>	8.33	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2)	R <sub>θJA</sub>	80	°C/W
Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 10 Seconds	T <sub>L</sub>	260	°C

2. Case 369C when surface mounted on minimum pad sizes recommended.

## MCR703A Series

### ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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#### OFF CHARACTERISTICS

Peak Repetitive Forward or Reverse Blocking Current ( $V_{AK} = \text{Rated } V_{DRM}$ or $V_{RRM}$ ; $R_{GK} = 1 \text{ K}\Omega$ )		$I_{DRM}, I_{RRM}$	–	–	10	$\mu\text{A}$
$T_C = 25^\circ\text{C}$						
$T_C = 110^\circ\text{C}$				200		

#### ON CHARACTERISTICS

Peak Forward "On" Voltage ( $I_{TM} = 8.2 \text{ A Peak}$ , Pulse Width = 1 to 2 ms, 2% Duty Cycle)		$V_{TM}$	–	–	2.2	V
Gate Trigger Current (Continuous dc) (Note 3) ( $V_{AK} = 12 \text{ Vdc}$ , $R_L = 24 \text{ Ohms}$ )		$I_{GT}$	–	25	75	$\mu\text{A}$
$T_C = 25^\circ\text{C}$				–	300	
$T_C = -40^\circ\text{C}$						
Gate Trigger Voltage (Continuous dc) (Note 3) ( $V_{AK} = 12 \text{ Vdc}$ , $R_L = 24 \text{ Ohms}$ )		$V_{GT}$	–	–	0.8	V
$T_C = 25^\circ\text{C}$				–	1.0	
$T_C = -40^\circ\text{C}$						
Gate Non-Trigger Voltage (Note 3) ( $V_{AK} = 12 \text{ Vdc}$ , $R_L = 100 \text{ Ohms}$ , $T_C = 110^\circ\text{C}$ )		$V_{GD}$	0.2	–	–	V
Holding Current ( $V_{AK} = 12 \text{ Vdc}$ , Gate Open) (Initiating Current = 200 mA)		$I_H$	–	–	5.0	mA
$T_C = 25^\circ\text{C}$					10	
$T_C = -40^\circ\text{C}$						
Peak Reverse Gate Blocking Voltage ( $I_{GR} = 10 \mu\text{A}$ )		$V_{RGM}$	10	12.5	18	V
Peak Reverse Gate Blocking Current ( $V_{GR} = 10 \text{ V}$ )		$I_{RGM}$	–	–	1.2	$\mu\text{A}$
Total Turn-On Time (Source Voltage = 12 V, $R_S = 6 \text{ k}\Omega$ ) ( $I_{TM} = 8.2 \text{ A}$ , $I_{GT} = 2 \text{ mA}$ , Rated $V_{DRM}$ ) (Rise Time = 20 ns, Pulse Width = 10 $\mu\text{s}$ )		$t_{gt}$	–	2.0	–	$\mu\text{s}$

#### DYNAMIC CHARACTERISTICS

Critical Rate of Rise of Off-State Voltage ( $V_D = \text{Rated } V_{DRM}$ , $R_{GK} = 1 \text{ k}\Omega$ , Exponential Waveform, $T_C = 110^\circ\text{C}$ )		$dv/dt$	–	10	–	$\text{V}/\mu\text{s}$
Repetitive Critical Rate of Rise of On-State Current ( $C_f = 60 \text{ Hz}$ , $I_{PK} = 30 \text{ A}$ , $PW = 100 \mu\text{s}$ , $diG/dt = 1 \text{ A}/\mu\text{s}$ )		$di/dt$	–	–	100	$\text{A}/\mu\text{s}$

3.  $R_{GK}$  current not included in measurement.

#### ORDERING INFORMATION

Device	Package Type	Package	Shipping†
MCR703AT4	DPAK	369C	2500 Tape & Reel
MCR706AT4	DPAK	369C	2500 Tape & Reel
MCR706AT4G	DPAK (Pb-Free)	369C	2500 Tape & Reel
MCR708A	DPAK	369C	2500 Tape & Reel
MCR708A1	DPAK-3	369D	75 Units / Rail
MCR708AT4	DPAK	369C	2500 Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MCR703A Series

## Voltage Current Characteristic of SCR

Symbol	Parameter
$V_{DRM}$	Peak Repetitive Off-State Forward Voltage
$I_{DRM}$	Peak Forward Blocking Current
$V_{RRM}$	Peak Repetitive Off-State Reverse Voltage
$I_{RRM}$	Peak Reverse Blocking Current
$V_{TM}$	Peak On-State Voltage
$I_H$	Holding Current

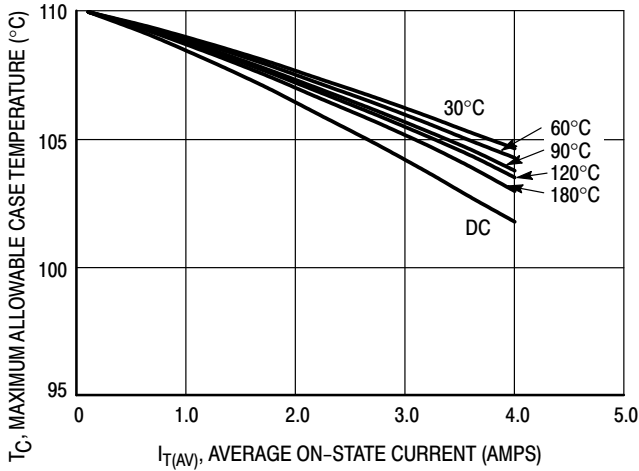
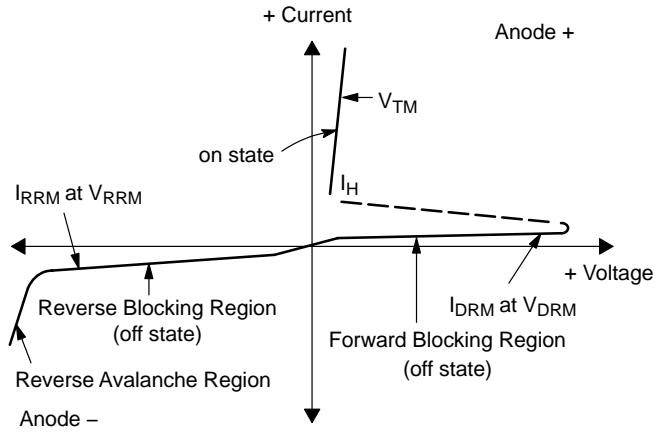


Figure 1. Average Current Derating

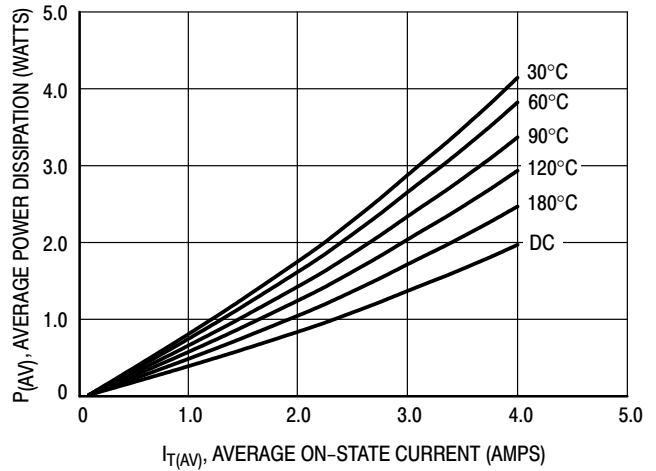


Figure 2. On-State Power Dissipation

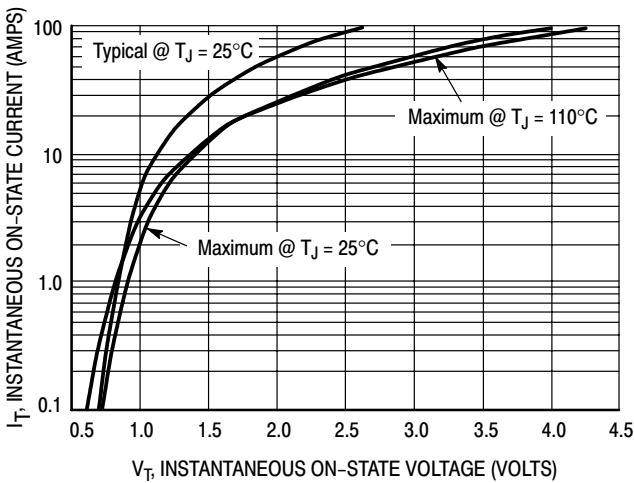


Figure 3. On-State Characteristics

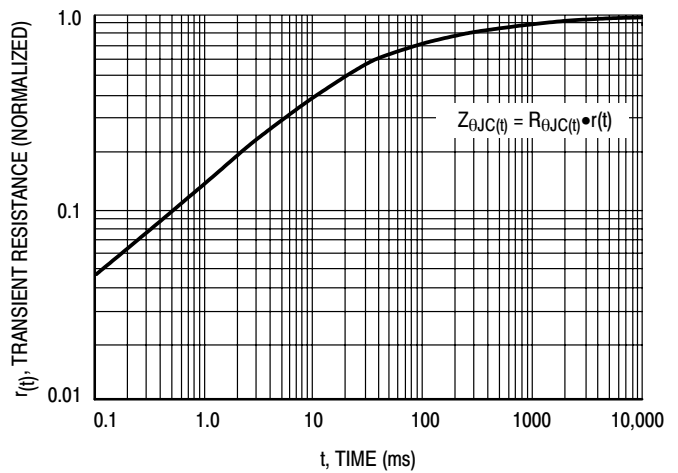


Figure 4. Transient Thermal Response

## MCR703A Series

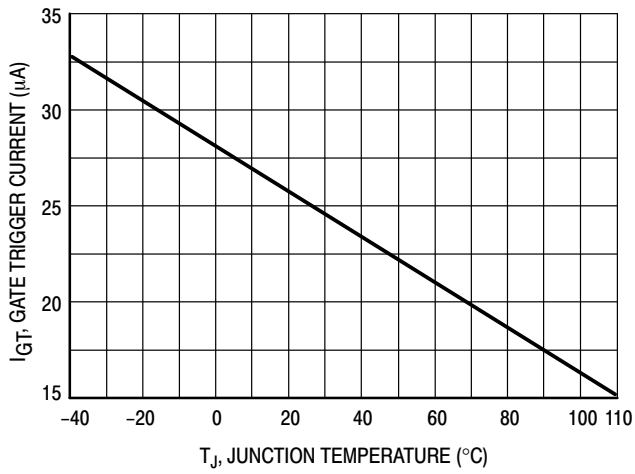


Figure 5. Typical Gate Trigger Current versus Junction Temperature

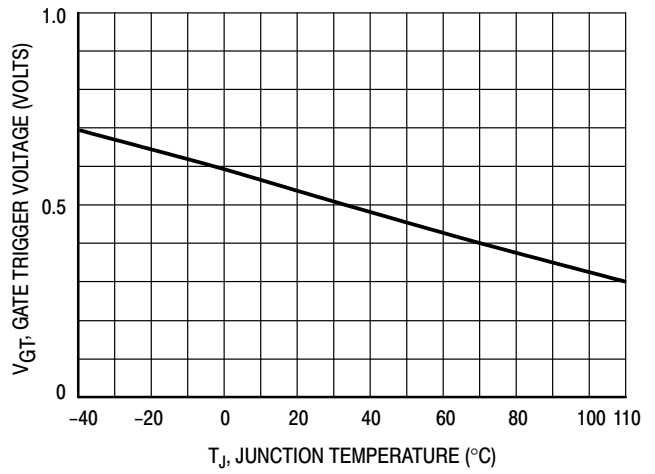


Figure 6. Typical Gate Trigger Voltage versus Junction Temperature

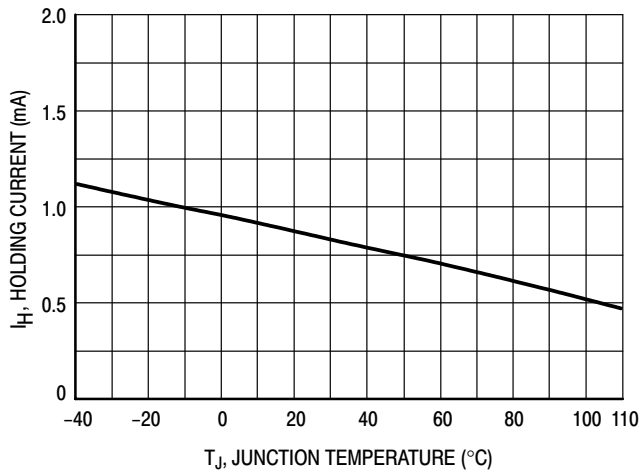


Figure 7. Typical Holding Current versus Junction Temperature

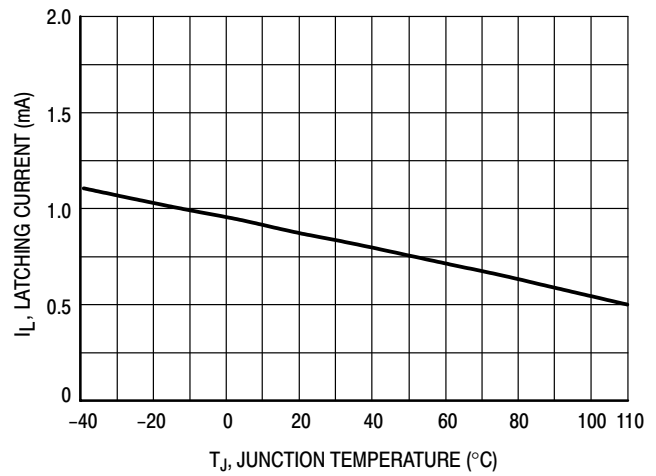
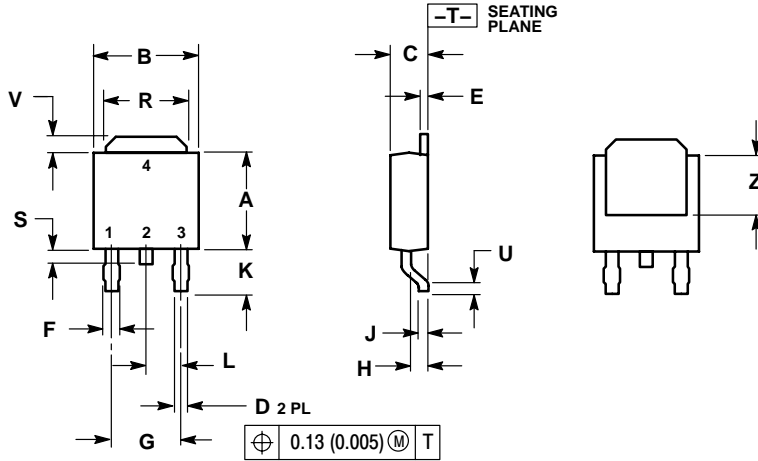


Figure 8. Typical Latching Current versus Junction Temperature

# MCR703A Series

## PACKAGE DIMENSIONS

DPAK  
CASE 369C  
ISSUE O

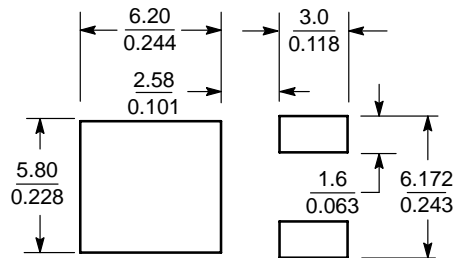


- NOTES:  
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.  
2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.235	0.245	5.97	6.22
B	0.250	0.265	6.35	6.73
C	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
E	0.018	0.023	0.46	0.58
F	0.037	0.045	0.94	1.14
G	0.180 BSC		4.58 BSC	
H	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.102	0.114	2.60	2.89
L	0.090 BSC		2.29 BSC	
R	0.180	0.215	4.57	5.45
S	0.025	0.040	0.63	1.01
U	0.020	---	0.51	---
V	0.035	0.050	0.89	1.27
Z	0.155	---	3.93	---

- STYLE 2:  
PIN 1. GATE  
2. DRAIN  
3. SOURCE  
4. DRAIN

### SOLDERING FOOTPRINT\*



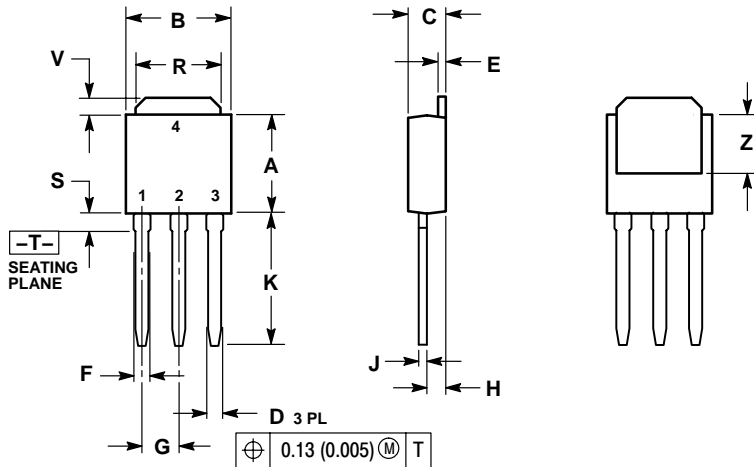
SCALE 3:1  $\left(\frac{\text{mm}}{\text{inches}}\right)$

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# MCR703A Series

## PACKAGE DIMENSIONS

DPAK-3  
CASE 369D-01  
ISSUE B



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.235	0.245	5.97	6.35
B	0.250	0.265	6.35	6.73
C	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
E	0.018	0.023	0.46	0.58
F	0.037	0.045	0.94	1.14
G	0.090 BSC		2.29 BSC	
H	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.350	0.380	8.89	9.65
R	0.180	0.215	4.45	5.45
S	0.025	0.040	0.63	1.01
V	0.035	0.050	0.89	1.27
Z	0.155	---	3.93	---

STYLE 2:

- PIN 1. GATE
2. DRAIN
3. SOURCE
4. DRAIN

# MCR703A Series

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