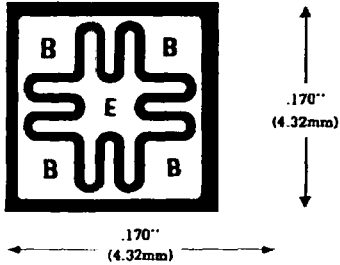


MEDIUM VOLTAGE

CHIP NUMBER

345



NPN SINGLE DIFFUSED MESA TRANSISTOR (FORMERLY 45)

CONTACT METALLIZATION

Base, Emitter and Collector Solder Coated 95/5% lead/tin.

ASSEMBLY RECOMMENDATIONS

It is advisable that:

- a) the chip be assembled in a reducing gas atmosphere.
- b) passivation be performed with a suitable junction coating material.

Special assemblies with the 345 chip mounted on nickel plated copper pedestals can be produced for individual requirements.

Base: .042" x .065" (1.07mm x 1.65mm)
 Emitter: .044" x .044" (1.12mm x 1.12mm)

TYPICAL ELECTRICAL CHARACTERISTICS AT 25°C

The following typical electrical characteristics apply for a completely finished component employing the chip number 345 in a TO-3 or equivalent case:

V _{CEO}	V _{CE(s)} @	I _C	I _B	h _{FE} @	I _C	V _{CE}
> 40V	< 1.1V	4.0A	0.4A	> 20-70	4.0A	5.0V
> 60V	< 1.1V	4.0A	0.4A	> 20-70	4.0A	5.0V

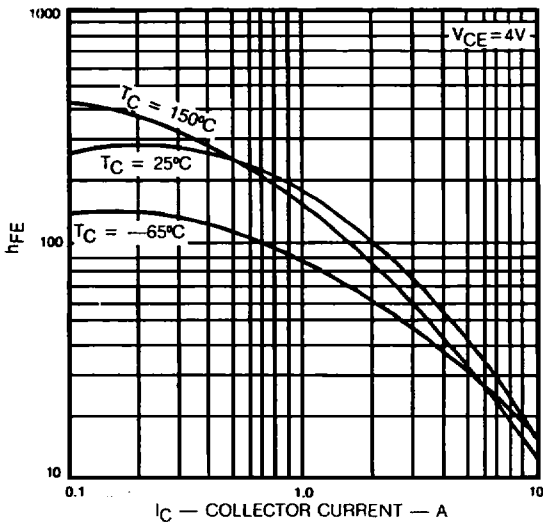
V _{CEO}	V _{CB0}	V _{EBO}	f _T	θ _{JC}
> 45V	100V	> 7V	0.8MHz	< 1.0°C/W
> 60V	100V	> 7V	0.8MHz	< 1.0°C/W

TYPICAL DEVICE TYPES: JAN2N3055, 2N6253, 2N3232, 2N6099, 2N6101

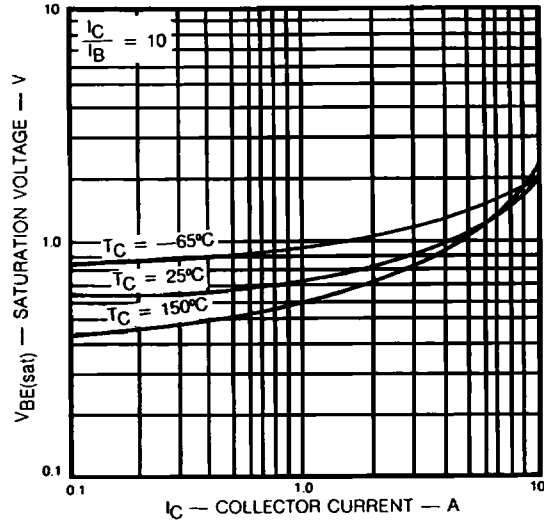
I_S/I_B rated to 117W (60V, 1.95A for 1 sec.)

CHIP TYPE 345

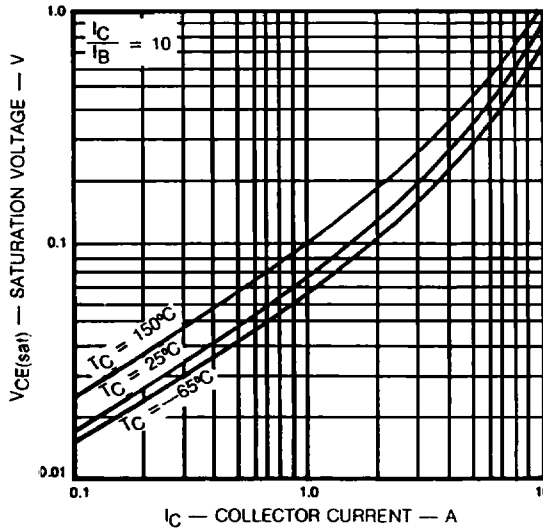
TYPICAL STATIC FORWARD CURRENT TRANSFER RATIO



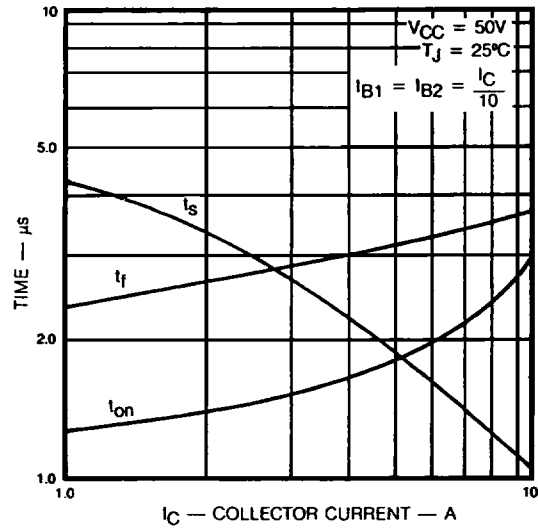
TYPICAL BASE EMITTER SATURATION VOLTAGE



TYPICAL COLLECTOR EMITTER SATURATION VOLTAGE



TYPICAL SWITCHING TIME



MAXIMUM OPERATING CONDITIONS

