

- 1N962BUR-1 THRU 1N986BUR-1 AVAILABLE IN JAN, JANTX AND JANTXV
- ZENER DIODES
- LEADLESS PACKAGE FOR SURFACE MOUNT
- DOUBLE PLUG CONSTRUCTION

**1N962BUR-1 THRU
1N986BUR-1
AND
CDLL957B THRU CDLL986B**

MAXIMUM RATINGS

Operating Temperature: -65°C to +200°C
 Storage Temperature: -65°C to +200°C
 DC Power Dissipation: 400 mW @ 50°C
 Power Derating: 2.67 mW/°C above 50°C
 Forward Voltage @ 200mA: 1.5 volts maximum

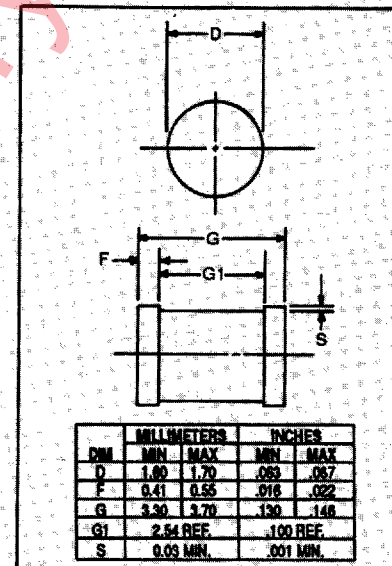
ELECTRIAL CHARACTERISTICS @ 25°C

CDI TYPE NUMBER (Note 1)	NOMINAL ZENER VOLTAGE (Note 2) V_Z VOLTS	ZENER TEST CURRENT I_{ZT} mA	MAXIMUM ZENER IMPEDANCE (Note 3)			MAX. DC ZENER CURRENT I_{ZM} mA	MAX. REVERSE LEAKAGE CURRENT $I_R @ V_R$	
			$Z_{ZT} @ I_{ZT}$		μA		Volts	
			OHMS	OHMS				OHMS
CDLL957B	6.8	18.5	4.5	700	1.0	55	150	5.2
CDLL958B	7.5	16.5	5.5	700	.5	50	75	5.7
CDLL959B	8.2	15.0	6.5	700	.5	45	50	6.2
CDLL960B	9.1	14.0	7.5	700	.5	41	25	6.9
CDLL961B	10	12.5	8.5	700	.25	38	10	7.6
CDLL962B	11	11.5	9.5	700	.25	32	5	8.4
CDLL963B	12	10.5	11.5	700	.25	31	5	9.1
CDLL964B	13	9.5	13	700	.25	28	5	9.9
CDLL965B	15	8.5	16	700	.25	25	5	11.4
CDLL966B	16	7.8	17	700	.25	24	5	12.2
CDLL967B	18	7.0	21	750	.25	20	5	13.7
CDLL968B	20	6.2	25	750	.25	18	5	15.2
CDLL969B	22	5.6	29	750	.25	16	5	16.7
CDLL970B	24	5.2	33	750	.25	15	5	18.2
CDLL971B	27	4.6	41	750	.25	13	5	20.6
CDLL972B	30	4.2	49	1000	.25	12	5	22.8
CDLL973B	33	3.8	58	1000	.25	11	5	25.1
CDLL974B	36	3.4	70	1000	.25	10	5	27.4
CDLL975B	39	3.2	90	1000	.25	9.5	5	29.7
CDLL976B	43	3.0	93	1500	.25	8.8	5	32.7
CDLL977B	47	2.7	105	1500	.25	7.9	5	35.8
CDLL978B	51	2.5	125	1500	.25	7.4	5	38.8
CDLL979B	56	2.2	150	2000	.25	6.8	5	42.6
CDLL980B	62	2.0	185	2000	.25	6.0	5	47.1
CDLL981B	68	1.8	230	2000	.25	5.5	5	51.7
CDLL982B	75	1.7	270	2000	.25	5.0	5	56.0
CDLL983B	82	1.5	330	3000	.25	4.6	5	62.2
CDLL984B	91	1.4	400	3000	.25	4.1	5	69.2
CDLL985B	100	1.3	500	3000	.25	3.7	5	76.0
CDLL986B	110	1.1	750	4000	.25	3.3	5	83.6

NOTE 1 "B" suffix = ± 5%, "A" suffix = ± 10%, no suffix = ± 20% of nominal Zener voltage.

NOTE 2 Zener voltage is measured with the device junction in thermal equilibrium at an ambient temperature of 25°C.

NOTE 3 Zener impedance is derived by superimposing on I_{ZT} a 60Hz rms a.c. current equal to 10% of I_{ZT} .



DESIGN DATA

CASE: DO-213AA, Hermetically sealed glass case. (MELF, SOD-80, LL34)

LEAD FINISH: Tin/Lead

THERMAL RESISTANCE: Junction to END CAP = 100 °C/W maximum

POLARITY: Diode to be operated with the banded (cathode) end positive with respect to the opposite end.

WEIGHT: 0.05 grams (Typical)

MOUNTING POSITION: Any.

MOUNTING SURFACE SELECTION: The Axial Coefficient Of Expansion (COE) Of this Device Is Approximately +8PPM/°C. The COE of the Mounting Surface System Should Be Selected To Provide A Suitable Match With This Device.



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CDLL957 thru CDLL986B

FIGURE 2

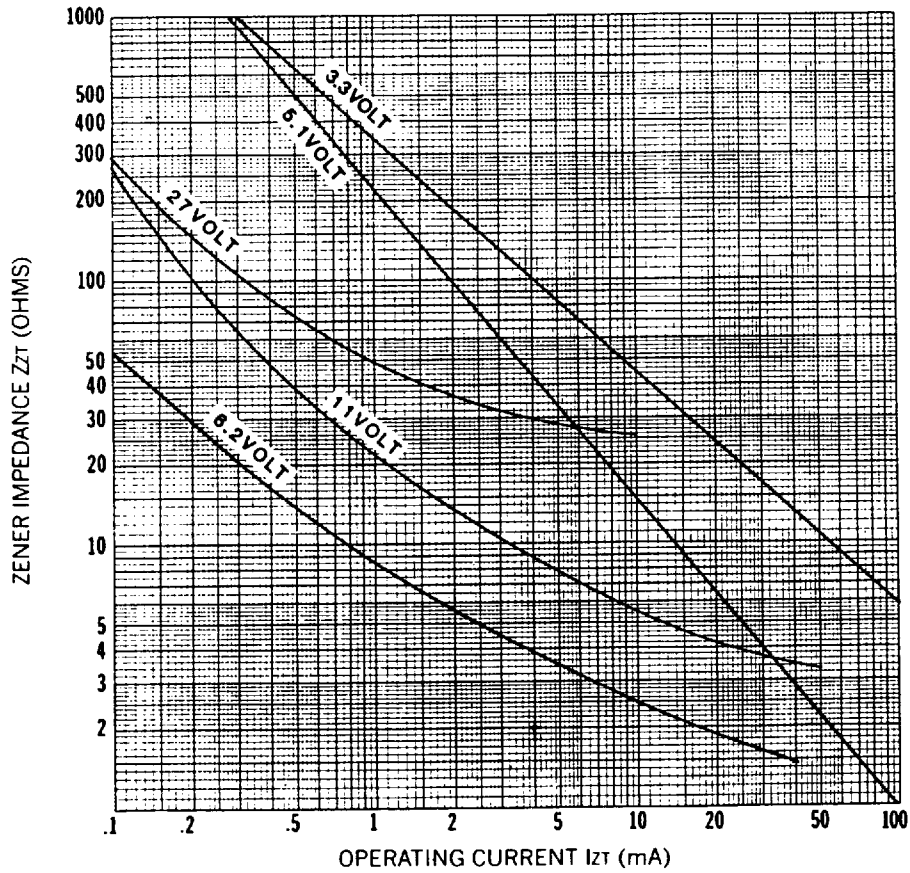
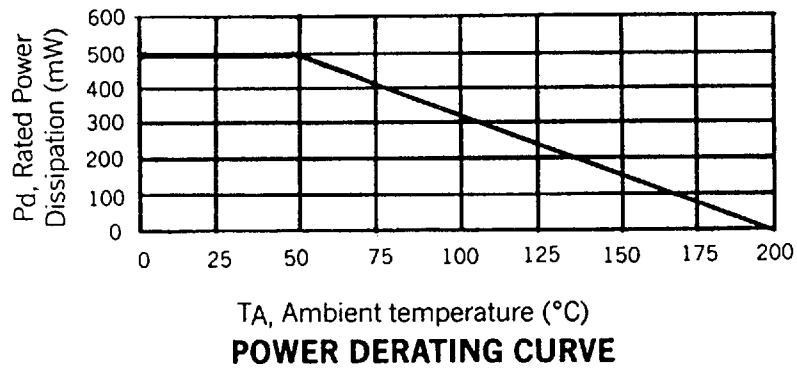


FIGURE 3

TYPICAL CHANGE OF ZENER IMPEDANCE WITH CHANGE IN OPERATING CURRENT FOR NOMINAL VOLTAGES SHOWN