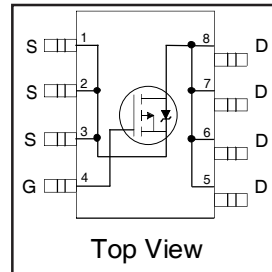


IRF7233PbF

HEXFET® Power MOSFET

- Ultra Low On-Resistance
- P-Channel MOSFET
- Surface Mount
- Available in Tape & Reel
- Lead-Free



$V_{DSS} = -12V$
$R_{DS(on)} = 0.020\Omega$

Description

These P-Channel MOSFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit provides the designer with an extremely efficient device for use in battery and load management applications.

The SO-8 has been modified through a customized leadframe for enhanced thermal characteristics and multiple-die capability making it ideal in a variety of power applications. With these improvements, multiple devices can be used in an application with dramatically reduced board space. The package is designed for vapor phase, infrared, or wave soldering techniques.



Absolute Maximum Ratings

	Parameter	Max.	Units
V_{DS}	Drain- Source Voltage	-12	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V$	± 9.5	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V$	± 6.0	
I_{DM}	Pulsed Drain Current ①	± 76	
$P_D @ T_A = 25^\circ C$	Power Dissipation	2.5	W
$P_D @ T_A = 70^\circ C$	Power Dissipation	1.6	
	Linear Derating Factor	0.02	W/°C
E_{AS}	Single Pulse Avalanche Energy②	60	mJ
V_{GS}	Gate-to-Source Voltage	± 12	V
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to + 150	°C

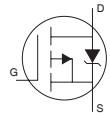
Thermal Resistance

	Parameter	Max.	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient③	50	°C/W

Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	-14	—	—	V	V _{GS} = 0V, I _D = -5.0mA
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	-12	—	—	V	V _{GS} = 0V, I _D = -250μA
ΔV _{(BR)DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient	—	0.001	—	V/°C	Reference to 25°C, I _D = -1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance	—	0.013	0.020	Ω	V _{GS} = -4.5V, I _D = -9.5A ②
		—	0.023	0.033		V _{GS} = -2.5V, I _D = -6.0A ②
V _{GS(th)}	Gate Threshold Voltage	-0.6	—	—	V	V _{DS} = V _{GS} , I _D = -250μA
g _{fs}	Forward Transconductance	3.3	—	—	S	V _{DS} = -10V, I _D = -9.5A
I _{DSS}	Drain-to-Source Leakage Current	—	—	-10	μA	V _{DS} = -12V, V _{GS} = 0V
		—	—	-1.0		V _{DS} = -9.6V, V _{GS} = 0V
		—	—	-100		V _{DS} = -12V, V _{GS} = 0V, T _J = 70°C
I _{GSS}	Gate-to-Source Forward Leakage	—	—	-100	nA	V _{GS} = -12V
	Gate-to-Source Reverse Leakage	—	—	100		V _{GS} = 12V
Q _g	Total Gate Charge	—	49	74	nC	I _D = -9.5A
Q _{gs}	Gate-to-Source Charge	—	9.3	14		V _{DS} = -10V
Q _{gd}	Gate-to-Drain ("Miller") Charge	—	22	32		V _{GS} = -5.0V ②
t _{d(on)}	Turn-On Delay Time	—	26	—	ns	V _{DD} = -10V
t _r	Rise Time	—	540	—		I _D = -9.5A
t _{d(off)}	Turn-Off Delay Time	—	77	—		R _D = 1.0Ω
t _f	Fall Time	—	370	—		R _G = 6.2Ω ②
C _{iss}	Input Capacitance	—	4530	6000	pF	V _{GS} = 0V
C _{oss}	Output Capacitance	—	2400	—		V _{DS} = -10V
C _{rss}	Reverse Transfer Capacitance	—	2220	—		f = 1.0kHz

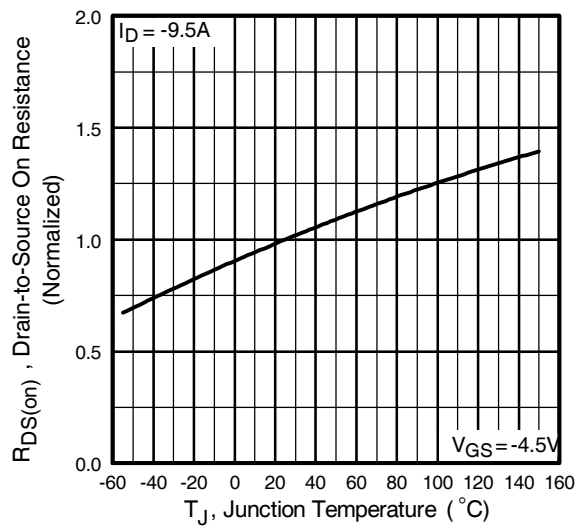
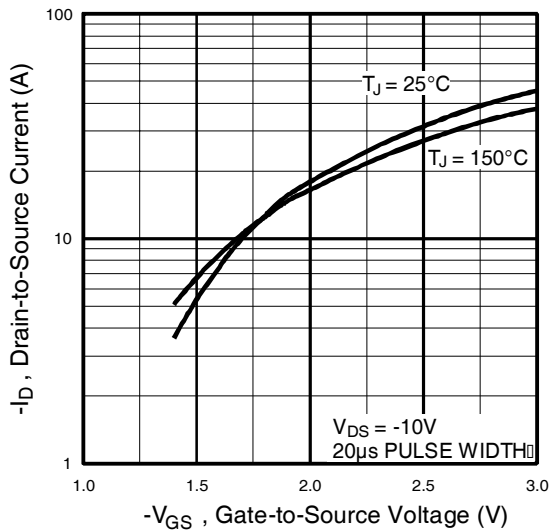
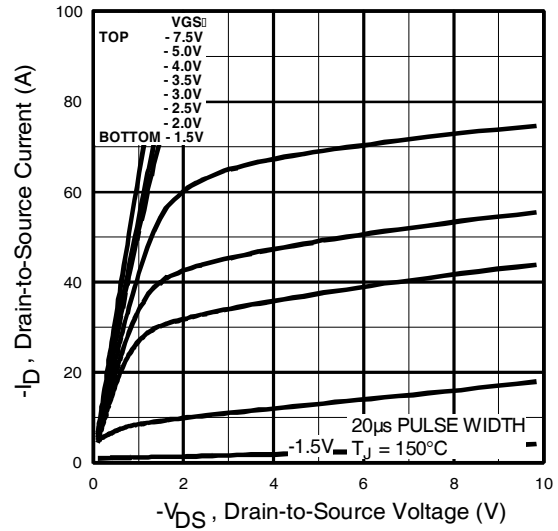
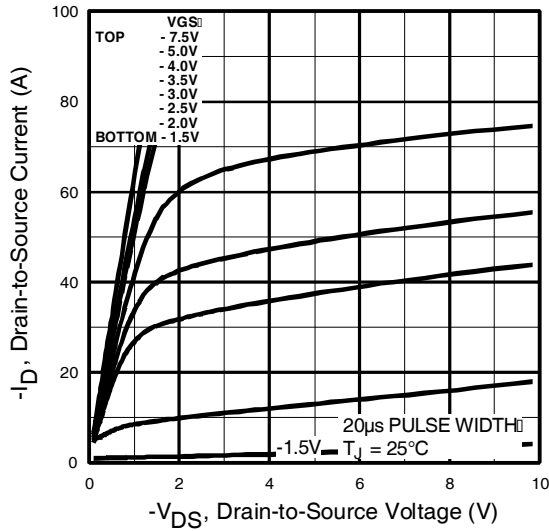
Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I _S	Continuous Source Current (Body Diode)	—	—	-2.5	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I _{SM}	Pulsed Source Current (Body Diode) ①	—	—	-76		
V _{SD}	Diode Forward Voltage	—	—	-1.2	V	T _J = 25°C, I _S = -2.5A, V _{GS} = 0V ②
t _{rr}	Reverse Recovery Time	—	43	65	ns	T _J = 25°C, I _F = -2.5A
Q _{rr}	Reverse Recovery Charge	—	35	52	nC	di/dt = 100A/μs ②

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
② Pulse width ≤ 300μs; duty cycle ≤ 2%.

- ③ When mounted on 1 inch square copper board, t < 10 sec
④ Starting T_J = 25°C, L = 1.3mH
R_G = 25Ω, I_{AS} = 9.5A.



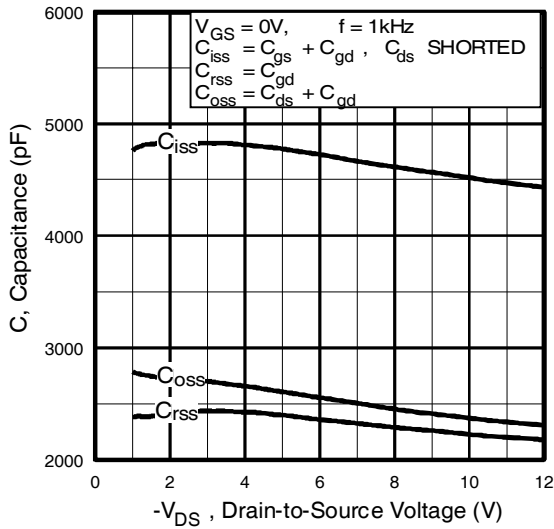


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

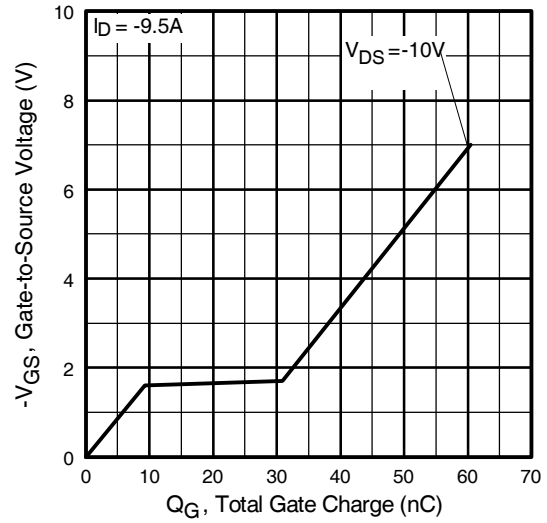


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

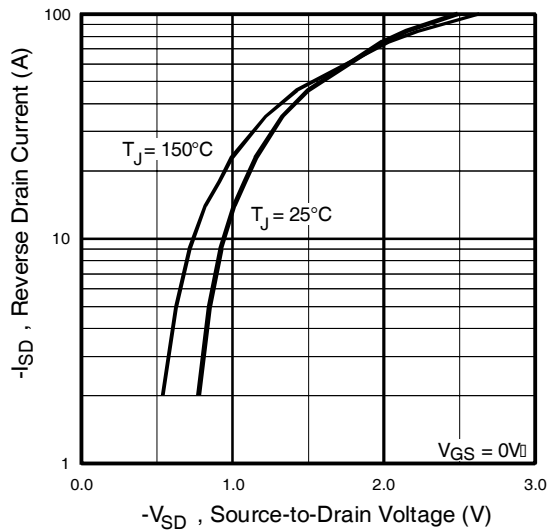


Fig 7. Typical Source-Drain Diode Forward Voltage

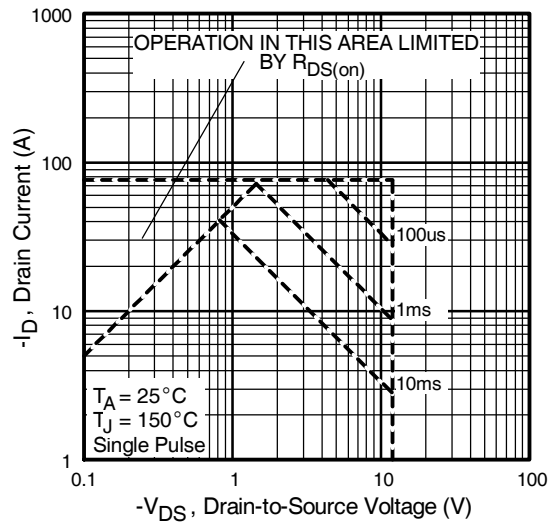


Fig 8. Maximum Safe Operating Area

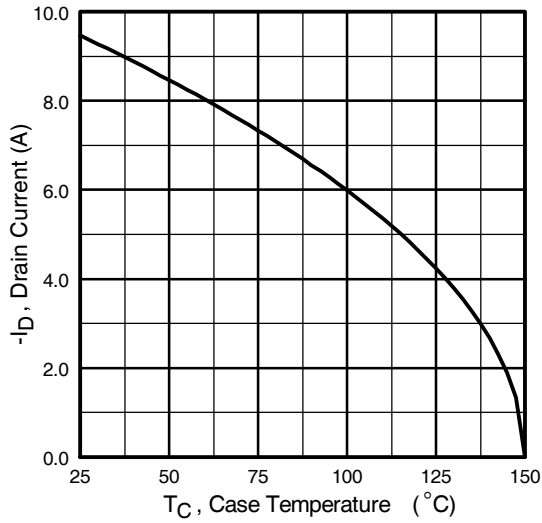


Fig 9. Maximum Drain Current Vs. Case Temperature

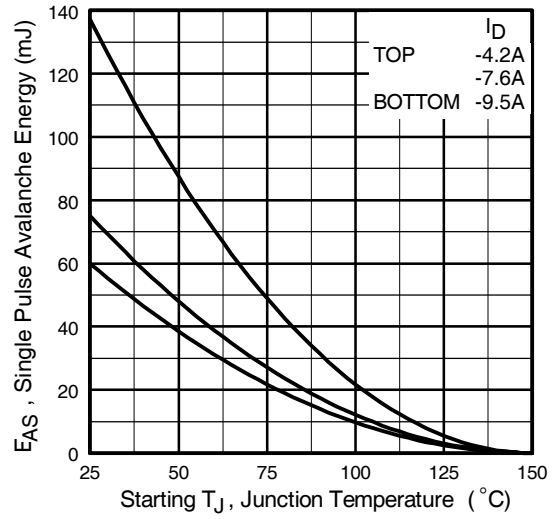


Fig 10. Maximum Avalanche Energy Vs. Drain Current

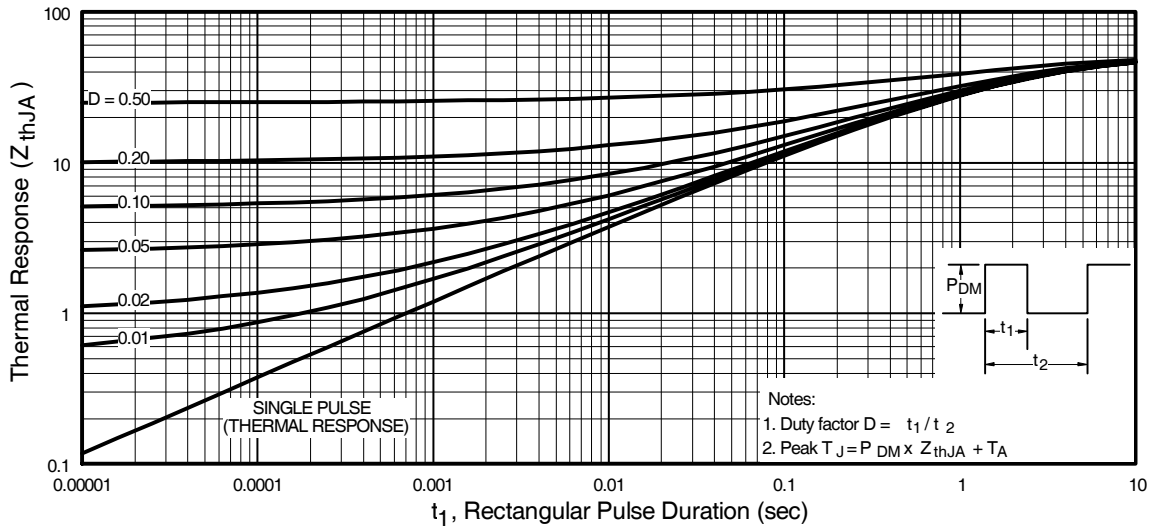


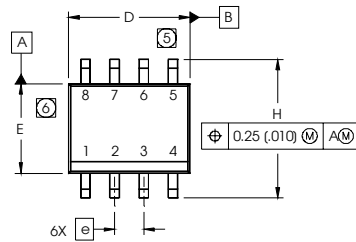
Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

IRF7233PbF

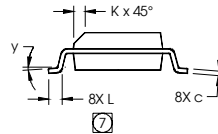
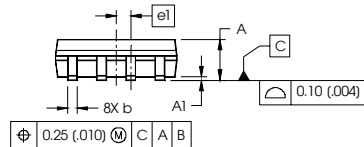


SO-8 Package Outline

Dimensions are shown in millimeters (inches)

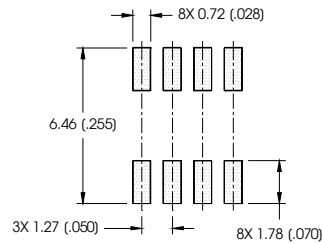


DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
c	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
E	.1497	.1574	3.80	4.00
e	.050 BASIC		1.27 BASIC	
e1	.025 BASIC		0.635 BASIC	
H	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.050	0.40	1.27
y	0°	8°	0°	8°



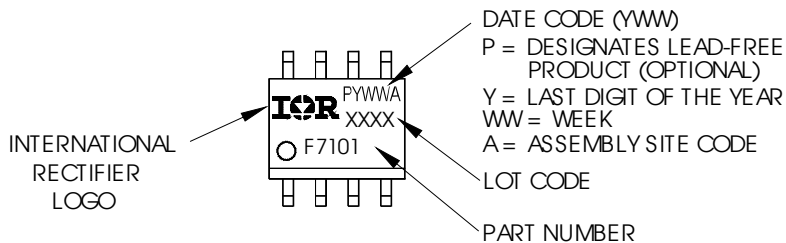
- NOTES:
1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
 2. CONTROLLING DIMENSION: MILLIMETER
 3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
 4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
 5. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 (0.006).
 6. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 (0.010).
 7. DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE.

FOOTPRINT



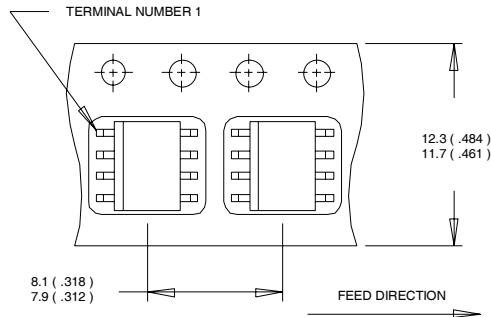
SO-8 Part Marking Information (Lead-Free)

EXAMPLE: THIS IS AN IRF7101 (MOSFET)



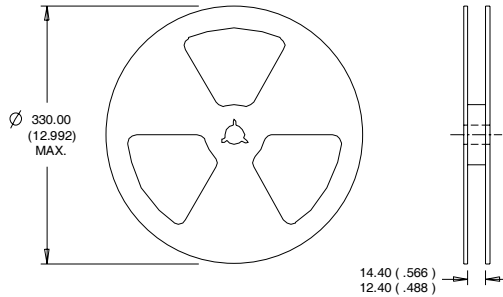
SO-8 Tape and Reel

Dimensions are shown in millimeters (inches)



NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Data and specifications subject to change without notice.
 This product has been designed and qualified for the Consumer market.
 Qualifications Standards can be found on IR's Web site.