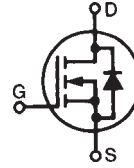


# PolarHV™ HiPerFET Power MOSFET

## IXFN 82N60P

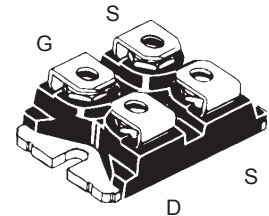
$V_{DSS} = 600 \text{ V}$   
 $I_{D25} = 82 \text{ A}$   
 $R_{DS(on)} \leq 75 \text{ m}\Omega$   
 $t_{rr} \leq 200 \text{ ns}$

N-Channel Enhancement Mode  
Avalanche Rated  
Fast Intrinsic Diode



Symbol	Test Conditions	Maximum Ratings	
		Value	Unit
$V_{DSS}$	$T_J = 25^\circ\text{C}$ to $150^\circ\text{C}$	600	V
$V_{DGR}$	$T_J = 25^\circ\text{C}$ to $150^\circ\text{C}$ ; $R_{GS} = 1 \text{ M}\Omega$	600	V
$V_{GSS}$	Continuous	$\pm 30$	V
$V_{GSM}$	Transient	$\pm 40$	V
$I_{D25}$	$T_C = 25^\circ\text{C}$	72	A
$I_{DM}$	$T_C = 25^\circ\text{C}$ , pulse width limited by $T_{JM}$	200	A
$I_{AR}$	$T_C = 25^\circ\text{C}$	82	A
$E_{AR}$	$T_C = 25^\circ\text{C}$	100	mJ
$E_{AS}$	$T_C = 25^\circ\text{C}$	5	J
$dv/dt$	$I_S \leq I_{DM}$ , $di/dt \leq 100 \text{ A}/\mu\text{s}$ , $V_{DD} \leq V_{DSS}$ , $T_J \leq 150^\circ\text{C}$ , $R_G = 2 \Omega$	20	V/ns
$P_D$	$T_C = 25^\circ\text{C}$	1040	W
$T_J$		-55 ... +150	$^\circ\text{C}$
$T_{JM}$		150	$^\circ\text{C}$
$T_{stg}$		-55 ... +150	$^\circ\text{C}$
$T_L$	1.6 mm (0.062 in.) from case for 10 s	300	$^\circ\text{C}$
$V_{ISOL}$	50/60 Hz, RMS, $I_{ISOL} \leq 1 \text{ mA}$	$T = 1 \text{ min}$ 2500 $T = 1 \text{ s}$ 3000	V~ V~
$M_d$	Mounting torque, Terminal connection torque	1.5/13	lb.in.
<b>Weight</b>		30	g

miniBLOC, SOT-227 B (IXFN)  
E153432



G = Gate  
S = Source  
D = Drain

Either Source terminal S can be used as the Source terminal or the Kelvin Source (gate return) terminal.

### Features

- International standard package
- Encapsulating epoxy meets UL 94 V-0, flammability classification
- miniBLOC with Aluminium nitride isolation
- Low  $R_{DS(on)}$  HDMOS™ process
- Rugged polysilicon gate cell structure
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
- Fast intrinsic Rectifier

### Applications

- DC-DC converters
- Synchronous rectification
- Battery chargers
- Switched-mode and resonant-mode power supplies
- DC choppers
- Temperature and lighting controls
- Low voltage relays

### Advantages

- Easy to mount
- Space savings
- High power density

Symbol	Test Conditions ( $T_J = 25^\circ\text{C}$ , unless otherwise specified)	Characteristic Values		
		Min.	Typ.	Max.
$BV_{DSS}$	$V_{GS} = 0 \text{ V}$ , $I_D = 3 \text{ mA}$	600		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 8 \text{ mA}$	3.0		5.0 V
$I_{GSS}$	$V_{GS} = \pm 30 \text{ V}_{DC}$ , $V_{DS} = 0$			$\pm 200 \text{ nA}$
$I_{DSS}$	$V_{DS} = V_{DSS}$ $V_{GS} = 0 \text{ V}$ $T_J = 125^\circ\text{C}$			25 $\mu\text{A}$ 1000 $\mu\text{A}$
$R_{DS(on)}$	$V_{GS} = 10 \text{ V}$ , $I_D = I_T$ , Note 1			75 $\text{m}\Omega$

Symbol	Test Conditions	Characteristic Values ( $T_J = 25^\circ\text{C}$ , unless otherwise specified)		
		Min.	Typ.	Max.
$g_{fs}$	$V_{DS} = 20\text{ V}; I_D = I_T$ , Note 1	50	80	S
$C_{iss}$ $C_{oss}$ $C_{rss}$	$V_{GS} = 0\text{ V}, V_{DS} = 25\text{ V}, f = 1\text{ MHz}$		23	nF
			1490	pF
			200	pF
$t_{d(on)}$ $t_r$ $t_{d(off)}$ $t_f$	$V_{GS} = 10\text{ V}, V_{DS} = 0.5 V_{DSS}, I_D = I_T$ $R_G = 1\ \Omega$ (External)		28	ns
			23	ns
			79	ns
			24	ns
$Q_{g(on)}$ $Q_{gs}$ $Q_{gd}$	$V_{GS} = 10\text{ V}, V_{DS} = 0.5 V_{DSS}, I_D = I_T$		240	nC
			96	nC
			67	nC
$R_{thJC}$ $R_{thCS}$			0.12	$^\circ\text{C/W}$
		0.13		$^\circ\text{C/W}$

### Source-Drain Diode

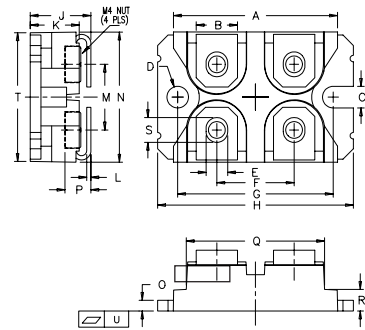
Symbol	Test Conditions	Characteristic Values ( $T_J = 25^\circ\text{C}$ , unless otherwise specified)		
		Min.	Typ.	Max.
$I_S$	$V_{GS} = 0\text{ V}$			82 A
$I_{SM}$	Repetitive			200 A
$V_{SD}$	$I_F = I_S, V_{GS} = 0\text{ V}$ , Note 1			1.5 V
$t_{rr}$ $Q_{RM}$ $I_{RM}$	$I_F = 25\text{ A}, -di/dt = 100\text{ A}/\mu\text{s}$ $V_R = 100\text{ V}$			200 ns
			0.6	$\mu\text{C}$
			6.0	A

### Notes:

1. Pulse test,  $t \leq 300\ \mu\text{s}$ , duty cycle  $d \leq 2\%$

Test Current  $I_T = 41\text{ A}$

### SOT-227B Outline

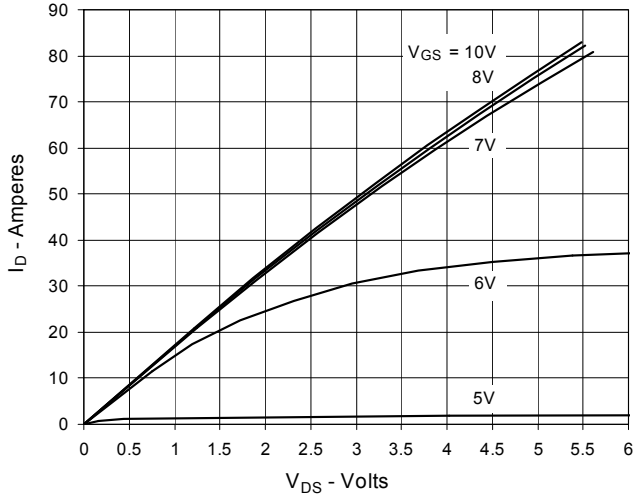
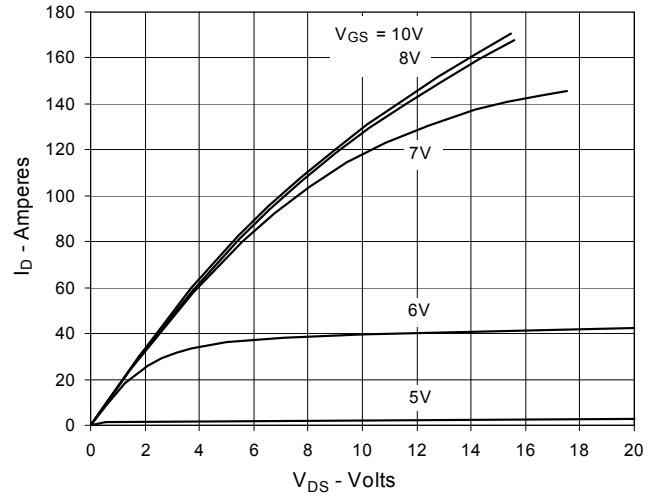
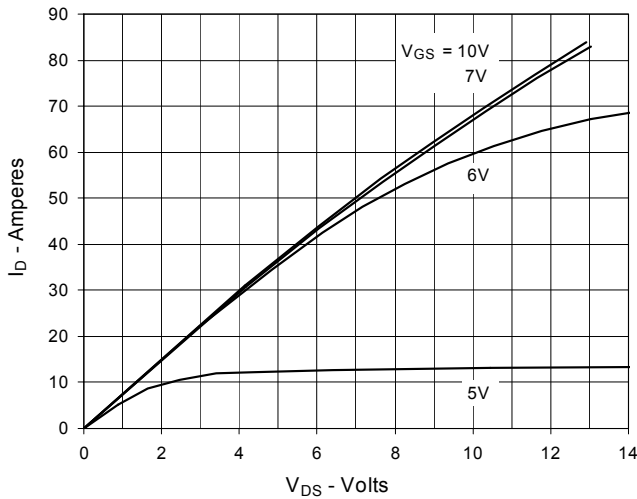
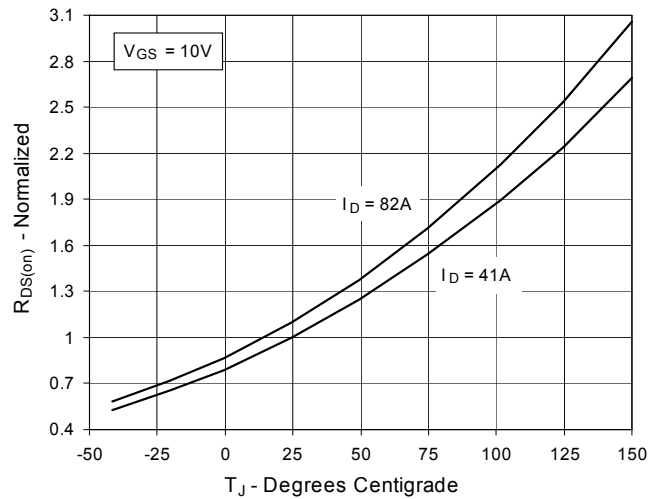
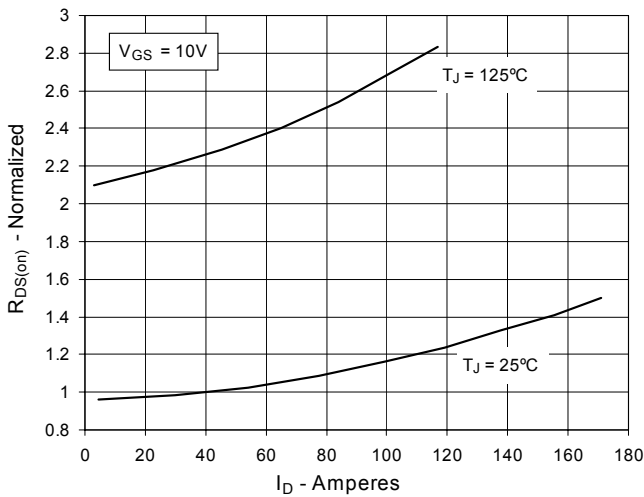
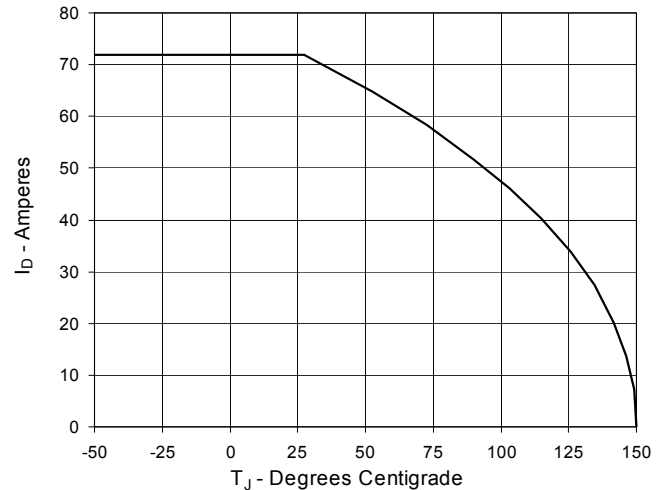


SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	1.240	1.255	31.50	31.88
B	.307	.323	7.80	8.20
C	.161	.169	4.09	4.29
D	.161	.169	4.09	4.29
E	.161	.169	4.09	4.29
F	.587	.595	14.91	15.11
G	1.186	1.193	30.12	30.30
H	1.496	1.505	38.00	38.23
J	.460	.481	11.68	12.22
K	.351	.378	8.92	9.60
L	.030	.033	0.76	0.84
M	.496	.506	12.60	12.85
N	.990	1.001	25.15	25.42
O	.078	.084	1.98	2.13
P	.195	.235	4.95	5.97
Q	1.045	1.059	26.54	26.90
R	.155	.174	3.94	4.42
S	.186	.191	4.72	4.85
T	.968	.987	24.59	25.07
U	-.002	.004	-0.05	0.1

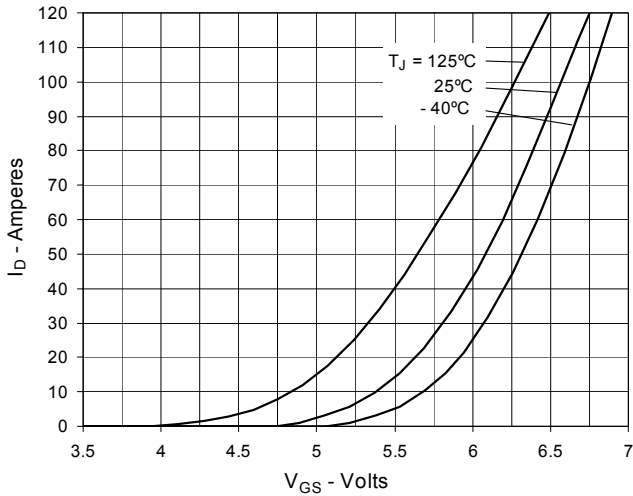
IXYS reserves the right to change limits, test conditions, and dimensions.

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one or more of the following U.S. patents: 4,850,072 5,017,508 5,063,307 5,381,025 6,259,123 B1 6,534,343 6,710,405 B2 6,759,692  
4,881,106 5,034,796 5,187,117 5,486,715 6,306,728 B1 6,583,505 6,710,463 6,771,478 B2

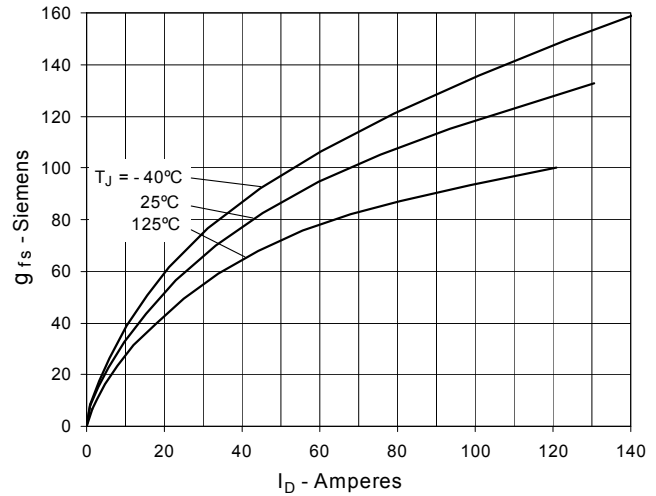
<http://store.iicc.com/>

**Fig. 1. Output Characteristics  
@ 25°C**

**Fig. 2. Extended Output Characteristics  
@ 25°C**

**Fig. 3. Output Characteristics  
@ 125°C**

**Fig. 4.  $R_{DS(on)}$  Normalized to  $I_D = 41A$  Value  
vs. Junction Temperature**

**Fig. 5.  $R_{DS(on)}$  Normalized to  $I_D = 41A$  Value  
vs. Drain Current**

**Fig. 6. Maximum Drain Current vs.  
Case Temperature**


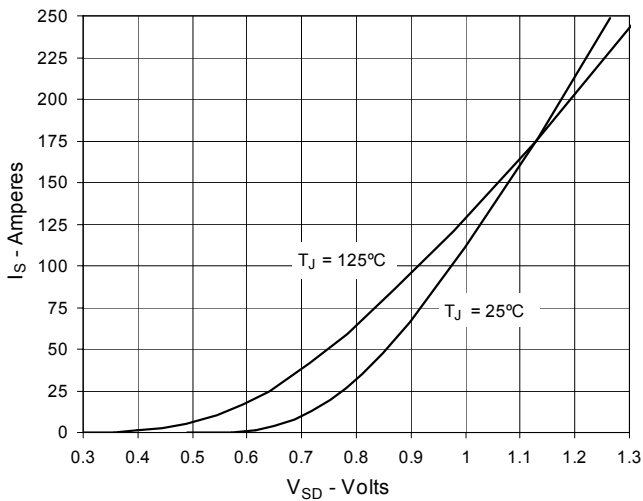
**Fig. 7. Input Admittance**



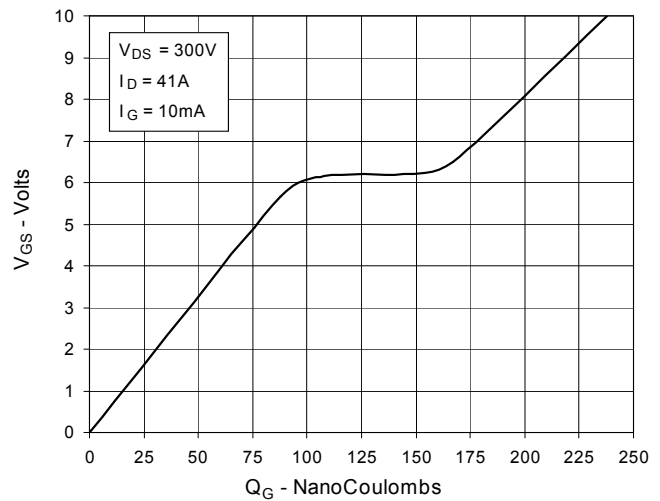
**Fig. 8. Transconductance**



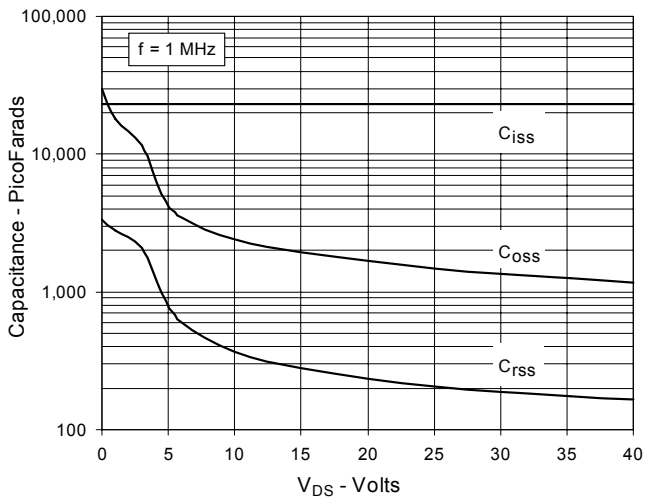
**Fig. 9. Forward Voltage Drop of Intrinsic Diode**



**Fig. 10. Gate Charge**



**Fig. 11. Capacitance**



**Fig. 12. Forward-Bias Safe Operating Area**

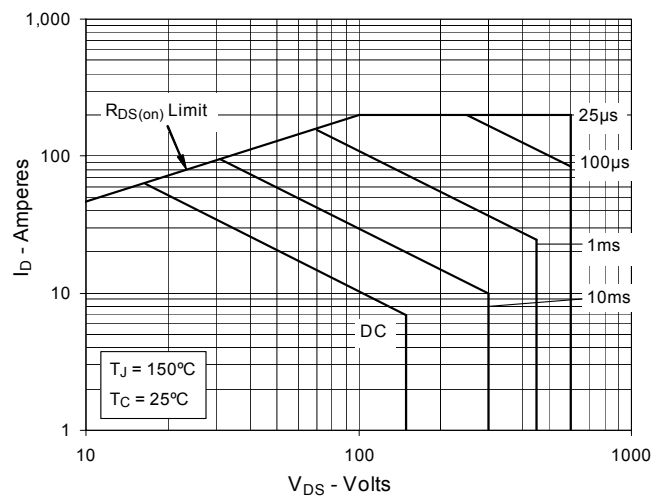


Fig. 13. Maximum Transient Thermal Resistance

