- **5-Ω Switch Connection Between Two Ports**
- **TTL-Compatible Input Levels**

description/ordering information

The SN74CBTS3384 provides ten bits of high-speed TTL-compatible bus switching with Schottky diodes on the I/Os to clamp undershoot. The low on-state resistance of the switch allows connections to be made with minimal propagation delay.

The device is organized as two 5-bit bus switches with separate output-enable ($\overline{OE}$) inputs. When $\overline{OE}$ is low, the switch is on, and port A is connected to port B. When $\overline{OE}$ is high, the switch is open, and the high-impedance state exists between the two ports.

DB, DBQ, DGV, DW, OR PW PACKAGE
(TOP VIEW)

1$\overline{OE}$	1		24	V_{CC}
1B1	2		23	2B5
1A1	3		22	2A5
1A2	4		21	2A4
1B2	5		20	2B4
1B3	6		19	2B3
1A3	7		18	2A3
1A4	8		17	2A2
1B4	9		16	2B2
1B5	10		15	2B1
1A5	11		14	2A1
GND	12		13	2$\overline{OE}$
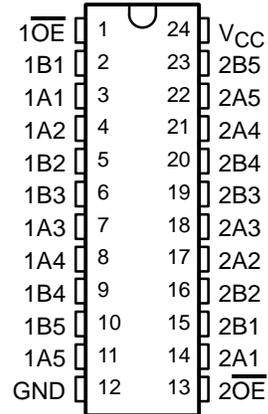
ORDERING INFORMATION

T_A	PACKAGE†		ORDERABLE PART NUMBER	TOP-SIDE MARKING
−40°C to 85°C	SOIC – DW	Tube	SN74CBTS3384DW	CBTS3384
		Tape and reel	SN74CBTS3384DWR	
	SSOP – DB	Tape and reel	SN74CBTS3384DBR	CR384
	SSOP (QSOP) – DBQ	Tape and reel	SN74CBTS3384DBQR	CBTS3384
	TSSOP – PW	Tube	SN74CBTS3384PW	CR384
		Tape and reel	SN74CBTS3384PWR	
	TVSOP – DGV	Tape and reel	SN74CBTS3384DGVR	CR384

† Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

FUNCTION TABLE
(each 5-bit bus switch)

INPUTS		INPUTS/OUTPUTS	
1$\overline{OE}$	2$\overline{OE}$	1B1–1B5	2B1–2B5
L	L	1A1–1A5	2A1–2A5
L	H	1A1–1A5	Z
H	L	Z	2A1–2A5
H	H	Z	Z

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TEXAS INSTRUMENTS

logic diagram (positive logic)
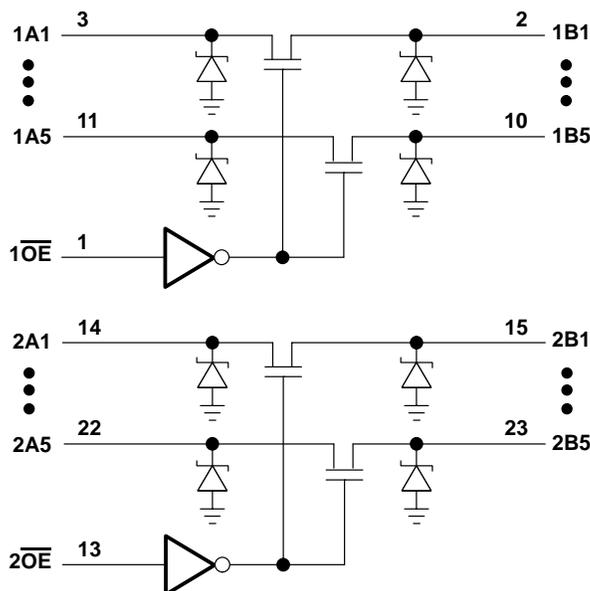
absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, V_{CC} .... −0.5 V to 7 V
Input voltage range, V_I (see Note 1) .... −0.5 V to 7 V
Continuous channel current .... 128 mA
Input clamp current, I_{IK} ($V_{I/O} < 0$) .... −50 mA
Package thermal impedance, θ_{JA} (see Note 2): DB package .... 63°C/W
                                          DBQ package .... 61°C/W
                                          DGV package .... 86°C/W
                                          DW package .... 46°C/W
                                          PW package .... 88°C/W
Storage temperature range, T_{stg} .... −65°C to 150°C

† Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and
  functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not
  implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
NOTES: 1. The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
           2. The package thermal impedance is calculated in accordance with JESD 51-7.

recommended operating conditions (see Note 3)

		MIN	MAX	UNIT
V_{CC}	Supply voltage	4	5.5	V
V_{IH}	High-level control input voltage	2		V
V_{IL}	Low-level control input voltage		0.8	V
T_A	Operating free-air temperature	−40	85	°C

NOTE 3:   All unused control inputs of the device must be held at V_{CC} or GND to ensure proper device operation. Refer to the TI application report,
                Implications of Slow or Floating CMOS Inputs, literature number SCBA004.

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS			MIN	TYP[†]	MAX	UNIT
V_{IK}	A or B inputs	V_{CC} = 4.5 V,	I_I = −18 mA				−0.6	V
	Control inputs						−1.2	
I_I	I_{IL}	V_{CC} = 5.5 V,	V_I = GND				−1	µA
	I_{IH}	V_{CC} = 5.5 V,	V_I = 5.5 V				150	
I_{CC}		V_{CC} = 5.5 V,	I_O = 0,	V_I = V_{CC} or GND			3	µA
ΔI_{CC}[‡]	Control inputs	V_{CC} = 5.5 V,	One input at 3.4 V,	Other inputs at V_{CC} or GND			2.5	mA
C_i	Control inputs	V_I = 3 V or 0				6		pF
$C_{io(OFF)}$		V_O = 3 V or 0,	$\overline{OE}$ = V_{CC}			6.5		pF
r_{on}[§]		V_{CC} = 4 V, TYP at V_{CC} = 4 V	V_I = 2.4 V,	I_I = 15 mA		14	20	Ω
		V_{CC} = 4.5 V	V_I = 0	I_I = 64 mA		5	7	
				I_I = 30 mA		5	7	
			V_I = 2.4 V,	I_I = 15 mA		10	15	

[†] All typical values are at V_{CC} = 5 V (unless otherwise noted), T_A = 25°C.
[‡] This is the increase in supply current for each input that is at the specified TTL voltage level, rather than V_{CC} or GND.
[§] Measured by the voltage drop between the A and B terminals at the indicated current through the switch. On-state resistance is determined by the lowest voltage of the two (A or B) terminals.

switching characteristics over recommended operating free-air temperature range, C_L = 50 pF (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V_{CC} = 4 V		V_{CC} = 5 V ± 0.5 V		UNIT
			MIN	MAX	MIN	MAX	
t_{pd}[¶]	A or B	B or A		0.35		0.25	ns
t_{en}	$\overline{OE}$	A or B		6.2	1.9	5.7	ns
t_{dis}	$\overline{OE}$	A or B		5.5	2.1	5.2	ns

[¶] The propagation delay is the calculated RC time constant of the typical on-state resistance of the switch and the specified load capacitance, when driven by an ideal voltage source (zero output impedance).

TEXAS INSTRUMENTS

PARAMETER MEASUREMENT INFORMATION

TEST	S1
t_{pd}	Open
t_{PLZ}/t_{PZL}	7 V
t_{PHZ}/t_{PZH}	Open

LOAD CIRCUIT

VOLTAGE WAVEFORMS
PROPAGATION DELAY TIMES

VOLTAGE WAVEFORMS
ENABLE AND DISABLE TIMES
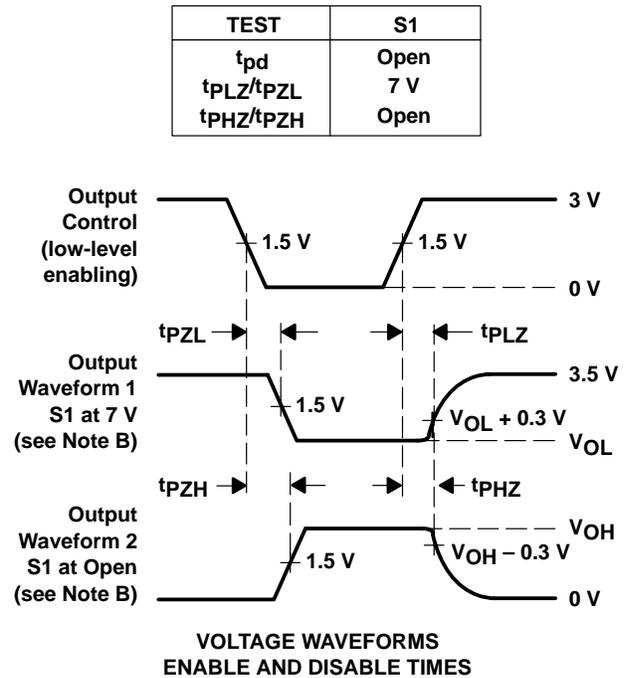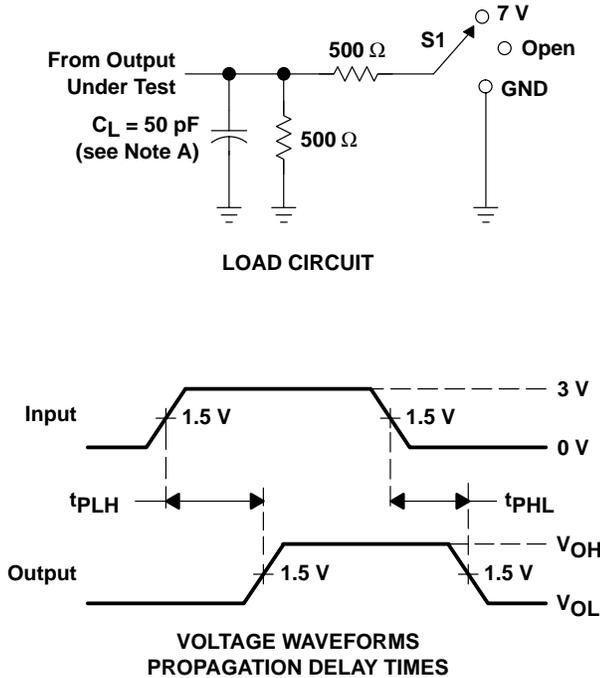
NOTES: A. C_L includes probe and jig capacitance.
          B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control.
              Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
          C. All input pulses are supplied by generators having the following characteristics: PRR ≤ 10 MHz, $Z_O = 50\ \Omega$, $t_r \le 2.5$ ns, $t_f \le 2.5$ ns.
          D. The outputs are measured one at a time with one transition per measurement.
          E. t_{PLZ} and t_{PHZ} are the same as t_{dis}.
          F. t_{PZL} and t_{PZH} are the same as t_{en}.
          G. t_{PLH} and t_{PHL} are the same as t_{pd}.

Figure 1. Load Circuit and Voltage Waveforms

PACKAGING INFORMATION

Orderable Device	Status [1]	Package Type	Package Drawing	Pins	Package Qty	Eco Plan [2]	Lead/Ball Finish	MSL Peak Temp [3]
74CBTS3384DBQRE4	ACTIVE	SSOP/ QSOP	DBQ	24	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
74CBTS3384DGVRE4	ACTIVE	TVSOP	DGV	24	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTS3384DBLE	OBSOLETE	SSOP	DB	24		TBD	Call TI	Call TI
SN74CBTS3384DBQR	ACTIVE	SSOP/ QSOP	DBQ	24	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
SN74CBTS3384DBR	ACTIVE	SSOP	DB	24	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTS3384DBRE4	ACTIVE	SSOP	DB	24	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTS3384DGVR	ACTIVE	TVSOP	DGV	24	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTS3384DW	ACTIVE	SOIC	DW	24	25	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTS3384DWE4	ACTIVE	SOIC	DW	24	25	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTS3384DWR	ACTIVE	SOIC	DW	24	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTS3384DWRE4	ACTIVE	SOIC	DW	24	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTS3384PW	ACTIVE	TSSOP	PW	24	60	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTS3384PWE4	ACTIVE	TSSOP	PW	24	60	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTS3384PWLE	OBSOLETE	TSSOP	PW	24		TBD	Call TI	Call TI
SN74CBTS3384PWR	ACTIVE	TSSOP	PW	24	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74CBTS3384PWRE4	ACTIVE	TSSOP	PW	24	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

[1] The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

[2] Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.
TBD: The Pb-Free/Green conversion plan has not been defined.
Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.
Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

[3] MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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**DGV (R-PDSO-G**)**   **PLASTIC SMALL-OUTLINE**

24 PINS SHOWN

	PINS **	14	16	20	24	38	48	56
DIM								
A MAX		3,70	3,70	5,10	5,10	7,90	9,80	11,40
A MIN		3,50	3,50	4,90	4,90	7,70	9,60	11,20
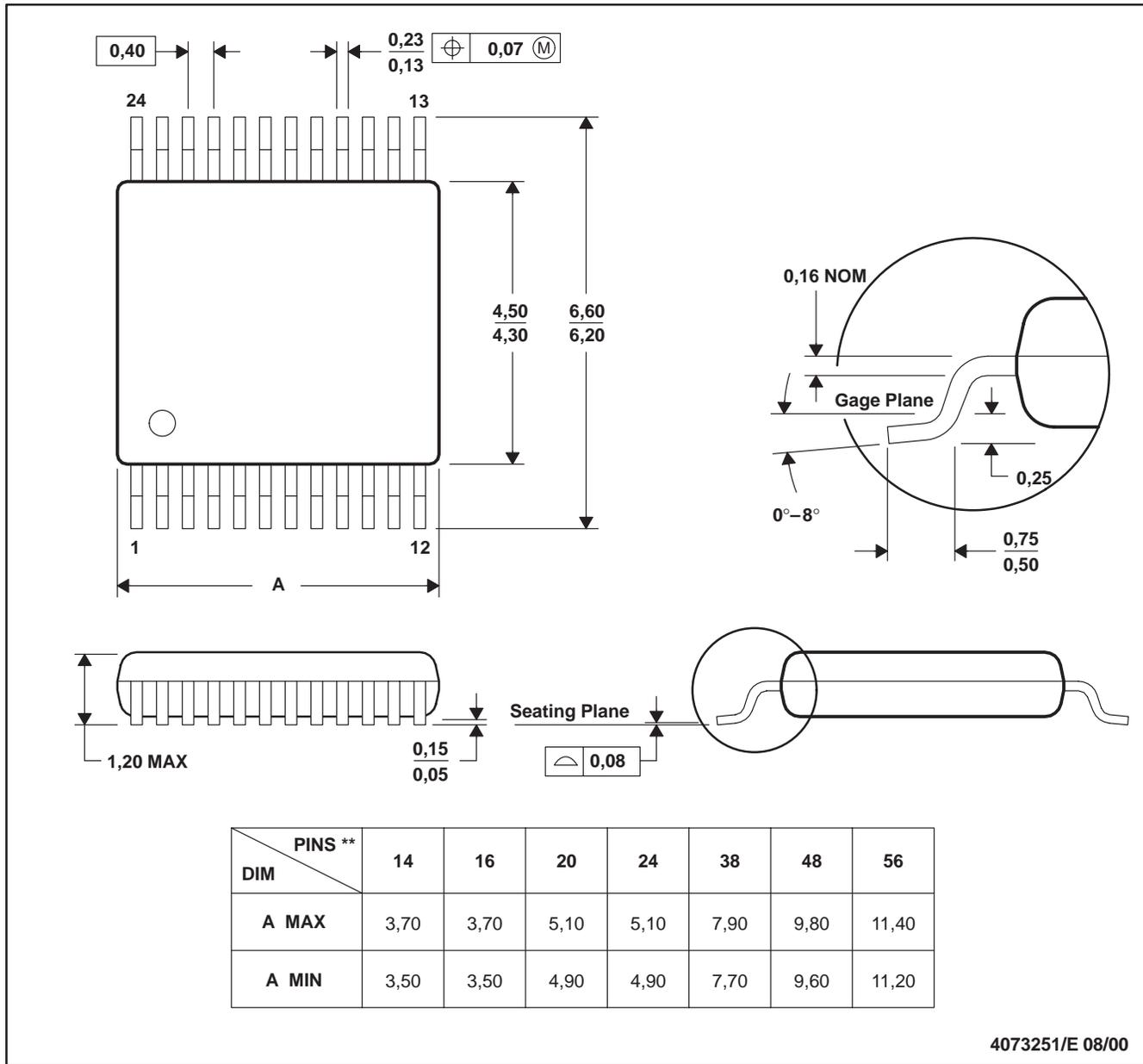
4073251/E 08/00

NOTES: A. All linear dimensions are in millimeters.
            B. This drawing is subject to change without notice.
            C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15 per side.
            D. Falls within JEDEC: 24/48 Pins – MO-153
                                  14/16/20/56 Pins – MO-194

TEXAS INSTRUMENTS

POST OFFICE BOX 655303 ● DALLAS, TEXAS 75265

DW (R−PDSO−G24) PLASTIC SMALL−OUTLINE PACKAGE

0.614 (15,60)
0.598 (15,20)

24   13

0.419 (10,63)
0.393 (9,97)

0.299 (7,60)
0.291 (7,40)

1   12

Pin 1
Index Area

0.050 (1,27)

0.020 (0,51)
0.012 (0,31)

⌖ 0.010 (0,25) Ⓜ

0.104 (2,65) Max

0.012 (0,30)
0.004 (0,10)

0.013 (0,33)
0.008 (0,20)

Gauge Plane

0.004 (0,10)

Seating Plane

0.010 (0,25)   0°−8°

0.050 (1,27)
0.016 (0,40)

4040000−5/F 06/2004

NOTES: A. All linear dimensions are in inches (millimeters).
        B. This drawing is subject to change without notice.
        C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
        D. Falls within JEDEC MS−013 variation AD.

DBQ (R−PDSO−G24)　　　　　　PLASTIC SMALL−OUTLINE PACKAGE

0.344 (8,75)
0.337 (8,55)

24

13

0.244 (6,20)
0.228 (5,80)

0.157 (4,00)
0.150 (3,80)

1

12

Pin 1
Index Area

0.025 (0,635)

0.012 (0,30)
0.008 (0,20)

⊕ 0.005 (0,13) Ⓜ

0.069 (1,75) Max

0.010 (0,25)
0.004 (0,10)

0.010 (0,25)
0.006 (0,15)

Gauge Plane

0.004 (0,10)

Seating Plane

0.010 (0,25)

0°−8°

0.035 (0,89)
0.016 (0,40)

4073301−4/G　02/2005

NOTES:　A.　All linear dimensions are in inches (millimeters).
　　　　　B.　This drawing is subject to change without notice.
　　　　　C.　Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15) per side.
　　　　　D.　Falls within JEDEC MO−137 variation AE.

TEXAS
INSTRUMENTS
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DB (R-PDSO-G**)   **PLASTIC SMALL-OUTLINE**

28 PINS SHOWN
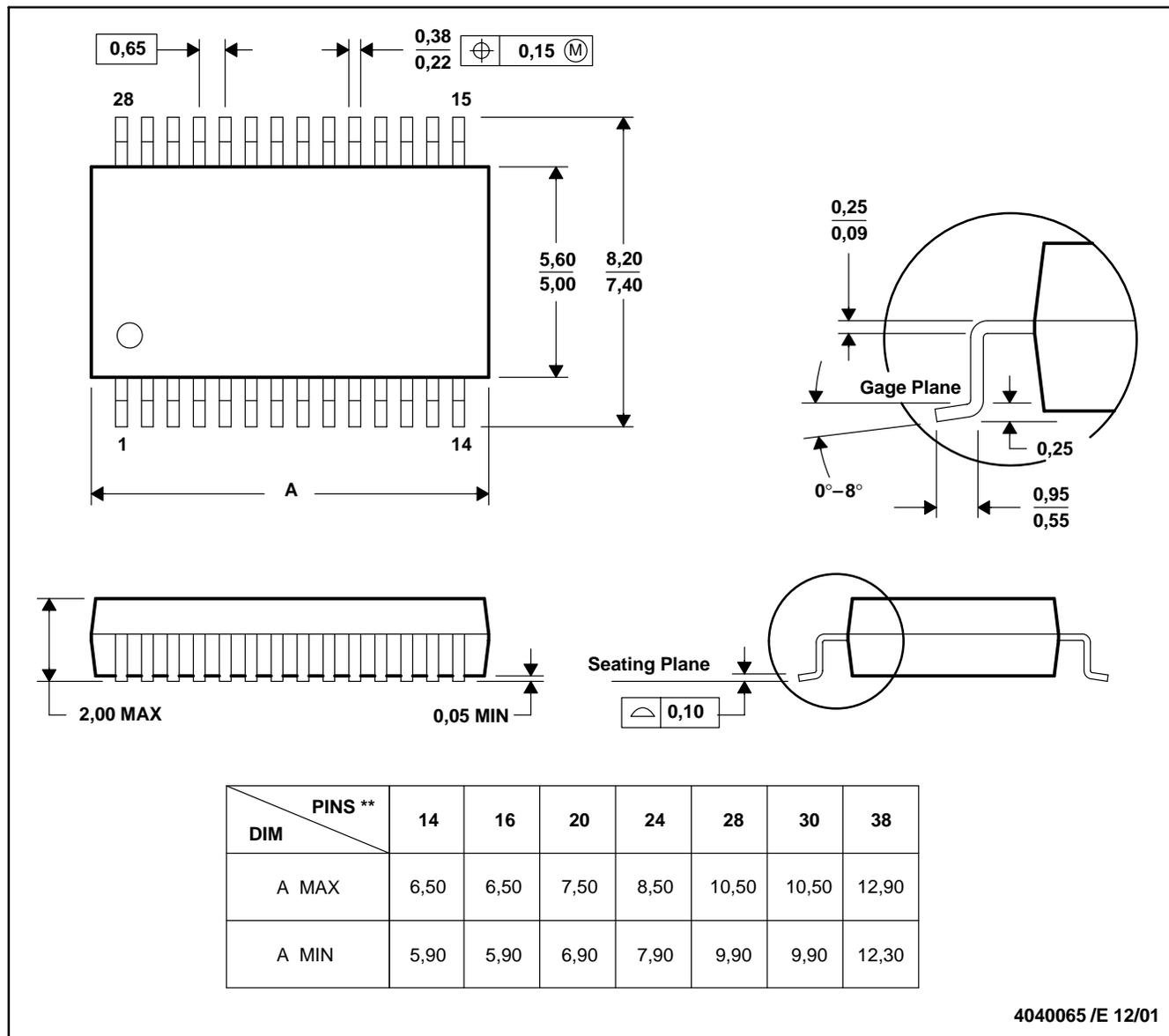
PINS ** DIM	14	16	20	24	28	30	38
A MAX	6,50	6,50	7,50	8,50	10,50	10,50	12,90
A MIN	5,90	5,90	6,90	7,90	9,90	9,90	12,30

4040065 /E 12/01

NOTES: A. All linear dimensions are in millimeters.
           B. This drawing is subject to change without notice.
           C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
           D. Falls within JEDEC MO-150

TEXAS INSTRUMENTS

POST OFFICE BOX 655303 ● DALLAS, TEXAS 75265

PW (R-PDSO-G**)   **PLASTIC SMALL-OUTLINE PACKAGE**

14 PINS SHOWN

PINS ** DIM	8	14	16	20	24	28
A MAX	3,10	5,10	5,10	6,60	7,90	9,80
A MIN	2,90	4,90	4,90	6,40	7,70	9,60
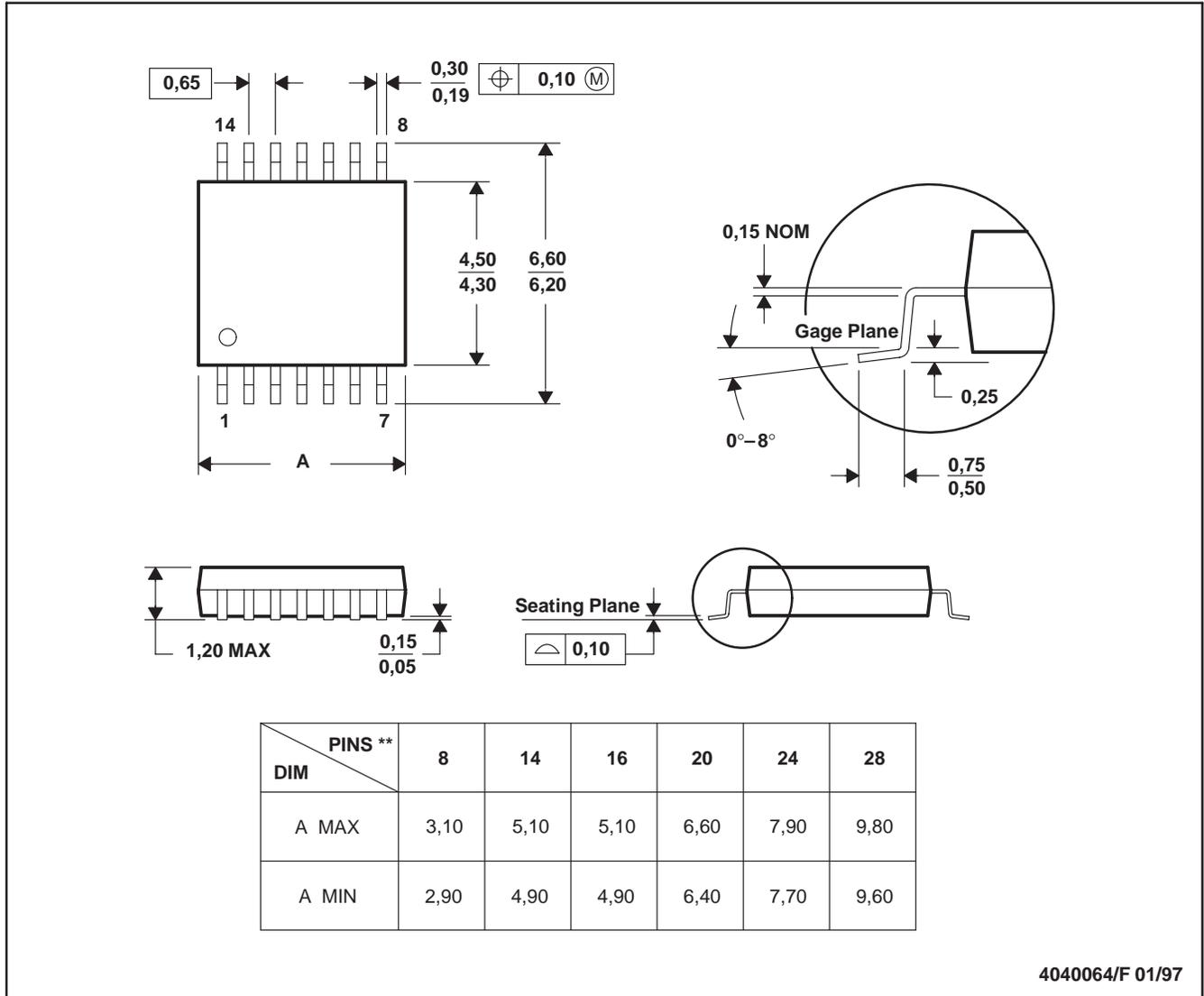
4040064/F 01/97

NOTES: A. All linear dimensions are in millimeters.
           B. This drawing is subject to change without notice.
           C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
           D. Falls within JEDEC MO-153

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View ROHS Compliant Devices

View RoHS Compliant Devices

clear gif

SN74CBTS3384, Status: **ACTIVE**

10-Bit FET Bus Switch With Schottky Diode Clamping

clear gif

Features	Samples	Technical Documents
Quality & Pb-Free Data	Pricing/Packaging	Applications Notes
Related Products	Inventory	Simulation Models
Tools & Software	Symbols/Footprints	Reference Designs

Refine Your Selection
- Logic: Digital Bus Switc

Support
- KnowledgeBase
- Contact Technical Supp
- TI Cross Reference
- Training
- Part Marking Lookup
- Part Number Nomencla

Datasheet

Download Datasheet **SN74CBTS3384 (Rev. M)** (sn74cbts3384.pdf, 304 KB)
09 Jul 2003 Download

	SN74CBTS3384
Voltage Nodes(V)	5
Vcc range(V)	4.0 to 5.5
No. of Bits	10
Input Level	TTL
ron(max)(ohms)	7
Static Current	0.003
tpd max(ns)	0.25
	Samples
	Inventory

Product Information

Features   Save this to your personal library

    5-   Switch Connection Between Two Ports
    TTL-Compatible Input Levels

Description

The SN74CBTS3384 provides ten bits of high-speed TTL-compatible bus switching with Schottky diodes on the I/Os to clamp undershoot. The low on-state resistance of the switch allows connections to be made with minimal propagation delay.

The device is organized as two 5-bit bus switches with separate output-enable (OE\) inputs. When OE\ is low, the switch is on, and port A is connected to port B. When OE\ is high, the switch is open, and the high-impedance state exists between the two ports.

Pricing/Packaging/CAD Design Tools/Samples

			Price	Packaging			CAD Design Tools	Samples
Device	Status	Temp (ºC)	Budget Price ($US) | QTY	Industry Standard (TI Pkg) | Pins	Top Side Marking	Standard Pack Quantity	Footprints	Samples
74CBTS3384DBQRE4	ACTIVE	-40 to 85	0.51 | 1KU	SSOP/QSOP (DBQ) | 24	View	2500	☐	Purchase Samples
74CBTS3384DGVRE4	ACTIVE	-40 to 85	0.51 | 1KU	TVSOP (DGV) | 24		2000	☐	Purchase Samples
SN74CBTS3384DBLE	OBSOLETE	-40 to 85		SSOP (DB) | 24	View		☐	Not Available
SN74CBTS3384DBQR	ACTIVE	-40 to 85	0.51 | 1KU	SSOP/QSOP (DBQ) | 24	View	2500	☐	Contact TI Distributor or Sales Office
SN74CBTS3384DBR	ACTIVE	-40 to 85	0.51 | 1KU	SSOP (DB) | 24	View	2000	☐	Contact TI Distributor or Sales Office
SN74CBTS3384DBRE4	ACTIVE	-40 to 85	0.51 | 1KU	SSOP (DB) | 24	View	2000	☐	Purchase Samples
SN74CBTS3384DGVR	ACTIVE	-40 to 85	0.51 | 1KU	TVSOP (DGV) | 24		2000	☐	Purchase Samples
SN74CBTS3384DW	ACTIVE	-40 to 85	0.51 | 1KU	SOIC (DW) | 24	View	25	☐	Contact TI Distributor or Sales Office
SN74CBTS3384DWE4	ACTIVE	-40 to 85	0.51 | 1KU	SOIC (DW) | 24	View	25	☐	Request Free Samples
SN74CBTS3384DWR	ACTIVE	-40 to 85	0.51 | 1KU	SOIC (DW) | 24	View	2000	☐	Purchase Samples
SN74CBTS3384DWRE4	ACTIVE	-40 to 85	0.51 | 1KU	SOIC (DW) | 24	View	2000	☐	Purchase Samples
SN74CBTS3384PW	ACTIVE	-40 to 85	0.51 | 1KU	TSSOP (PW) | 24	View	60	☐	Purchase Samples
SN74CBTS3384PWE4	ACTIVE	-40 to 85	0.51 | 1KU	TSSOP (PW) | 24	View	60	☐	Purchase Samples
SN74CBTS3384PWLE	OBSOLETE	-40 to 85		TSSOP (PW) | 24	View		☐	Not Available
SN74CBTS3384PWR	ACTIVE	-40 to 85	0.51 | 1KU	TSSOP (PW) | 24	View	2000	☐	Contact TI Distributor or Sales Office
SN74CBTS3384PWRE4	ACTIVE	-40 to 85	0.51 | 1KU	TSSOP (PW) | 24	View	2000	☐	Request Free Samples

Inventory

View all Distributors

Choose a Region

	TI Inventory Status			Reported Distributor Inventory			
74CBTS3384DBQRE4	As of 9:08 AM GMT, 25 Nov 2005			As of 9:08 AM GMT, 25 Nov 2005			
	In Stock	In Progress QTY | Date	Lead Time	Region	Company	In Stock	Purchase
	1117*	10k | 8 Dec	6 Weeks	None Reported View Distributors			
74CBTS3384DGVRE4	As of 9:08 AM GMT, 25 Nov 2005			As of 9:08 AM GMT, 25 Nov 2005			
	In Stock	In Progress QTY | Date	Lead Time	Region	Company	In Stock	Purchase
	0*	1026 | 20 Dec	10 Weeks	None Reported View Distributors			
SN74CBTS3384DBQR	As of 9:08 AM GMT, 25 Nov 2005			As of 9:08 AM GMT, 25 Nov 2005			
	In Stock	In Progress QTY | Date	Lead Time	Region	Company	In Stock	Purchase
	1117*	10k | 8 Dec	6 Weeks	Americas	DigiKey	>1k	
SN74CBTS3384DBR	As of 9:08 AM GMT, 25 Nov 2005			As of 9:08 AM GMT, 25 Nov 2005			
	In Stock	In Progress QTY | Date	Lead Time	Region	Company	In Stock	Purchase

	In Stock	In Progress QTY | Date	Lead Time	Region	Company	In Stock	Purchase
	0*	1117 | 20 Dec	10 Weeks	Americas	DigiKey	>1k	
		>10k | 14 Feb					

SN74CBTS3384DBRE4 — As of 9:08 AM GMT, 25 Nov 2005 / As of 9:08 AM GMT, 25 Nov 2005

	In Stock	In Progress QTY | Date	Lead Time	Region	Company	In Stock	Purchase
	0*	1117 | 20 Dec	10 Weeks	None Reported View Distributors			
		>10k | 14 Feb					

SN74CBTS3384DGVR — As of 9:08 AM GMT, 25 Nov 2005 / As of 9:08 AM GMT, 25 Nov 2005

	In Stock	In Progress QTY | Date	Lead Time	Region	Company	In Stock	Purchase
	0*	1026 | 20 Dec	10 Weeks	None Reported View Distributors			

SN74CBTS3384DW — As of 9:08 AM GMT, 25 Nov 2005 / As of 9:08 AM GMT, 25 Nov 2005

	In Stock	In Progress QTY | Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k | 8 Dec	6 Weeks	Americas	DigiKey	92	
				Europe	Spoerle	149	

SN74CBTS3384DWE4 — As of 9:08 AM GMT, 25 Nov 2005 / As of 9:08 AM GMT, 25 Nov 2005

	In Stock	In Progress QTY | Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k | 8 Dec	6 Weeks	None Reported View Distributors			

SN74CBTS3384DWR — As of 9:08 AM GMT, 25 Nov 2005 / As of 9:08 AM GMT, 25 Nov 2005

	In Stock	In Progress QTY | Date	Lead Time	Region	Company	In Stock	Purchase
	2000*	330 | 25 Nov	6 Weeks	None Reported View Distributors			
		>10k | 19 Dec					

SN74CBTS3384DWRE4 — As of 9:08 AM GMT, 25 Nov 2005 / As of 9:08 AM GMT, 25 Nov 2005

	In Stock	In Progress QTY | Date	Lead Time	Region	Company	In Stock	Purchase
	2000*	330 | 25 Nov	6 Weeks	None Reported View Distributors			
		>10k | 19 Dec					

SN74CBTS3384PW — As of 9:08 AM GMT, 25 Nov 2005 / As of 9:08 AM GMT, 25 Nov 2005

	In Stock	In Progress QTY | Date	Lead Time	Region	Company	In Stock	Purchase
	0*	2903 | 30 Nov	10 Weeks	Americas	Avnet	898	
		1085 | 9 Dec					
		>10k | 10 Feb					

SN74CBTS3384PWE4 — As of 9:08 AM GMT, 25 Nov 2005 / As of 9:08 AM GMT, 25 Nov 2005

	In Stock	In Progress QTY | Date	Lead Time	Region	Company	In Stock	Purchase
	0*	2903 | 30 Nov	10 Weeks	None Reported View Distributors			
		1085 | 9 Dec					
		>10k | 10 Feb					

SN74CBTS3384PWR — As of 9:08 AM GMT, 25 Nov 2005 / As of 9:08 AM GMT, 25 Nov 2005

	In Stock	In Progress QTY | Date	Lead Time	Region	Company	In Stock	Purchase
	0*	1102 | 12 Dec	12 Weeks	Americas	DigiKey	740	
		>10k | 13 Feb					

SN74CBTS3384PWRE4	As of 9:08 AM GMT, 25 Nov 2005			As of 9:08 AM GMT, 25 Nov 2005			
	In Stock	**In Progress QTY | Date**	**Lead Time**	**Region**	**Company**	**In Stock**	**Purchase**
	0*	1102 | 12 Dec	12 Weeks	None Reported View Distributors			
		>10k | 13 Feb					

* Our information is updated daily, so please check back with us soon if this does not meet your needs. You may also contact your TI Authorized Distributor, including those listed above, for real time stock information.

** Lead time information is not available at this time. However, our information is updated daily so please check back with us soon. Please contact your preferred TI Authorized Distributor for additional information.

Quality & Lead (Pb)-Free Data

Device	Product Content			MTBF/FIT Rate
	Eco Plan*	Lead/Ball Finish	MSL Rating/Peak Reflow	Details	Details
74CBTS3384DBQRE4	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR	View	View
74CBTS3384DGVRE4	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74CBTS3384DBQR	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR	View	View
SN74CBTS3384DBR	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74CBTS3384DBRE4	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74CBTS3384DGVR	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74CBTS3384DW	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74CBTS3384DWE4	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74CBTS3384DWR	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74CBTS3384DWRE4	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74CBTS3384PW	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74CBTS3384PWE4	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74CBTS3384PWR	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View
SN74CBTS3384PWRE4	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View

* The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please click on the Product Content Details "View" link in the table above for the latest availability information and additional product content details.

If the information you are requesting is not available online at this time, contact one of our Product Information Centers regarding the availability of this information.

Technical Documents

Datasheets
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SN74CBTS3384 (Rev. M) (sn74cbts3384.pdf, 304 KB)

09 Jul 2003 Download

Application Notes

Semiconductor Packing Material Electrostatic Discharge (ESD) Protection (szza047.htm, 9 KB)

08 Jul 2004 Abstract

Selecting the Right Level Translation Solution (Rev. A) (scea035a.htm, 9 KB)

22 Jun 2004 Abstract

Shelf-Life Evaluation of Lead-Free Component Finishes (szza046.htm, 9 KB)

24 May 2004 Abstract

Understanding and Interpreting Standard-Logic Data Sheets (Rev. B) (szza036b.htm, 8 KB)

28 May 2003 Abstract

Bus FET Switch Solutions for Live Insertion Applications (scda009.htm, 9 KB)

07 Feb 2003 Abstract

Texas Instruments Little Logic Application Report (scea029.htm, 9 KB)

01 Nov 2002 Abstract

TI IBIS File Creation, Validation, and Distribution Processes (szza034.htm, 9 KB)

29 Aug 2002 Abstract

16-Bit Widebus Logic Families in 56-Ball, 0.65-mm Pitch Very Thin Fine-Pitch BGA (Rev. B) (szza029b.htm, 9 KB)

22 May 2002 Abstract

Selecting the Right Texas Instruments Signal Switch (szza030.htm, 9 KB)

07 Sep 2001 Abstract

Flexible Voltage-Level Translation With CBT Family Devices (scda006.htm, 9 KB)

20 Jul 1999 Abstract

3.3-V to 2.5-V Translation with Texas Instruments Crossbar Technology (Rev. A) (scda004a.htm, 8 KB)

03 Apr 1998 Abstract

Implications of Slow or Floating CMOS Inputs (Rev. C) (scba004c.htm, 9 KB)

01 Feb 1998 Abstract

5-V To 3.3-V Translation With the SN74CBTD3384 (Rev. B) (scda003b.htm, 9 KB)

01 Mar 1997 Abstract

SN74CBTS3384 Bus Switches Provide Fast Connection and Ensure Isolation (Rev. A) (scda002a.htm, 9 KB)

01 Aug 1996 Abstract

Understanding Advanced Bus-Interface Products Design Guide (scaa029.pdf, 253 KB)

01 May 1996 Download

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User Guides

Signal Switch Data Book (Rev. A) (scdd003a.pdf, 19732 KB)

14 Nov 2003 Download

LOGIC Pocket Data Book (scyd013.pdf, 4835 KB)

05 Dec 2002 Download

CBT (5-V) And CBTLV (3.3-V) Bus Switches Data Book (Rev. B) (scdd001b.pdf, 5433 KB)

01 Dec 1998 Download

More Literature

Logic Selection Guide 2005 (Rev. X) (sdyu001x.pdf, 6909 KB)

15 Mar 2005 Download

Digital Bus Switch Selection Guide (Rev. A) (scdb006a.pdf, 719 KB)

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Design Summary for WCSP Little Logic (Rev. B) (scet007b.pdf, 295 KB)

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Military Semiconductors Selection Guide 2004-2005 (Rev. D) (sgyc003d.pdf, 964 KB)

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Logic Cross-Reference (Rev. A) (scyb017a.pdf, 2938 KB)

07 Oct 2003 Download

CBT RAID Application Clip (scyb015.pdf, 180 KB)

12 Jun 2003 Download

Standard Linear & Logic for PCs, Servers & Motherboards (scyb005.pdf, 3997 KB)

13 Jun 2002 Download

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