

ACTIVE DELAY LINES, 5-TAP & 10-TAP THROUGH-HOLE & SURFACE MOUNT

A08 SERIES: 8-Pin DIP
A14 SERIES: 14-Pin DIP
SA08 SERIES: 8-Pin SIP
SMA14 SERIES: 14-Pin SO



Term.W is RoHS compliant & 260°C compatible



- Economical cost
- A1405 popular values from stock!
- Wide selection, 20 - 1000nS
- Choice of 5 or 10 equally spaced taps
- TTL Schottky interfaced, TTL&DTL compatible

RCD's active delay lines have been designed to provide precise tap delays with all the necessary drive and pick-off circuitry. All inputs/outputs are schottky-type, requiring no additional components to achieve specified delays. Units are 100% inspected. Excellent for applications requiring high delay stability, fast rise times and no jitter, such as memory boards, disk drives, and signal processing. Application Guide available.

OPTIONS

- Opt.T= trailing edge design
- Opt.F =fast TTL, H=HCMOS, C=FACT
- Opt.A = auto-insertable design
- Opt.ER = -55 to +125°C operating temp.
- Opt.39 = -40 to +85°C operating temp.
- Tighter tolerances, faster rise times
- Military screening

TOTAL-DELAY TIMES (T_D)

20nS, **25nS**, 30nS, 40nS, **45nS**, **50nS**, **60nS**, **75nS**, **100nS**, **125nS**, **150nS**, **200nS**, **250nS**, **300nS**, 350nS, 400nS, 450nS, **500nS**, 750nS, **1000nS**
 (popular values listed in bold. Intermediate and extended range values available on special order).

SPECIFICATIONS

Operating Temp: 0 to 70°C (opt.39= -40 to +85°C, opt.ER= -55 to +125°C)
 Rise Time: 4nS max.
 Delay Tol: ±2nS or ±5%, whichever greater
 Tap Tol: ±2nS or 5%, whichever greater
 Peak Soldering Temp: +230°C

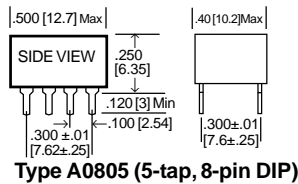
CHARACTERISTICS

RCD Type	Delay Range	No. of Taps	Delay per Tap
A0805	20nS - 500nS	5	20% TD
A0805AG	20nS - 500nS	5	20% TD
A1405	20nS -1000nS	5	20% TD
A1405AG	20nS -1000nS	5	20% TD
A1410	50nS -1000nS	10	10% TD
A1410AG	50nS -1000nS	10	10% TD
SA0805	20nS - 500nS	5	20% TD
SMA1405	20nS - 250nS	5	20% TD

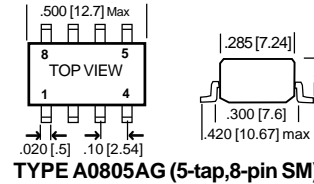
Most popular models are listed in boldface. A1405AG is most popular SM model, A1405 is most popular thru-hole.

TEST CONDITIONS @25°C

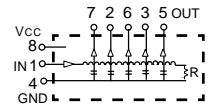
- 1.) Input test pulse voltage: 3.2V
- 2.) Input pulse width: 50nS or 1.2x the total delay (whichever is greater)
- 3.) Input rise time: 2.0nS (0.75V to 2.4V)
- 4.) Delay measured at 1.5V on leading edge only with no loads on output (specify opt. T for trailing edge design)
- 5.) Supply Voltage (Vcc): 5V
- 6.) Pulse spacing: 2x pulse width min.



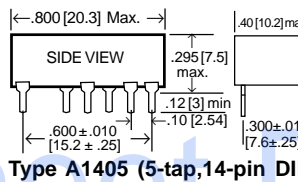
Type A0805 (5-tap, 8-pin DIP)



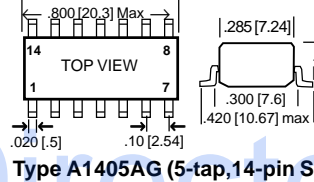
TYPE A0805AG (5-tap, 8-pin SM)



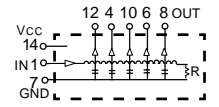
A0805 Schematic



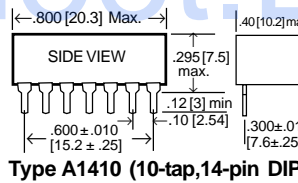
Type A1405 (5-tap, 14-pin DIP)



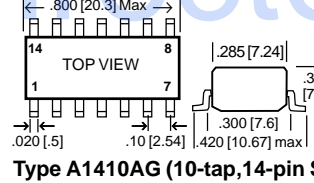
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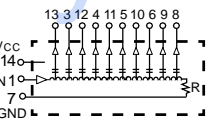
A1405 Schematic



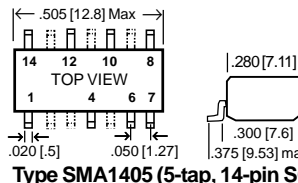
Type A1410 (10-tap, 14-pin DIP)



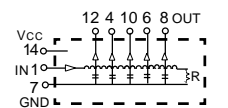
Type A1410AG (10-tap, 14-pin SM)



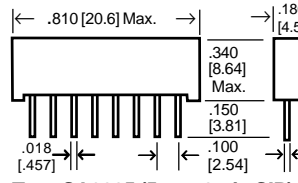
A1410 Schematic



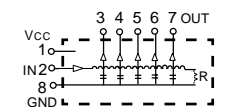
Type SMA1405 (5-tap, 14-pin SO)



SMA1405 Schematic



Type SA0805 (5-tap, 8-pin SIP)



SA0805 Schematic

P/N DESIGNATION:

Type: A0805, A0805AG, A1405, A1405AG, A1410, A1410AG, SMA1405, SA0805

Options: T, H, F, ER, C, A, 39 (leave blank if std.)

Delay Time: 20NS, 25NS, 100NS, etc.

Packaging: B=Bulk (magazine tube std), T=Tape & Reel (SM1405 only)

Termination: W=Lead-free, Q=Tin/Lead (leave blank if either is acceptable)

A0805 - 100NS - B W