

# 933.685

### MAXIMUM RATINGS

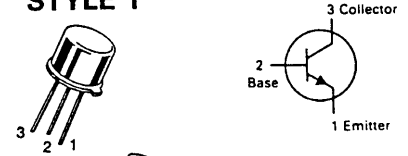
Rating	Symbol	2N2219 2N2222	2N2218A 2N2219A 2N2222A	Unit
Collector-Emitter Voltage	V <sub>CEO</sub>	30	40	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	60	75	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	5.0	6.0	Vdc
Collector Current — Continuous	I <sub>C</sub>	800	800	mAdc
		2N2218A 2N2219,A	2N2222,A	
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	0.8 4.57	0.4 2.28	Watt mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	3.0 17.1	1.2 6.85	Watts mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +200		°C

### THERMAL CHARACTERISTICS

Characteristic	Symbol	2N2218A 2N2219,A	2N2222,A	Unit
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	219	437.5	°C/W
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	58	145.8	°C/W

## 2N2218A, 2N2219,A★ 2N2222,A★

2N2218, A/2N2219,A  
CASE 79-04  
TO-39 (TO-205AD)  
STYLE 1



A/2N2222,A  
CASE 22-03  
TO-18 (TO-206AA)  
STYLE 1

**GENERAL PURPOSE  
TRANSISTORS**  
NPN SILICON

★2N2219A and 2N2222A  
are Motorola designated  
preferred devices.

### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>				
Collector-Emitter Breakdown Voltage (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	30 40	—	Vdc
				Non-A Suffix A-Suffix
Collector-Base Breakdown Voltage (I <sub>C</sub> = 10 μAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	60 75	—	Vdc
				Non-A Suffix A-Suffix
Emitter-Base Breakdown Voltage (I <sub>E</sub> = 10 μAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	5.0 6.0	—	Vdc
				Non-A Suffix A-Suffix
Collector Cutoff Current (V <sub>CE</sub> = 60 Vdc, V <sub>EB(off)</sub> = 3.0 Vdc)	I <sub>CEX</sub>	—	10	nAdc
				A-Suffix
Collector Cutoff Current (V <sub>CB</sub> = 50 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	—	0.01	μAdc
				Non-A Suffix
(V <sub>CB</sub> = 60 Vdc, I <sub>E</sub> = 0)			0.01	
				A-Suffix
(V <sub>CB</sub> = 50 Vdc, I <sub>E</sub> = 0, T <sub>A</sub> = 150°C)			10	
				Non-A Suffix
(V <sub>CB</sub> = 60 Vdc, I <sub>E</sub> = 0, T <sub>A</sub> = 150°C)			10	
				A-Suffix
Emitter Cutoff Current (V <sub>EB</sub> = 3.0 Vdc, I <sub>C</sub> = 0)	I <sub>EBO</sub>	—	10	nAdc
				A-Suffix
Base Cutoff Current (V <sub>CE</sub> = 60 Vdc, V <sub>EB(off)</sub> = 3.0 Vdc)	I <sub>BL</sub>	—	20	nAdc
				A-Suffix
<b>ON CHARACTERISTICS</b>				
DC Current Gain (I <sub>C</sub> = 0.1 mAdc, V <sub>CE</sub> = 10 Vdc)	h <sub>FE</sub>	20 35	—	—
				2N2218A 2N2219,A, 2N2222,A
(I <sub>C</sub> = 1.0 mAdc, V <sub>CE</sub> = 10 Vdc)		25 50	—	—
				2N2218A 2N2219,A, 2N2222,A
(I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 10 Vdc)(1)		35 75	—	—
				2N2218A 2N2219,A, 2N2222,A
(I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 10 Vdc, T <sub>A</sub> = -55°C)(1)		15 35	—	—
				2N2218A 2N2219,A, 2N2222,A
(I <sub>C</sub> = 150 mAdc, V <sub>CE</sub> = 10 Vdc)(1)		40 100	120 300	—
				2N2218A 2N2219,A, 2N2222,A

**2N2218A 2N2219,A 2N2222,A**
**ELECTRICAL CHARACTERISTICS** (continued) ( $T_A = 25^\circ\text{C}$  unless otherwise noted.)

Characteristic	Symbol	Min	Max	Unit
( $I_C = 150 \text{ mAdc}$ , $V_{CE} = 1.0 \text{ Vdc}$ )(1)	2N2218A 2N2219,A, 2N2222,A	20 50	— —	
( $I_C = 500 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ )(1)	2N2219, 2N2222 2N2218A 2N2219A, 2N2222A	30 25 40	— — —	
Collector-Emitter Saturation Voltage(1) ( $I_C = 150 \text{ mAdc}$ , $I_B = 15 \text{ mAdc}$ )	Non-A Suffix A-Suffix	— —	0.4 0.3	Vdc
( $I_C = 500 \text{ mAdc}$ , $I_B = 50 \text{ mAdc}$ )	Non-A Suffix A-Suffix	— —	1.6 1.0	
Base-Emitter Saturation Voltage(1) ( $I_C = 150 \text{ mAdc}$ , $I_B = 15 \text{ mAdc}$ )	Non-A Suffix A-Suffix	0.6 0.6	1.3 1.2	Vdc
( $I_C = 500 \text{ mAdc}$ , $I_B = 50 \text{ mAdc}$ )	Non-A Suffix A-Suffix	— —	2.6 2.0	

**SMALL-SIGNAL CHARACTERISTICS**

Current Gain — Bandwidth Product(2) ( $I_C = 20 \text{ mAdc}$ , $V_{CE} = 20 \text{ Vdc}$ , $f = 100 \text{ MHz}$ )	All Types, Except 2N2219A, 2N2222A	$f_T$	250 300	— —	MHz
Output Capacitance(3) ( $V_{CB} = 10 \text{ Vdc}$ , $I_E = 0$ , $f = 1.0 \text{ MHz}$ )		$C_{obo}$	—	8.0	pF
Input Capacitance(3) ( $V_{EB} = 0.5 \text{ Vdc}$ , $I_C = 0$ , $f = 1.0 \text{ MHz}$ )	Non-A Suffix A-Suffix	$C_{ibo}$	— —	30 25	pF
Input Impedance ( $I_C = 1.0 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ , $f = 1.0 \text{ kHz}$ )	2N2218A 2N2219A, 2N2222A	$h_{je}$	1.0 2.0	3.5 8.0	kohms
( $I_C = 10 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ , $f = 1.0 \text{ kHz}$ )	2N2218A 2N2219A, 2N2222A		0.2 0.25	1.0 1.25	
Voltage Feedback Ratio ( $I_C = 1.0 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ , $f = 1.0 \text{ kHz}$ )	2N2218A 2N2219A, 2N2222A	$h_{re}$	— —	5.0 8.0	$\times 10^{-4}$
( $I_C = 10 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ , $f = 1.0 \text{ kHz}$ )	2N2218A 2N2219A, 2N2222A		— —	2.5 4.0	
Small-Signal Current Gain ( $I_C = 1.0 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ , $f = 1.0 \text{ kHz}$ )	2N2218A 2N2219A, 2N2222A	$h_{fe}$	30 50	150 300	—
( $I_C = 10 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ , $f = 1.0 \text{ kHz}$ )	2N2218A 2N2219A, 2N2222A		50 75	300 375	
Output Admittance ( $I_C = 1.0 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ , $f = 1.0 \text{ kHz}$ )	2N2218A 2N2219A, 2N2222A	$h_{oe}$	3.0 5.0	15 35	$\mu\text{mhos}$
( $I_C = 10 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ , $f = 1.0 \text{ kHz}$ )	2N2218A 2N2219A, 2N2222A		10 15	100 200	
Collector Base Time Constant ( $I_E = 20 \text{ mAdc}$ , $V_{CB} = 20 \text{ Vdc}$ , $f = 31.8 \text{ MHz}$ )	A-Suffix	$rb'C_c$	—	150	ps
Noise Figure ( $I_C = 100 \mu\text{Adc}$ , $V_{CE} = 10 \text{ Vdc}$ , $R_S = 1.0 \text{ kohm}$ , $f = 1.0 \text{ kHz}$ )	2N2222A	NF	—	4.0	dB
Real Part of Common-Emitter High Frequency Input Impedance ( $I_C = 20 \text{ mAdc}$ , $V_{CE} = 20 \text{ Vdc}$ , $f = 300 \text{ MHz}$ )	2N2218A, 2N2219A 2N2222A	$Re(h_{je})$	—	60	Ohms

 (1) Pulse Test: Pulse Width  $\leq 300 \mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .

 (2)  $f_T$  is defined as the frequency at which  $|h_{fe}|$  extrapolates to unity.

 (3) 2N5581 and 2N5582 are Listed  $C_{cb}$  and  $C_{eb}$  for these conditions and values.

ELECTRICAL CHARACTERISTICS (continued) ( $T_A = 25^\circ\text{C}$  unless otherwise noted.)

Characteristic		Symbol	Min	Max	Unit
<b>SWITCHING CHARACTERISTICS</b>					
Delay Time	$(V_{CC} = 30\text{ Vdc}, V_{BE(\text{off})} = -0.5\text{ Vdc}, I_C = 150\text{ mA}, I_{B1} = 15\text{ mA})$ (Figure 12)	$t_d$	—	10	ns
Rise Time		$t_r$	—	25	ns
Storage Time	$(V_{CC} = 30\text{ Vdc}, I_C = 150\text{ mA}, I_{B1} = I_{B2} = 15\text{ mA})$ (Figure 13)	$t_s$	—	225	ns
Fall Time		$t_f$	—	60	ns
Active Region Time Constant ( $I_C = 150\text{ mA}, V_{CE} = 30\text{ Vdc}$ ) (See Figure 11 for 2N2218A, 2N2219A, 2N2221A, 2N2222A)		$T_A$	—	2.5	ns

FIGURE 1 – NORMALIZED DC CURRENT GAIN

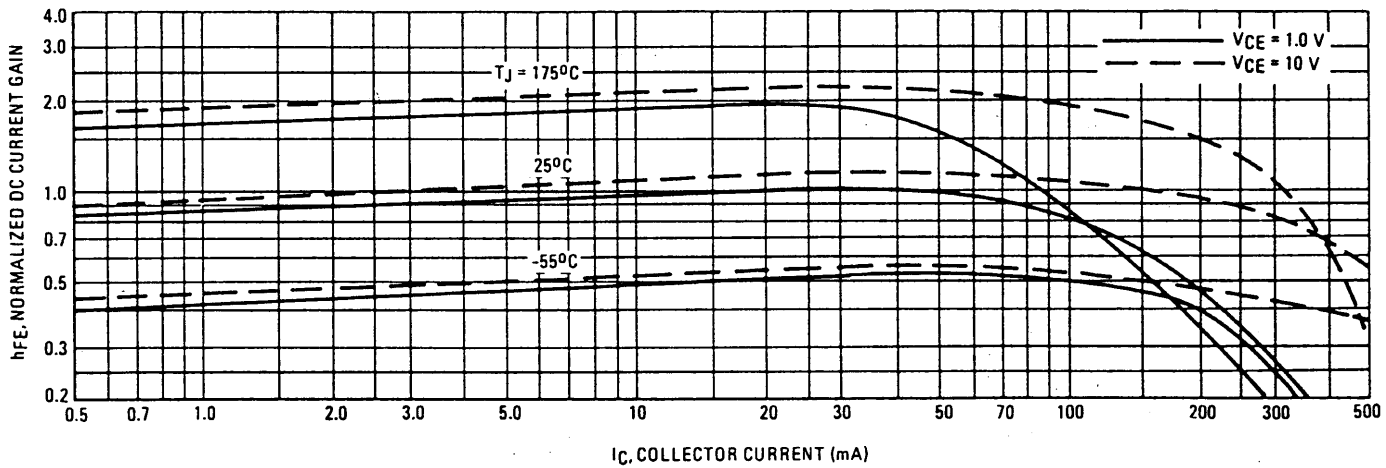
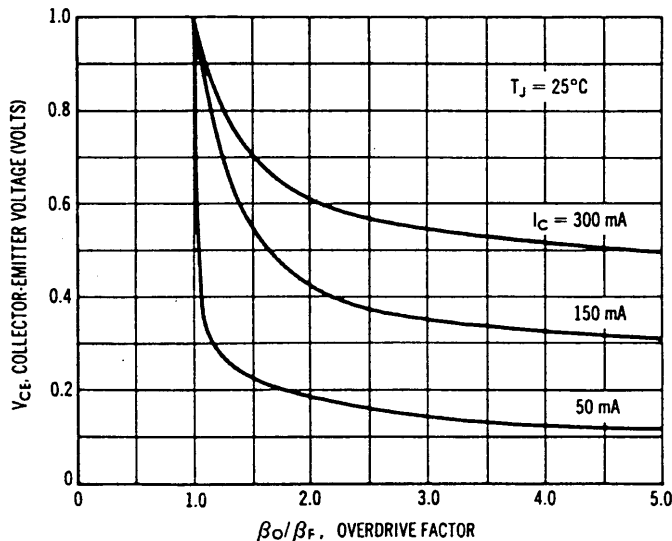


FIGURE 2 – COLLECTOR CHARACTERISTICS IN SATURATION REGION



This graph shows the effect of base current on collector current.  $\beta_o$  (current gain at the edge of saturation) is the current gain of the transistor at 1 volt, and  $\beta_f$  (forced gain) is the ratio of  $I_c/I_b$  in a circuit.

EXAMPLE: For type 2N2219, estimate a base current ( $I_b$ ) to insure saturation at a temperature of  $25^\circ\text{C}$  and a collector current of 150 mA.

Observe that at  $I_c = 150\text{ mA}$  an overdrive factor of at least 2.5 is required to drive the transistor well into the saturation region. From Figure 1, it is seen that  $h_{FE} @ 1\text{ V}$  is approximately 0.62 of  $h_{FE} @ 10\text{ V}$ . Using the guaranteed minimum gain of 100 @ 150 mA and 10 V,  $\beta_o = 62$  and substituting values in the overdrive equation, we find:

$$\frac{\beta_o}{\beta_f} = \frac{h_{FE} @ 1.0\text{ V}}{I_c/I_b} \quad 2.5 = \frac{62}{150/I_b} \quad I_b \approx 6.0\text{ mA}$$

2N2218A 2N2219,A 2N2222,A

FIGURE 3 — "ON" VOLTAGES

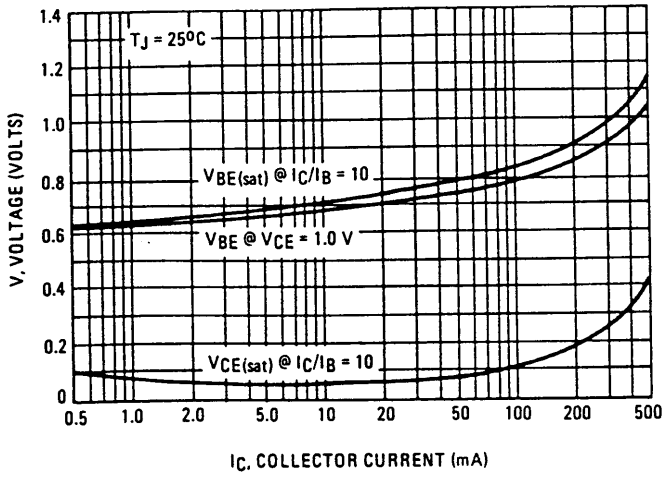
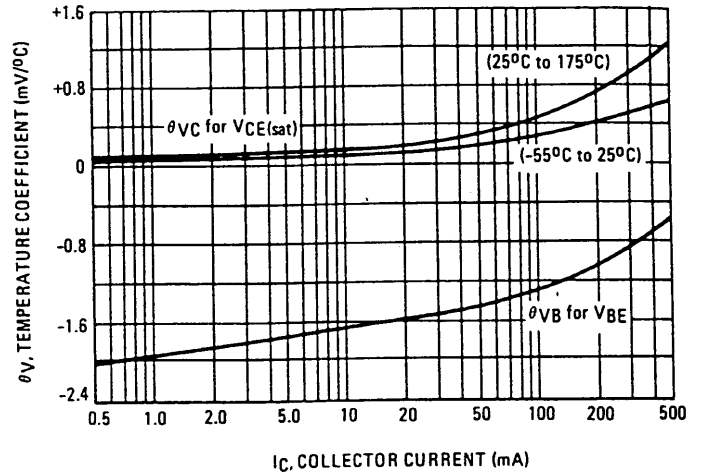


FIGURE 4 — TEMPERATURE COEFFICIENTS



**h** PARAMETERS

$V_{CE} = 10 \text{ Vdc}$ ,  $f = 1.0 \text{ kHz}$ ,  $T_A = 25^\circ\text{C}$

This group of graphs illustrates the relationship between  $h_{fe}$  and other "h" parameters for this series of transistors. To obtain these curves, a high-gain and a low-gain unit were selected and the same units were used to develop the correspondingly numbered curves on each graph.

FIGURE 5 — INPUT IMPEDANCE

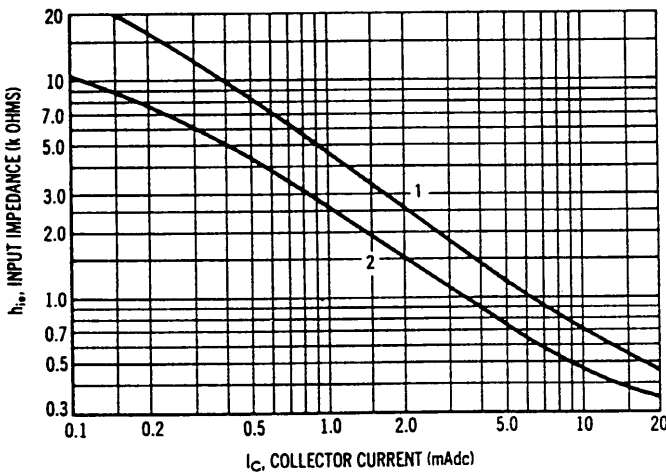


FIGURE 6 — VOLTAGE FEEDBACK RATIO

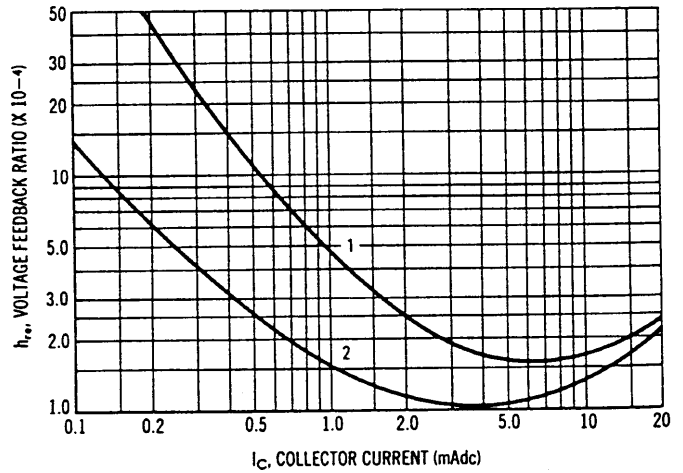


FIGURE 7 — CURRENT GAIN

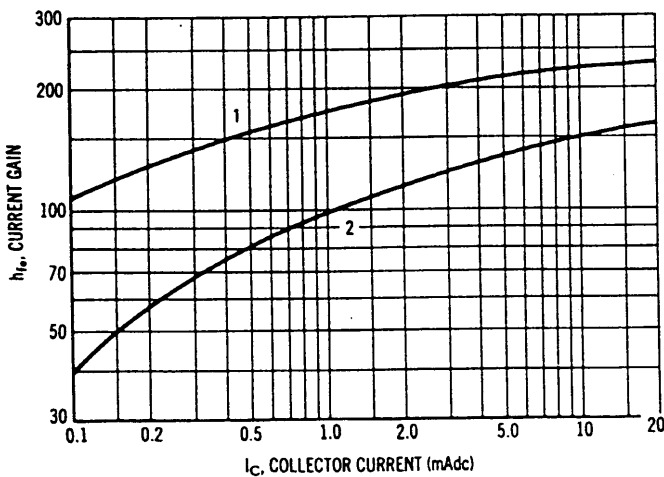
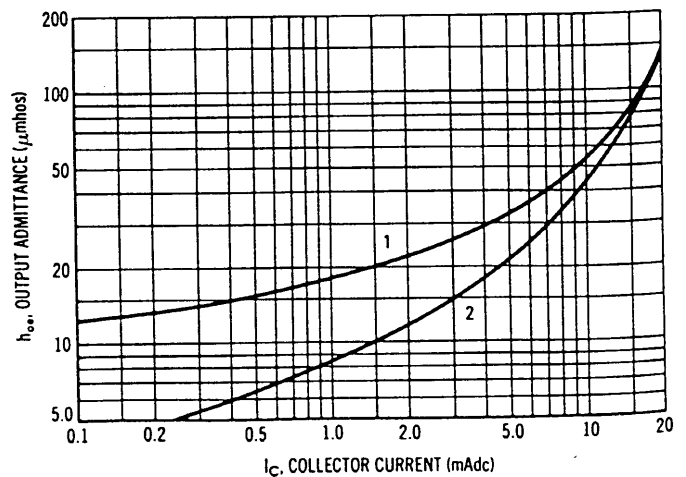


FIGURE 8 — OUTPUT ADMITTANCE



SWITCHING TIME CHARACTERISTICS

FIGURE 9 — TURN-ON TIME

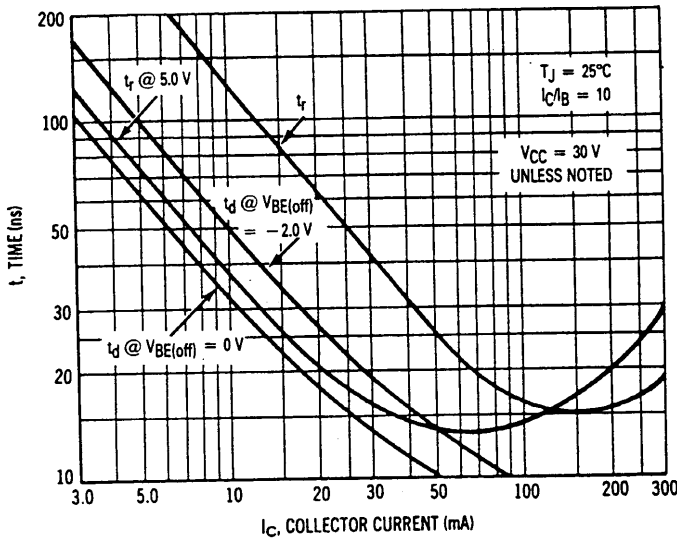


FIGURE 10 — CHARGE DATA

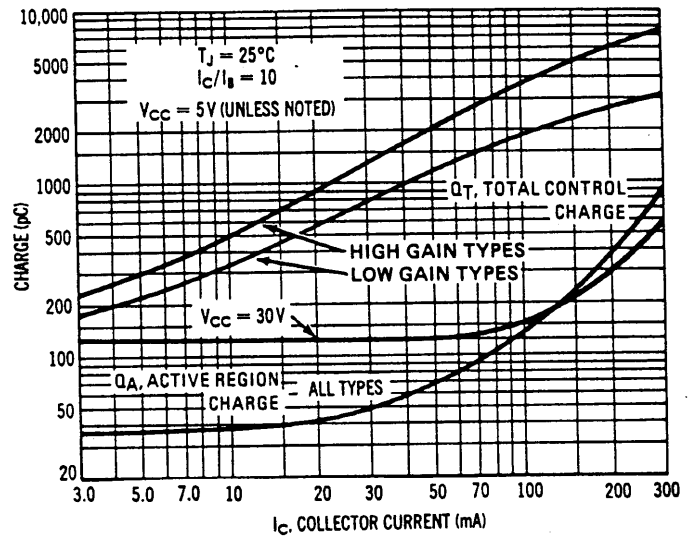


FIGURE 11 — TURN-OFF BEHAVIOR

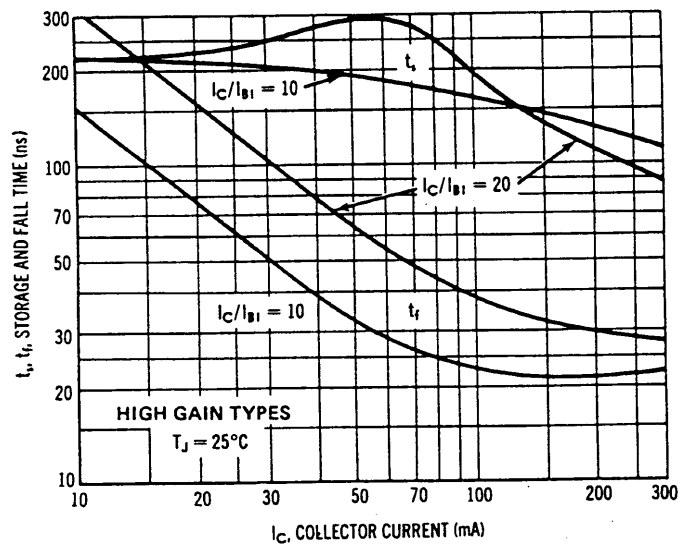
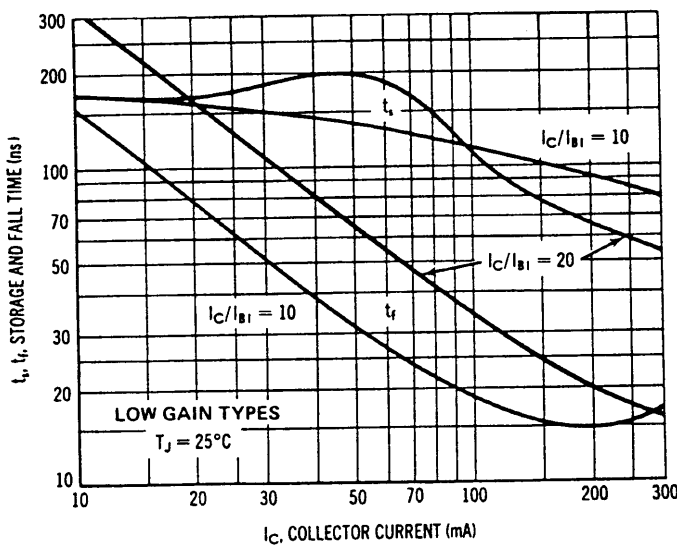


FIGURE 12 — DELAY AND RISE TIME EQUIVALENT TEST CIRCUIT

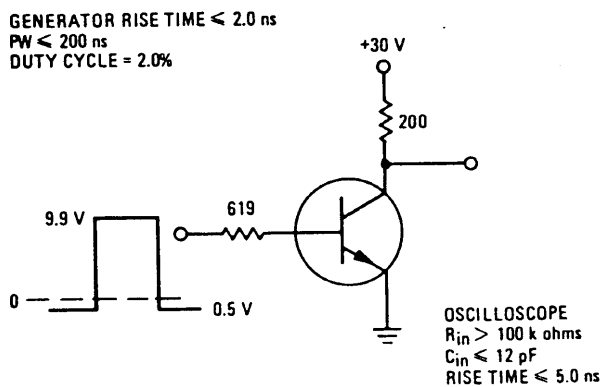


FIGURE 13 — STORAGE TIME AND FALL TIME EQUIVALENT TEST CIRCUIT

