

CFPT-5300 SERIES

ISSUE 1; 15 OCTOBER 2004

Delivery Options

- Please contact our sales office for current lead times

Description

- The CFPT-5300 series of temperature compensated crystal oscillators (TCXOs), provide for ultra high stabilities down to ± 0.3 ppm and operating temperature ranges as wide as -55 to 95°C. Housed in an industry standard 4 pin DIL 14 package, the oscillator uses C-MAC's latest custom ASIC "Pluto"; a single chip oscillator and analogue compensation circuit. Its wide frequency range, operating temperature range, coupled with its high stability and linear frequency pulling make it the ideal reference oscillator. Its ability to function down to supply voltage of 2.4V and low power consumption makes it suitable for a wide range of applications

Standard Frequency

- 3.2, 4.096, 5.0, 6.4, 8.192, 9.6, 10.0, 12.8, 13.0, 14.4, 14.85, 16.384, 16.8, 19.2, 19.44, 19.8, 20, 32.768, 38.88, 40.0 MHz

Non - standard frequencies in the range 1.25MHz to 40.0MHz are available on request, please contact our sales office

Waveform

- Square HCMOS 15pF load

The following non-standard outputs are available on request, contact sales office:

- Square ACMOS 50pF max load
- Sinewave, ≥ 1.0 pk-pk, 10k Ω // 10pF load
- Clipped Sinewave, ≥ 0.8 Vpk-pk, 10k Ω // 10pF load

Sinewave and clipped sinewave signals are superimposed on a DC offset, to remove this offset insert an external coupling capacitor in series with the output

Supply Voltages

- 3.3V or 5.0V
(non standard supply voltages in the range 2.4V to 6.0V are available on request, please contact our sales office)

Current Consumption

- HCMOS Typically »
 $1 + \text{Frequency (MHz)} * \text{Supply (V)} * \{\text{Load (pF)} + 15\} * 10^{-3} \text{mA}$
Eg 20MHz, 5V, 15pF » 4mA
- Sinewave, ≤ 8 mA
- Clipped sinewave,
 $1 + \text{Frequency (MHz)} * 1.2 * \{\text{Load (pF)} + 30\} * 10^{-3} \text{mA}$

Package Outline

- Low Profile DIL 14 resistance welded enclosure, hermetically sealed

Ageing

- ± 1 ppm maximum in first year, frequency ≤ 20 MHz
- ± 3 ppm maximum for 10 years, frequency ≤ 20 MHz
- ± 2 ppm maximum in first year, frequency > 20 MHz
- ± 5 ppm maximum for 10 years, frequency > 20 MHz

Frequency Stability

- Temperature: see table
 - Supply Voltage Variation $\pm 10\% \leq \pm 0.2$ ppm*
 - Load coefficient 15pF ± 5 pF $\leq \pm 0.2$ ppm*
- * Dependent on frequency and output type

Frequency Adjustment

■ Option A (standard):

- Control Voltage 1.5V \pm 1.0V, applied to pin 1
- $\geq \pm 5$ ppm*, frequency ≤ 20 MHz
- $\geq \pm 7$ ppm, frequency > 20 MHz
- Linearity $\leq 3\%$
- Slope Positive
- Input resistance > 100 k Ω
- Modulation Bandwidth > 2 kHz

■ Option B:

- $\geq \pm 5$ ppm *($\geq \pm 7$ ppm if frequency > 20 MHz) by means of an external 100k Ω potentiometer connected as a variable resistor from pin 1 to GND

* Higher adjustment range up to ± 50 ppm and non standard control voltage ranges are available on request. This may not be compatible with all stability options, contact sales office

■ Option C:

- No frequency adjustment, initial calibration @25°C $\leq \pm 1.0$ ppm

Storage Temperature Range

- -55 to 95°C

Environmental Specification

- Bump: IEC 60068-2-29, Test Eb, 4000 ±10 bumps at 390m/s² in each of the three mutually perpendicular axes
- Shock: IEC 60068-2-27 Test Ea, 980m/s² acceleration for 6ms duration, 3 shocks in each direction along three mutually perpendicular axes
- Solderability: IEC 60068-2-20, Test T, method 1 (solder bath): Temperature 235°C
- Vibration: IEC 60068-2-6 Test Fc Procedure B4, 10-60Hz 1.5mm displacement, 60-2000Hz at 98.1 m/s², 30 minutes in each of three mutually perpendicular axes at 1 octave per minute
- Damp Heat (steady state): IEC 60068-2-3, Test Ca, Duration 56 days
- Robustness of Termination: IEC 60068-2-21, Test Ua (tensile): Force 1Kg
- Sealing: IEC 60068-2-17, Test Qc (gross) & test Qk (fine)
- Marking: Heat cured epoxy ink, engraving or label
- Immersion in Solvents: IEC 60068-2-45, Test Xa

Marking Includes

- C-MAC
- Part Number (E and four digits)
- Frequency (MHz)
- Pin 1 / Static sensitivity identified (Triangle)
- Date Code and manufacturing location code (YYWWL)

Minimum Order Information Required

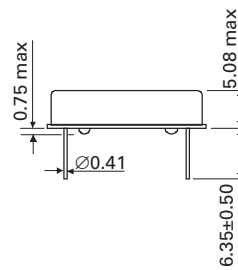
- Frequency + Model Number + Frequency Stability Vs
Operating Temperature Code + Frequency Adjustment Code

OR

- Discrete part number for repeat orders

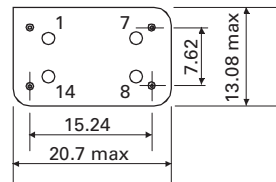
Please supply full information for non-standard options, if required

Outline in mm

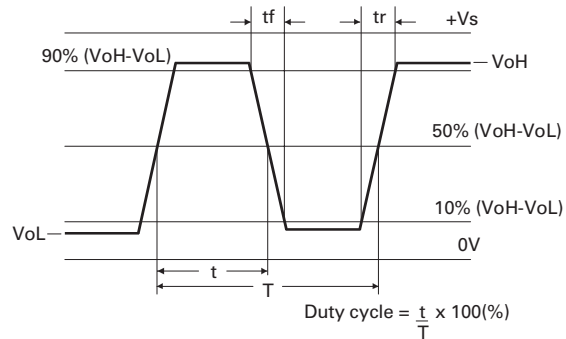


Pin Connections

1. Frequency Adjust (do not connect if Frequency Adjust option C has been selected)
7. GND
8. Output
14. +Vs



Output Waveform - HCMOS



LEADED TCXOs

Test Circuit - HCMOS

