TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSIII)

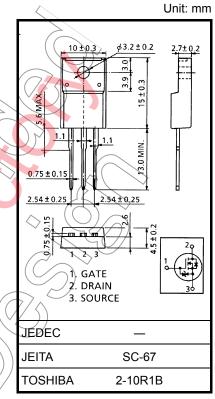
# 2SK3754

Relay Drive, DC–DC Converter and Motor Drive Applications

- 4.5-V gate drive
- Low drain-source ON resistance:  $R_{DS (ON)} = 71 \text{ m}\Omega (typ.)$
- High forward transfer admittance:  $|Y_{fs}| = 5.0 \text{ S}$  (typ.)
- Low leakage current: I<sub>DSS</sub> = 10 μA (max) (V<sub>DS</sub> = 30 V)
- Enhancement-model:  $V_{th}$  = 1.3 to 2.5 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	30	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )			V <sub>DGR</sub>	30	V
Gate-source voltage			V <sub>GSS</sub>	±20	V
Drain current	DC	(Note 1)	I <sub>D</sub>	5	
	Pulse	(Note 1)		15	
Drain power dissipation (Tc = $25^{\circ}$ C)			PD	25	< <w w</w 
Single pulse avalanche energy (Note 2)			EAS	4.0	mJ
Avalanche current				2.5	A
Repetitive avalanche energy (Note 3)			EAR	2.5	L'uu
Channel temperature			Tch	150	°C
Storage temperature range			Tstg	-55 to 1,50	∽°C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

## **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	5.0	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2:  $V_{DD}$  =24 V, T<sub>ch</sub> = 25°C (initial), L = 0.5 mH, R<sub>G</sub> = 25  $\Omega$ , I<sub>AR</sub> = 2.5 A

Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.

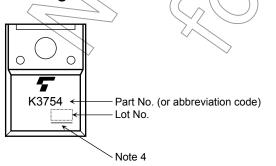
**Electrical Characteristics (Ta = 25°C)** 

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage current		I <sub>GSS</sub>	$V_{GS}=\pm 16~V,~V_{DS}=0~V$	_		±10	μA	
Drain cut-off current		I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μA	
Drain-source breakdown voltage		V (BR) DSS	$I_{D} = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30			v	
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	(15	2	_		
Gate threshold voltage		V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	1.3	Ŋ	2.5	V	
Drain-source ON resistance		D- a (a)	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 2.5 \text{ A}$		78	99	mΩ	
		R <sub>DS</sub> (ON)	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 2.5 \text{ A}$	Ţ	71	89	1115.2	
Forward transfer admittance		Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 2.5 \text{ A}$	2.5	5.0		S	
Input capacitance		C <sub>iss</sub>			1250			
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		155	/	pF	
Output capacitance		C <sub>oss</sub>		— (	170	>		
Switching time	Rise time	t <sub>r</sub>	$V_{GS}^{10}$ $V_{UD} = 2.5$ $A$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$ $R_L = 6 \Omega$	_(		> —	ns	
	Turn-on time	t <sub>on</sub>			16			
	Fall time	t <sub>f</sub>			18			
	Turn-off time	toff	Duty $\leq 1\%$ , t <sub>w</sub> = 10 $\mu$ s	_	69			
Total gate charge		Qg		_	25	_		
Gate-source charge		Qgs	$V_{DD} \simeq 24 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$	_	20	_	nC	
Gate-drain charge					5			

# Source-Drain Ratings and Characteristics (Ta = $25^{\circ}$ C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	TDR	$\rightarrow$ –	_	_	5	А
Pulse drain reverse current (Note 1)	IDRP	_	_	_	15	А
Reverse recovery time	trr	V <sub>DR</sub> = 5 A, V <sub>GS</sub> = 0 V,	_	37	_	ns
Reverse recovery charge	Qrr	dI <sub>DR</sub> /dt = 50 A/µs	_	20	_	nC

### Marking



Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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