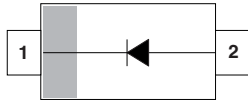
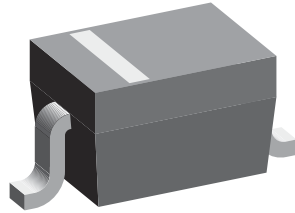


## Small Signal Fast Switching Diode



1 = Cathode  
2 = Anode

22611



20145

### FEATURES

- These diodes are also available in other case styles including the DO-35 case with the type designation 1N4148, the MiniMELF case with the type designation LL4148, and the SOT-23 case with the type designation IMBD4148-V
- Silicon epitaxial planar diode
- Fast switching diodes
- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)


**RoHS**  
COMPLIANT

### MARKING (example only)



22610

Bar = cathode marking  
XY = type code

### MECHANICAL DATA

**Case:** SOD-323

**Weight:** approx. 4.3 mg

**Packaging codes/options:**

GS18/10K per 13" reel (8 mm tape), 10K/box

GS08/3K per 7" reel (8 mm tape), 15K/box

### PARTS TABLE

PART	ORDERING CODE	TYPE MARKING	REMARKS
1N4148WS-V	1N4148WS-V-GS18 or 1N4148WS-V-GS08	A2	Tape and reel

### ABSOLUTE MAXIMUM RATINGS ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		$V_R$	75	V
Repetitive peak reverse voltage		$V_{RRM}$	100	
Average rectified current half wave rectification with resistive load <sup>(1)</sup>	$f \geq 50\text{ Hz}$	$I_{F(AV)}$	150	mA
Surge forward current	$t < 1\text{ s}$ and $T_j = 25\text{ }^{\circ}\text{C}$	$I_{FSM}$	350	
Power dissipation <sup>(1)</sup>		$P_{tot}$	200	mW

#### Note

<sup>(1)</sup> Valid provided that electrodes are kept at ambient temperature.



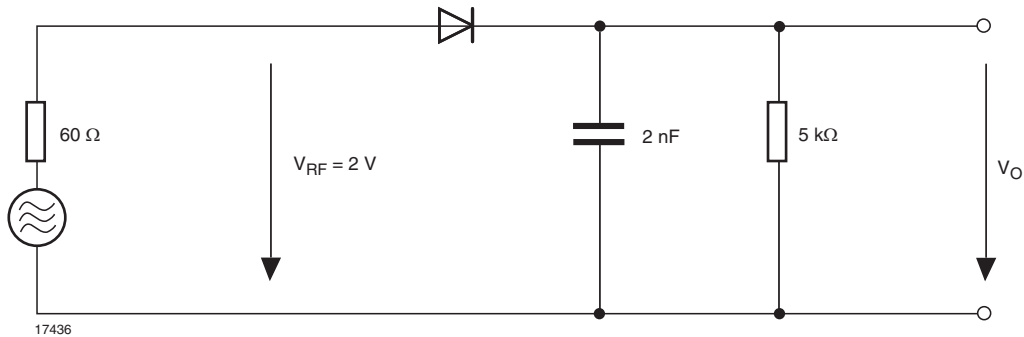
THERMAL CHARACTERISTICS ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Thermal resistance junction to ambient air <sup>(1)</sup>		$R_{thJA}$	650	K/W
Junction temperature		$T_j$	150	$^{\circ}\text{C}$
Operating temperature range		$T_j$	- 55 to + 150	
Storage temperature range		$T_{stg}$	- 65 to + 150	

Note

(1) Valid provided that electrodes are kept at ambient temperature

ELECTRICAL CHARACTERISTICS ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 10\text{ mA}$	$V_F$			1000	mV
	$I_F = 100\text{ mA}$	$V_F$			1200	
Leakage current	$V_R = 20\text{ V}$	$I_R$			25	nA
	$V_R = 75\text{ V}$	$I_R$			5	$\mu\text{A}$
	$V_R = 100\text{ V}$	$I_R$			100	
	$V_R = 20\text{ V}, T_j = 150\text{ }^{\circ}\text{C}$	$I_R$			50	
Diode capacitance	$V_F = V_R = 0\text{ V}$	$C_D$			4	pF
Voltage rise when switching ON	Tested with 50 mA pulses, $t_p = 0.1\text{ }\mu\text{s}$ , rise time < 30 ns, $f_p = (5\text{ to }100)\text{ kHz}$	$V_{fr}$			2.5	V
Reverse recovery time	$I_F = 10\text{ mA}, I_R = 1\text{ mA}, V_R = 6\text{ V},$ $R_L = 100\text{ }\Omega$	$t_{rr}$			4	ns
Rectification efficiency	$f = 100\text{ MHz}, V_{RF} = 2\text{ V}$	$\eta_v$	0.45			

RECTIFICATION EFFICIENCY MEASUREMENT CIRCUIT



**TYPICAL CHARACTERISTICS** ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)

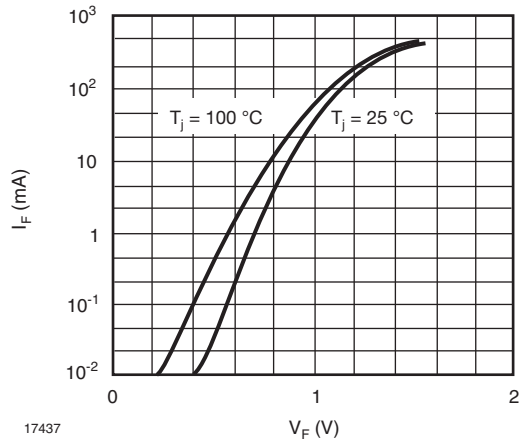


Fig. 1 - Forward characteristics

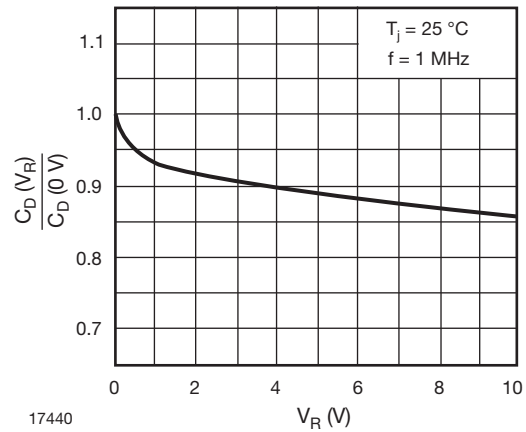


Fig. 4 - Relative Capacitance vs. Reverse Voltage

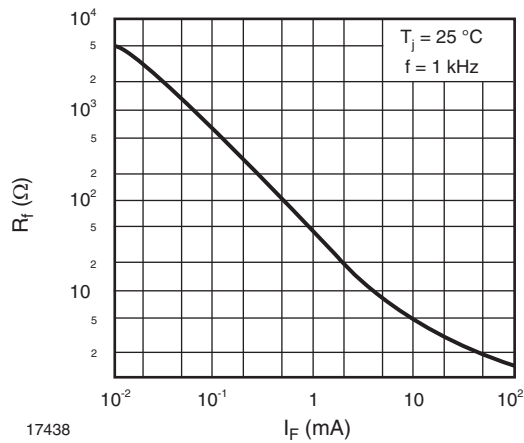


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

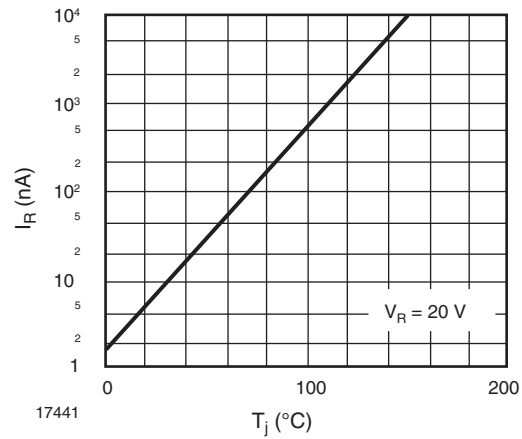


Fig. 5 - Leakage Current vs. Junction Temperature

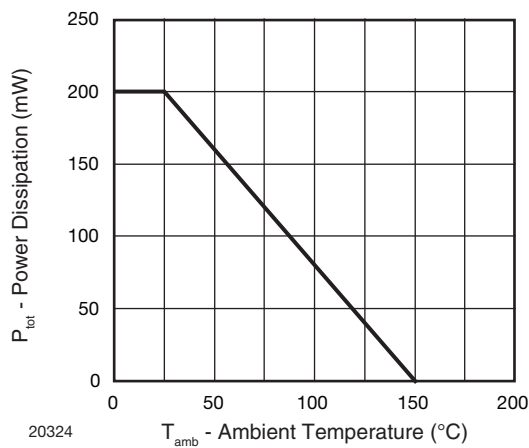


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

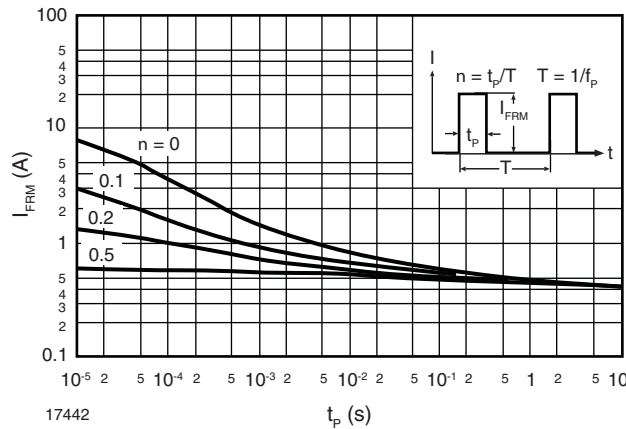
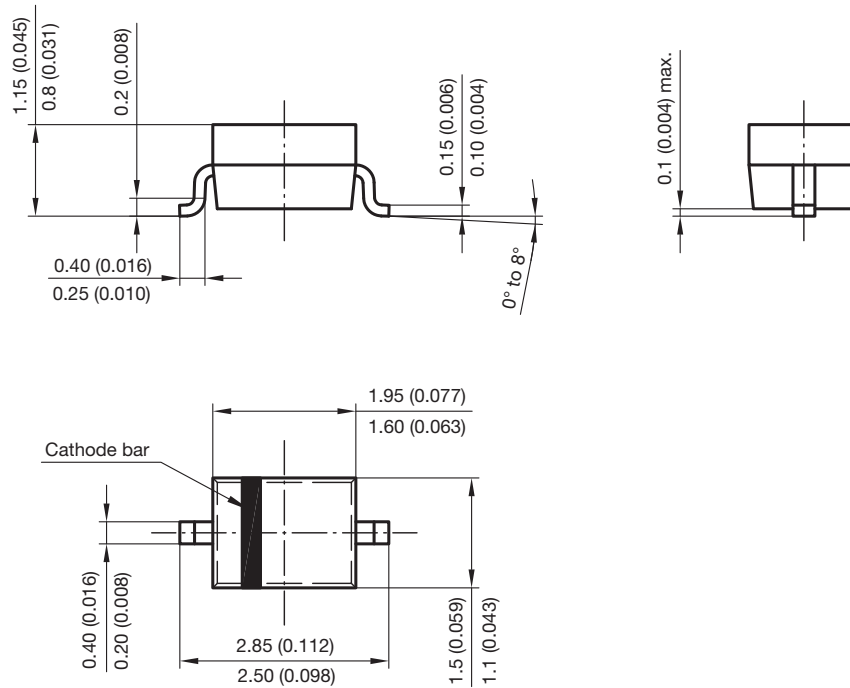
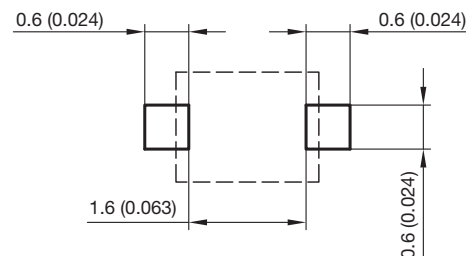


Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration

**PACKAGE DIMENSIONS** in millimeters (inches): **SOD-323**



Foot print recommendation:



Document no.:S8-V-3910.02-001 (4)  
 Created - Date: 24.August.2004  
 Rev. 5 - Date: 23.Sept.2009  
 17443



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