

QUAD SINGLE-SUPPLY OPERATIONAL AMPLIFIER

■ GENERAL DESCRIPTION

The NJM2902 consists of four independent high-gain operational amplifiers that are designed for single-supply operation.

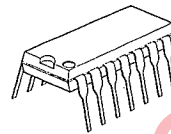
Operation from split power supplies is also possible and the low power supply drain is independent of the magnitude of the power supply voltage.

Used with a dual supply the circuit will operate over a wide range of supply voltages. However, a large amount of crossover distortion may occur with loads to ground. An external current-sinking resistor to  $-V_s$  will reduce crossover distortion. There is no crossover distortion problem in single-supply operation if the load is direct-coupled to ground.

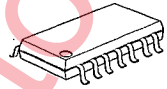
■ FEATURES

- Single Supply
- Operating Voltage (+3V ~ +30V)
- High Output Voltage ( $V^+ - 2V$ )
- Slew Rate (0.5V/μs typ.)
- Low Operating Current (1mA typ.)
- Package Outline DIP14, DMP14, SSOP14
- Bipolar Technology

■ PACKAGE OUTLINE



NJM2902N

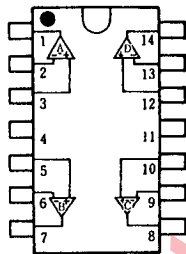


NJM2902M



NJM2902V

■ PIN CONFIGURATION

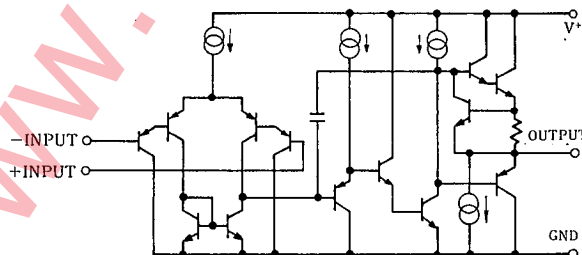


NJM2902N  
NJM2902M  
NJM2902V

PIN FUNCTION

- |             |              |
|-------------|--------------|
| 1. A OUTPUT | 8. C OUTPUT  |
| 2. A -INPUT | 9. C -INPUT  |
| 3. A +INPUT | 10. C +INPUT |
| 4. $V^+$    | 11. GND      |
| 5. B +INPUT | 12. D +INPUT |
| 6. B -INPUT | 13. D -INPUT |
| 7. B OUTPUT | 14. D OUTPUT |

■ EQUIVALENT CIRCUIT (1/4 Shown)



## ■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V <sup>+</sup> (V <sup>+</sup> /V <sup>-</sup> )	32(or ±16)	V
Differential Input Voltage	V <sub>ID</sub>	32	V
Input Voltage	V <sub>IC</sub>	-0.3~+32 (note)	V
Power Dissipation	P <sub>D</sub>	(DIP14) 570	mW
		(DMP14) 300	mW
		(SSOP14) 300	mW
Operating Temperature Range	T <sub>opr</sub>	-40~+85	°C
Storage Temperature Range	T <sub>stg</sub>	-50~+125	°C

## ■ ELECTRICAL CHARACTERISTICS

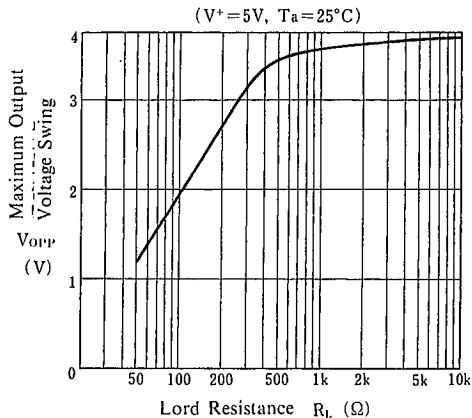
(Ta=25°C V<sup>+</sup>=5V)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V <sub>IO</sub>	R <sub>S</sub> =0Ω	—	2	10	mV
Input Offset Current	I <sub>IO</sub>	I <sub>IN</sub> <sup>+</sup> - I <sub>IN</sub> <sup>-</sup>	—	5	50	nA
Input Bias Current	I <sub>B</sub>	I <sub>IN</sub> <sup>+</sup> or I <sub>IN</sub> <sup>-</sup>	—	20	500	nA
Large Signal Voltage Gain	A <sub>V</sub>	R <sub>L</sub> ≥ 2kΩ	—	100	—	V/mV
Maximum Output Voltage Swing	V <sub>OM</sub>	R <sub>L</sub> =2kΩ	3.5	—	—	V
Input Common Mode Voltage Range	V <sub>ICM</sub>		0~3.5	—	—	V
Common Mode Rejection Ratio	CMR		—	85	—	dB
Supply Voltage Rejection Ratio	SVR		—	100	—	dB
Output Source Current	I <sub>SOURCE</sub>	V <sub>IN</sub> <sup>+</sup> =1V, V <sub>IN</sub> <sup>-</sup> =0V	20	40	—	mA
Output Sink Current	I <sub>SINK</sub>	V <sub>IN</sub> <sup>+</sup> =0V, V <sub>IN</sub> <sup>-</sup> =1V	8	20	—	mA
Channel Separation	CS	f=1k~20kHz, Input Referred	—	120	—	dB
Operating Current	I <sub>CC</sub>	R <sub>L</sub> =∞	—	1	2	mA
Slew Rate	SR	V <sup>+</sup> /V <sup>-</sup> = ±15V	—	0.5	—	V/μs
Gain Bandwidth Product	GB	V <sup>+</sup> /V <sup>-</sup> = ±15V	—	0.5	—	MHz

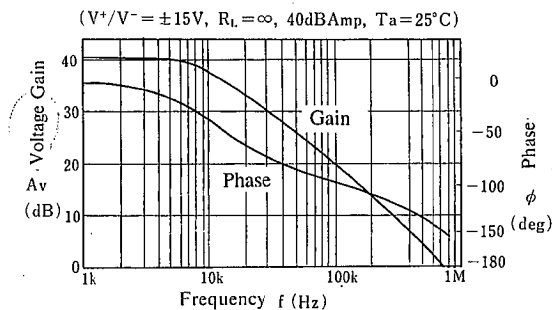
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## TYPICAL CHARACTERISTICS

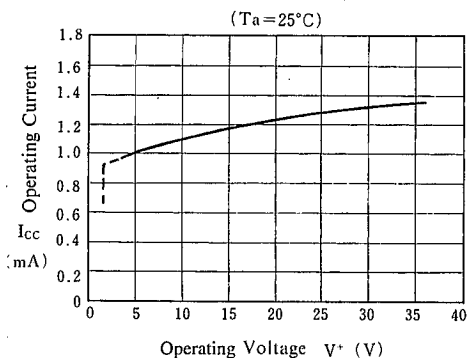
### Maximum Output Voltage Swing vs. Load Resistance



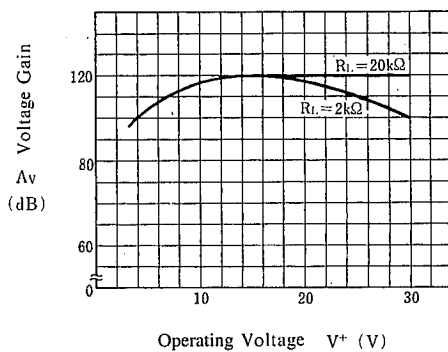
### Voltage Gain, Phase vs. Frequency



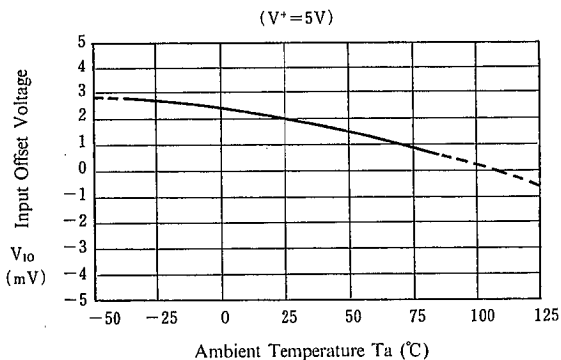
### Operating Current vs. Operating Voltage



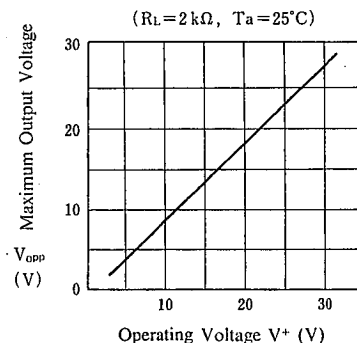
### Voltage Gain vs. Operating Voltage



### Input Offset Voltage vs. Temperature



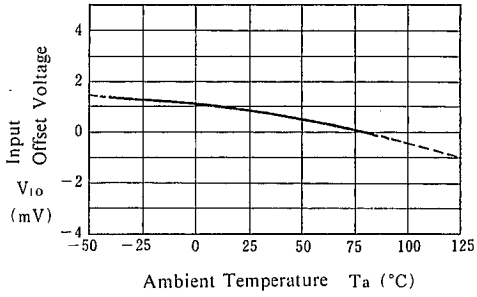
### Maximum Output Voltage vs. Operating Voltage



■ TYPICAL CHARACTERISTICS

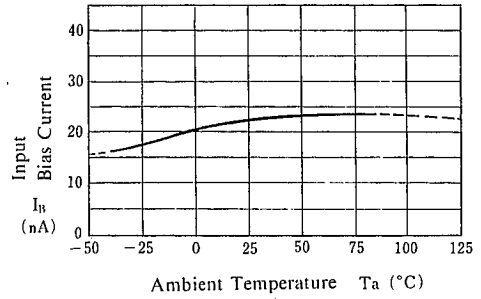
Input Offset Voltage vs. Temperature

( $V^+ = 5\text{ V}$ )



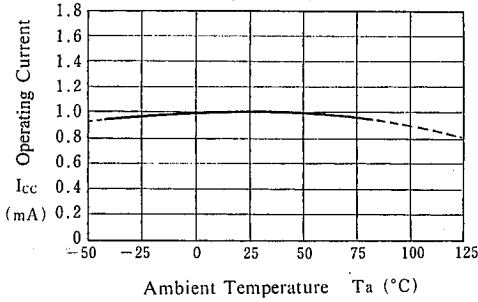
Input Bias Current vs. Temperature

( $V^+ = 5\text{ V}$ )



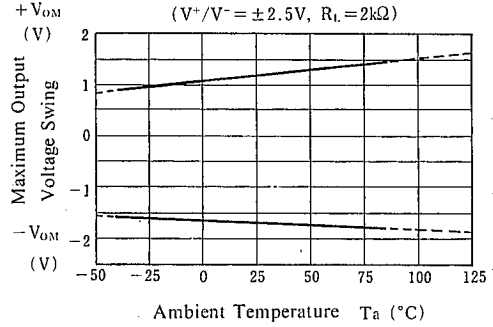
Operating Current vs. Temperature

( $V^+ = 5\text{ V}$ )



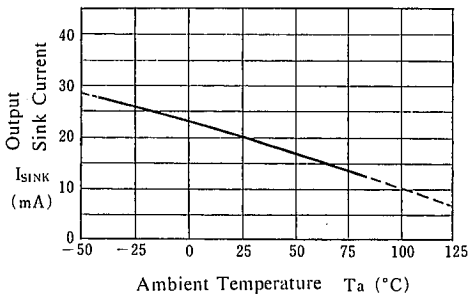
Maximum Output Voltage Swing vs. Temperature

( $V^+/V^- = \pm 2.5\text{ V}$ ,  $R_L = 2\text{ k}\Omega$ )

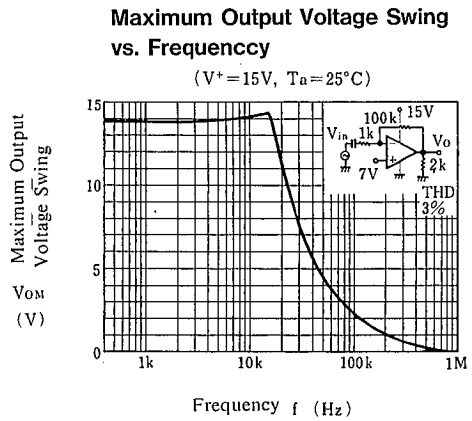
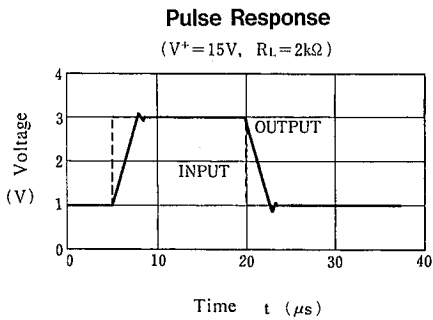
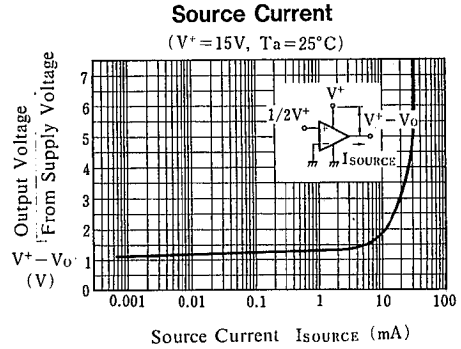
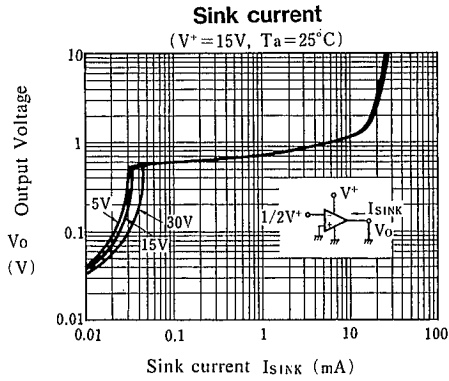


Output Sink Current vs. Temperature

( $V^+ = 5\text{ V}$ )

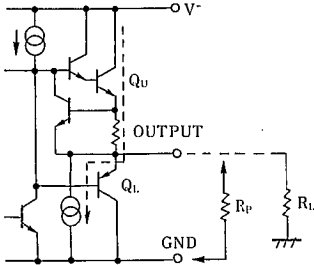


## ■ TYPICAL CHARACTERISTICS



■ APPLICATION

Improvement of Cross-over Distortion  
Equivalent circuit at the output stage

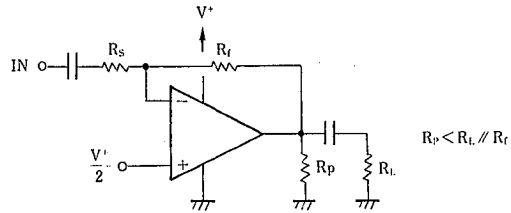
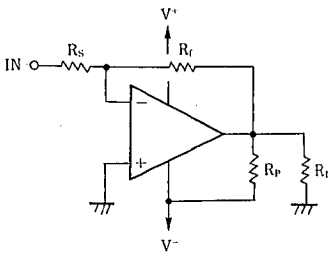


NJM2902, in its static state (No in and output condition) when design,  $Q_U$  being biased by constant current (breake down beam) yet,  $Q_L$  stays OFF.

While using with both power soure mode, the cross-over distortion might occure instantly when  $Q_L$  ON.

There might be cases when application for amplifier of audio signals, not only distortion but also the apparent frequency bandwidth being narrowed remarkably.

It is aduisable especially when using both power soure mode, constantly to use with higher current on  $Q_U$  than the load current (including feedback current), and then connect the pull-down resister  $R_P$  at the part between output and GND pins.



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## MEMO

[CAUTION]

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