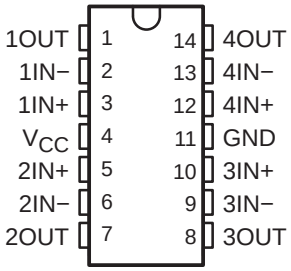
- **Qualification in Accordance With AEC-Q100†**
- **Qualified for Automotive Applications**
- **Customer-Specific Configuration Control Can Be Supported Along With Major-Change Approval**
- **ESD Protection <500 V Per MIL-STD-883, Method 3015; Exceeds 200 V Using Machine Model (C = 200 pF, R = 0); 1500 V Using Charged Device Model**
- **ESD Protection <2 kV Using Machine Model; 2000 V Using Charged Device Model for K-Suffix Devices**
- **Low Supply-Current Drain Independent of Supply Voltage . . . 0.8 mA Typ**
- **Low Input Bias and Offset Parameters:**
  - **Input Offset Voltage . . . 3 mV Typ**
  - **Input Offset Current . . . 2 nA Typ**
  - **Input Bias Current . . . 20 nA Typ**
- **Common-Mode Input Voltage Range Includes Ground, Allowing Direct Sensing Near Ground**
- **Differential Input Voltage Range Equal to Maximum-Rated Supply Voltage:**
  - **Non-V devices . . . 26 V**
  - **V-Suffix devices . . . 32 V**
- **Open-Loop Differential Voltage Amplification . . . 100 V/mV Typ**
- **Internal Frequency Compensation**

† Contact factory for details. Q100 qualification data available on request.

D OR PW PACKAGE
(TOP VIEW)



description/ordering information

This device consists of four independent high-gain frequency-compensated operational amplifiers that are designed specifically to operate from a single supply over a wide range of voltages. Operation from split supplies also is possible when the difference between the two supplies is 3 V to 26 V (3 V to 32 V for V-suffixed devices) and V_{CC} is at least 1.5 V more positive than the input common-mode voltage. The low supply-current drain is independent of the magnitude of the supply voltage.

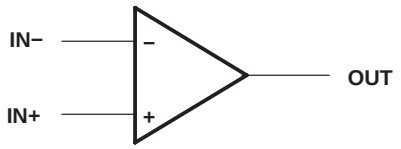
Applications include transducer amplifiers, dc amplification blocks, and all the conventional operational-amplifier circuits that now can be more easily implemented in single-supply-voltage systems. For example, the LM2902 can be operated directly from the standard 5-V supply that is used in digital systems and easily provides the required interface electronics without requiring additional ±15-V supplies.

ORDERING INFORMATION

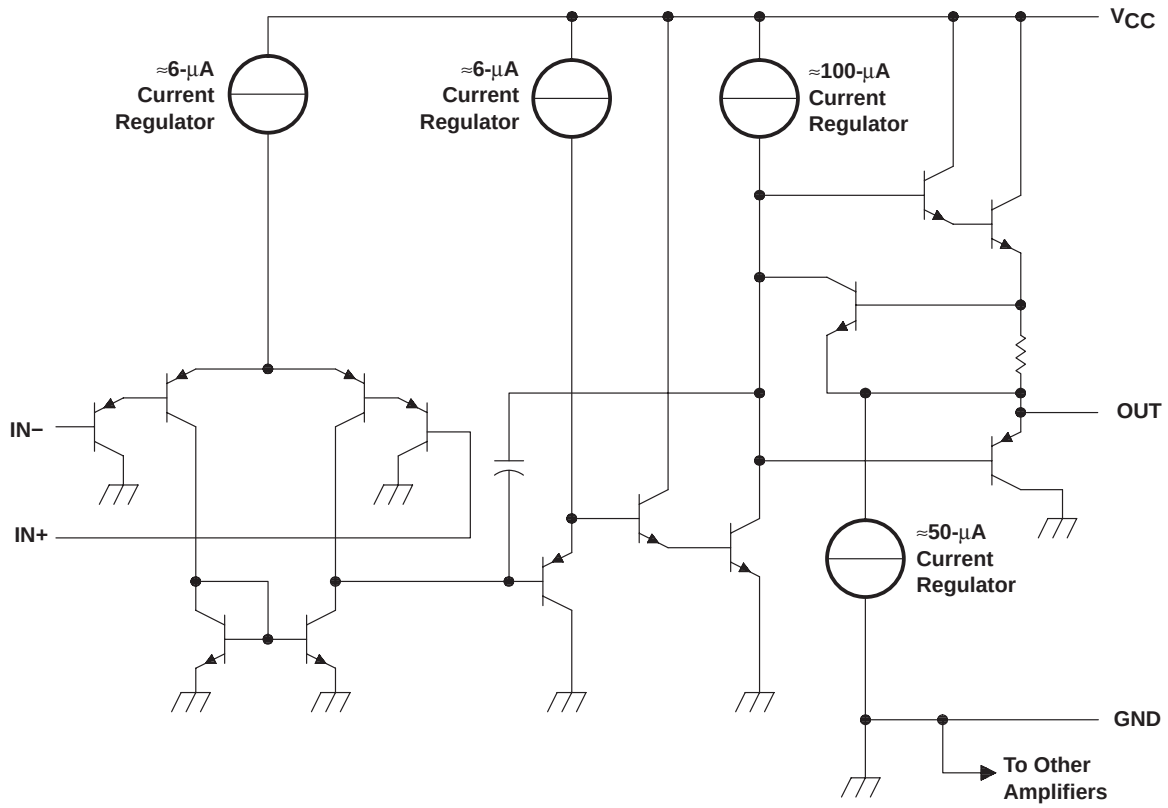
T_A	V_{IO} max AT 25°C	MAX V_{CC}	PACKAGE‡		ORDERABLE PART NUMBER	TOP-SIDE MARKING
−40°C to 125°C	7 mV	26 V	SOIC (D)	Reel of 2500	LM2902QDRQ1	2902Q1
			TSSOP (PW)	Reel of 2000	LM2902QPWRQ1	2902Q1
	7 mV	32 V	SOIC (D)	Reel of 2500	LM2902KVQDRQ1	2902KVQ
			TSSOP (PW)	Reel of 2000	LM2902KVQPWRQ1	2902KVQ
	2 mV	32 V	SOIC (D)	Reel of 2500	LM2902KAVQDRQ1	2902KAQ
			TSSOP (PW)	Reel of 2000	LM2902KAVQPWRQ1	2902KAQ

‡ Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

symbol (each amplifier)



schematic (each amplifier)



COMPONENT COUNT (TOTAL DEVICE)	
Epi-FET	1
Transistors	95
Diodes	4
Resistors	11
Capacitors	4

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

		LM2902-Q1	LM2902KV-Q1	UNIT
Supply voltage, V_{CC} (see Note 1)		26	32	V
Differential input voltage, V_{ID} (see Note 2)		±26	±32	V
Input voltage, V_I (either input)		−0.3 to 26	−0.3 to 32	V
Duration of output short circuit (one amplifier) to ground at (or below) T_A = 25°C, V_{CC} ≤ 15 V (see Note 3)		Unlimited	Unlimited	
Package thermal impedance, θ_{JA} (see Notes 4 and 5)	D package (0 LFPM)	101	101	°C/W
	PW package	113	113	
Operating virtual junction temperature, T_J		142	142	°C
Storage temperature range, T_{stg}		−65 to 150	−65 to 150	°C

† Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES:
1. All voltage values, except differential voltages and V_{CC} specified for the measurement of I_{OS}, are with respect to the network GND.
2. Differential voltages are at IN+ with respect to IN−.
3. Short circuits from outputs to V_{CC} can cause excessive heating and eventual destruction.
4. Maximum power dissipation is a function of $T_J(max)$, θ_{JA}, and T_A. The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) - T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 142°C can affect reliability.
5. The package thermal impedance is calculated in accordance with JESD 51-7.

electrical characteristics at specified free-air temperature, V_{CC} = 5 V (unless otherwise noted)

PARAMETER		TEST CONDITIONS†		T_A‡	LM2902-Q1			UNIT
					MIN	TYP§	MAX	
V_{IO}	Input offset voltage	V_{CC} = 5 V to 26 V, $V_{IC} = V_{ICR}$min, V_O = 1.4 V		25°C		3	7	mV
				Full range			10	
I_{IO}	Input offset current	V_O = 1.4 V		25°C		2	50	nA
				Full range			300	
I_{IB}	Input bias current	V_O = 1.4 V		25°C		−20	−250	nA
				Full range			−500	
V_{ICR}	Common-mode input voltage range	V_{CC} = 5 V to 26 V		25°C	0 to V_{CC} − 1.5			V
				Full range	0 to V_{CC} − 2			
V_{OH}	High-level output voltage	R_L = 10 kΩ		25°C	V_{CC} − 1.5			V
		V_{CC} = 26 V,	R_L = 2 kΩ	Full range	22			
		V_{CC} = 26 V,	$R_L \geq$ 10 kΩ	Full range	23	24		
V_{OL}	Low-level output voltage	$R_L \leq$ 10 kΩ		Full range		5	20	mV
A_{VD}	Large-signal differential voltage amplification	V_{CC} = 15 V, V_O = 1 V to 11 V, $R_L \geq$ 2 kΩ		25°C		100		V/mV
				Full range	15			
CMRR	Common-mode rejection ratio	$V_{IC} = V_{ICR}$min		25°C	50	80		dB
k_{SVR}	Supply-voltage rejection ratio ($\Delta V_{CC}/\Delta V_{IO}$)			25°C	50	100		dB
V_{O1}/V_{O2}	Crosstalk attenuation	f = 1 kHz to 20 kHz		25°C		120		dB
I_O	Output current	V_{CC} = 15 V, V_O = 0	V_{ID} = 1 V,	25°C	−20	−30	−60	mA
				Full range	−10			
		V_{CC} = 15 V, V_O = 15 V	V_{ID} = −1 V,	25°C	10	20		
				Full range	5			
		V_{ID} = −1 V,	V_O = 200 mV	25°C		30		µA
I_{OS}	Short-circuit output current	V_{CC} at 5 V, GND at −5 V	V_O = 0,	25°C		±40	±60	mA
I_{CC}	Supply current (four amplifiers)	V_O = 2.5 V,	No load	Full range		0.7	1.2	mA
		V_{CC} = 26 V V_O = 0.5 V_{CC},	No load	Full range		1.4	3	

† All characteristics are measured under open-loop conditions with zero common-mode input voltage, unless otherwise specified.
‡ Full range is −40°C to 125°C.
§ All typical values are at T_A = 25°C.

electrical characteristics at specified free-air temperature, V_{CC} = 5 V (unless otherwise noted) (continued)

PARAMETER		TEST CONDITIONS†		T_A‡	LM2902KV-Q1			UNIT
					MIN	TYP§	MAX	
V_{IO}	Input offset voltage	V_{CC} = 5 V to 32 V, $V_{IC} = V_{ICR}$min, V_O = 1.4 V		25°C		3	7	mV
				Full range			10	
$\Delta V_{IO}/\Delta T$	Temperature drift	R_S = 0 Ω		Full range		7		µV/°C
I_{IO}	Input offset current	V_O = 1.4 V		25°C		2	50	nA
				Full range			150	
$\Delta I_{IO}/\Delta T$	Temperature drift			Full range		10		pA/°C
I_{IB}	Input bias current	V_O = 1.4 V		25°C		−20	−250	nA
				Full range			−500	
V_{ICR}	Common-mode input voltage range	V_{CC} = 5 V to 32 V		25°C	0 to V_{CC} − 1.5			V
				Full range	0 to V_{CC} − 2			
V_{OH}	High-level output voltage	R_L = 10 kΩ		25°C	V_{CC} − 1.5			V
		V_{CC} = 32 V,	R_L = 2 kΩ	Full range	26			
		V_{CC} = 32 V,	$R_L \geq$ 10 kΩ	Full range	27	24		
V_{OL}	Low-level output voltage	$R_L \leq$ 10 kΩ		Full range		5	20	mV
A_{VD}	Large-signal differential voltage amplification	V_{CC} = 15 V, V_O = 1 V to 11 V, $R_L \geq$ 2 kΩ		25°C	25	100		V/mV
				Full range	15			
	Amplifier-to-amplifier coupling¶	f = 1 kHz to 20 kHz, input referred		25°C		120		dB
CMRR	Common-mode rejection ratio	$V_{IC} = V_{ICR}$min		25°C	60	80		dB
k_{SVR}	Supply-voltage rejection ratio ($\Delta V_{CC}/\Delta V_{IO}$)			25°C	60	100		dB
V_{O1}/V_{O2}	Crosstalk attenuation	f = 1 kHz to 20 kHz		25°C		120		dB
I_O	Output current	V_{CC} = 15 V_O = 0	V_{ID} = 1 V,	25°C	−20	−30	−60	mA
				Full range	−10			
		V_{CC} = 15 V_O = 15 V	V_{ID} = −1 V,	25°C	10	20		
				Full range	5			
		V_{ID} = −1 V,	V_O = 200 mV	25°C	12	40		µA
I_{OS}	Short-circuit output current	V_{CC} at 5 V, GND at −5 V	V_O = 0,	25°C		±40	±60	mA
I_{CC}	Supply current (four amplifiers)	V_O = 2.5 V,	No load	Full range		0.7	1.2	mA
		V_{CC} = 32 V V_O = 0.5 V_{CC},	No load	Full range		1.4	3	

† All characteristics are measured under open-loop conditions with zero common-mode input voltage, unless otherwise specified.
‡ Full range is −40°C to 125°C.
§ All typical values are at T_A = 25°C.
¶ Due to proximity of external components, ensure that coupling is not originating via stray capacitance between these external parts. Typically, this can be detected, as this type of coupling increases at higher frequencies.

operating conditions, V_{CC} = ±15 V, T_A = 25°C

PARAMETER		TEST CONDITIONS	TYP	UNIT
SR	Slew rate at unity gain	R_L = 1 MΩ, C_L = 30 pF, V_I = ±10 V (see Figure 1)	0.5	V/μs
B_1	Unity-gain bandwidth	R_L = 1 MΩ, C_L = 20 pF (see Figure 1)	1.2	MHz
V_n	Equivalent input noise voltage	R_S = 100 Ω, V_I = 0 V, f = 1 kHz (see Figure 2)	35	nV/√Hz

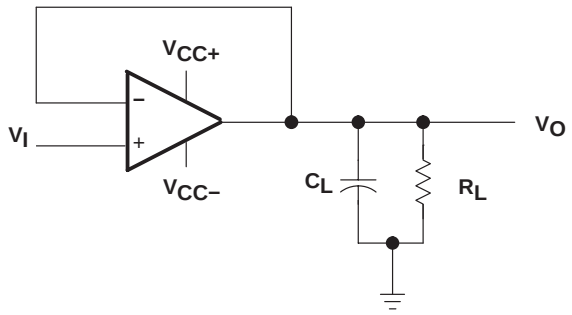


Figure 1. Unity-Gain Amplifier

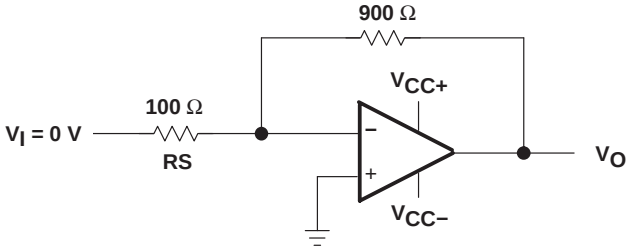


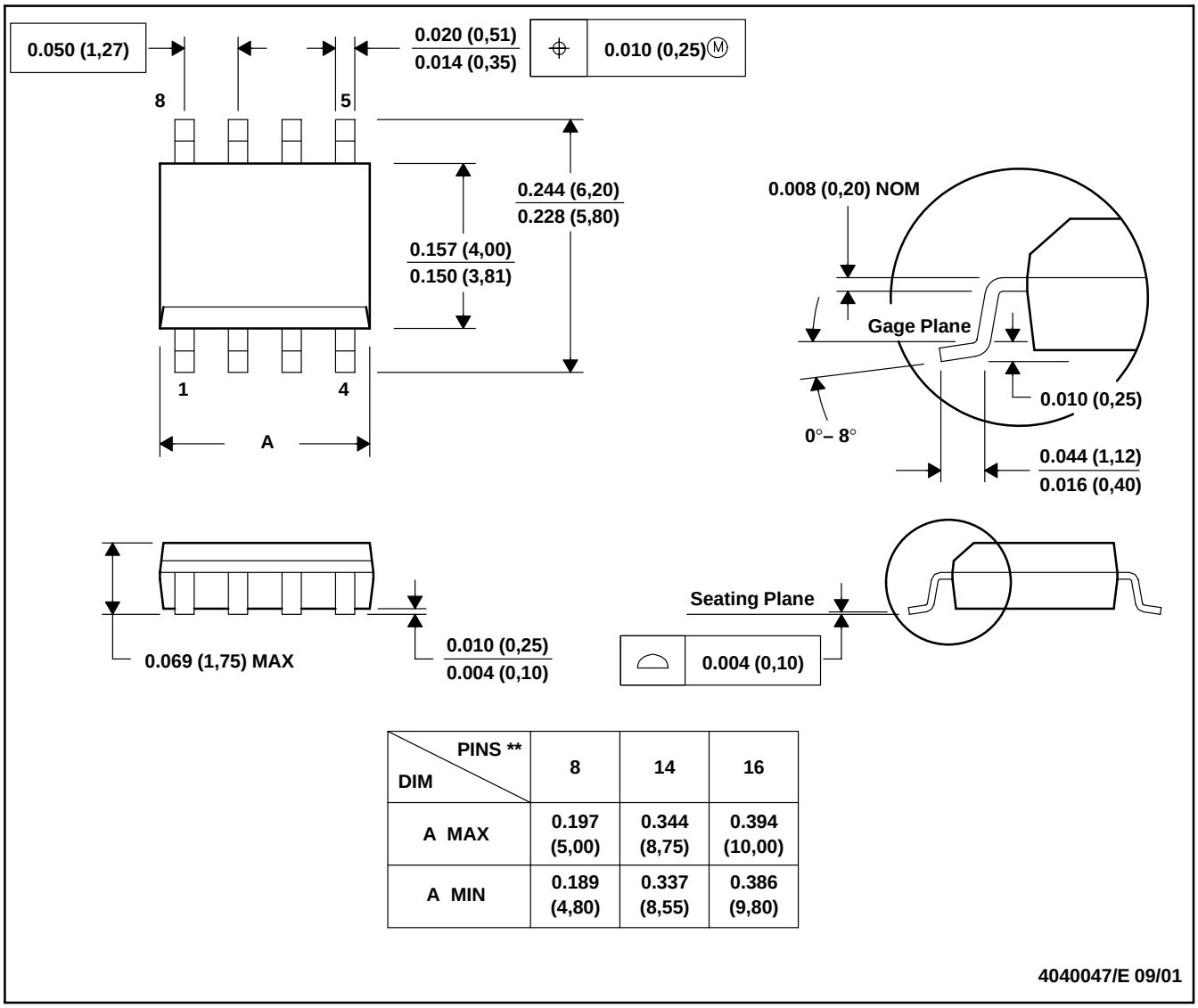
Figure 2. Noise-Test Circuit

MSOI002B – JANUARY 1995 – REVISED SEPTEMBER 2001

D (R-PDSO-G**)

PLASTIC SMALL-OUTLINE PACKAGE

8 PINS SHOWN



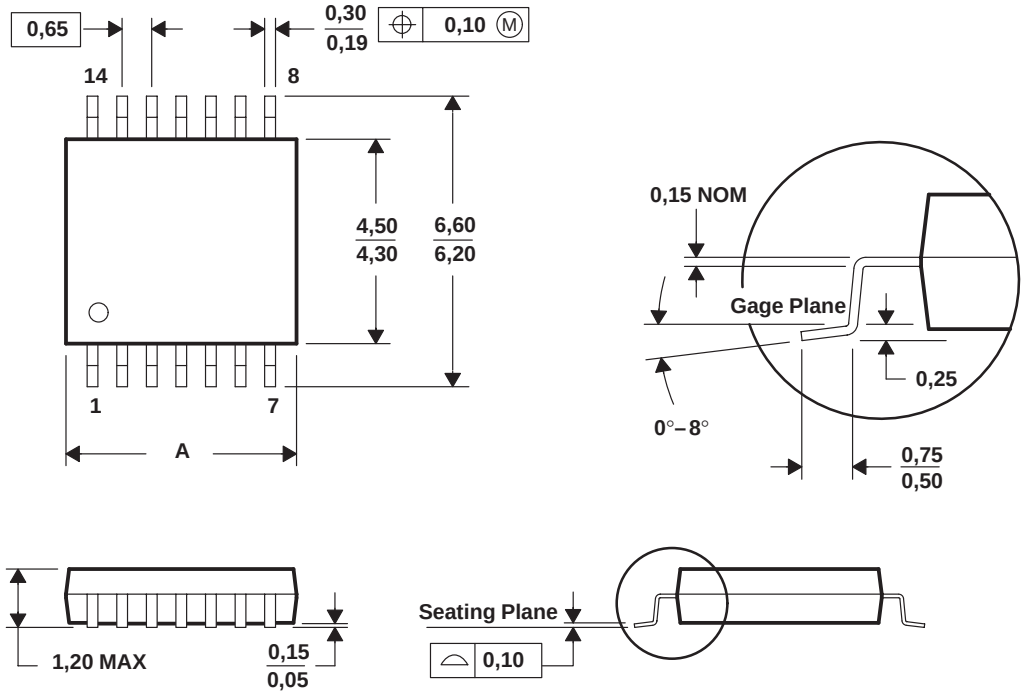
DIM \ PINS **	8	14	16
A MAX	0.197 (5,00)	0.344 (8,75)	0.394 (10,00)
A MIN	0.189 (4,80)	0.337 (8,55)	0.386 (9,80)

4040047/E 09/01

NOTES: A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion, not to exceed 0.006 (0,15).
D. Falls within JEDEC MS-012

PW (R-PDSO-G)** **PLASTIC SMALL-OUTLINE PACKAGE**

14 PINS SHOWN



DIM \ PINS **	8	14	16	20	24	28
A MAX	3,10	5,10	5,10	6,60	7,90	9,80
A MIN	2,90	4,90	4,90	6,40	7,70	9,60

4040064/F 01/97

NOTES: A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
D. Falls within JEDEC MO-153

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