RENESAS

HSM107S

Silicon Schottky Barrier Diode for System Protection

REJ03G0173-0700Z (Previous: ADE-208-058F) Rev.7.00 Jan.28.2004

Features

- Low V_F and high efficiency.
- HSM107S which is interconnected in series configuration is designed for protection from not only external excessive voltage but also miss-operation on electric systems.
- MPAK Package is suitable for high density surface mounting and high speed assembly.

Type No. Laser Mark Package Code HSM107S C5 MPAK Pin Arrangement I. Cathode 2 2. Anode 1 S. Cathode 1 Anode 2

Rev.7.00, Jan.28.2004, page 1 of 5

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Reverse voltage	V _R	8	V
Peak forward current	I _{FM}	0.1	А
Non-Repetitive peak forward surge current	I _{FSM} * ¹	0.5	А
Average forward current	l ₀ * ²	50	mA
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Electrical Characteristics *¹

Notes: 1. Rectangular wave, 10 ms 2. Per one device Electrical Characteristics * ¹ (Ta = 25°C)				menon	
ltem	Symbol	Min	Typ Max l	Jnit Test Condition	
Reverse voltage	VR	8 -	- (-)	$I_{\rm R} = 1.0 \rm mA$	
Reverse current	I _R	-	- 30 µ	$V_R = 5 V$	
Forward voltage	V _F	-0	– 0.3 V	/ I _F = 10 mA	
ESD Capability * ²	-	100 -		 C = 200 pF, Both forward and reverse direction 1 pulse 	

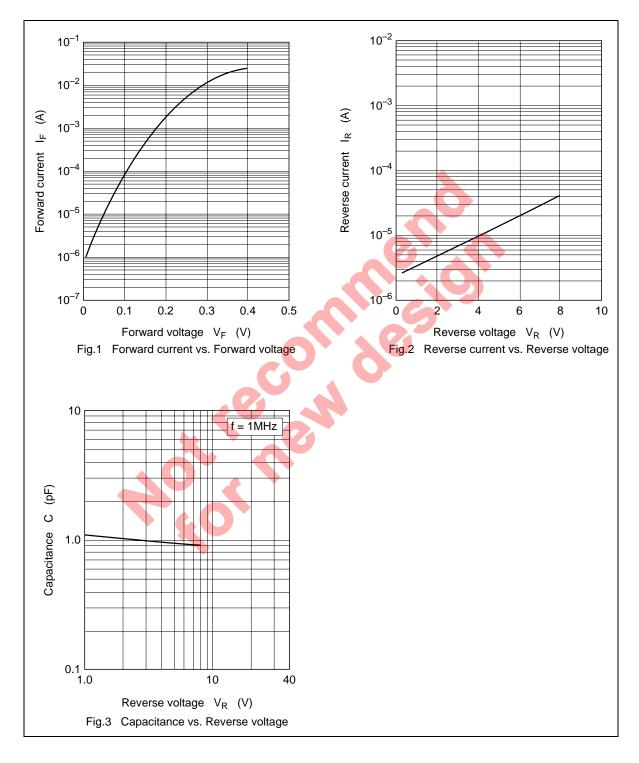
Notes: 1. Per one device

2. Failure Criterion ; $I_R \ge 60 \ \mu A$ at $V_R = 5 \ V$



HSM107S

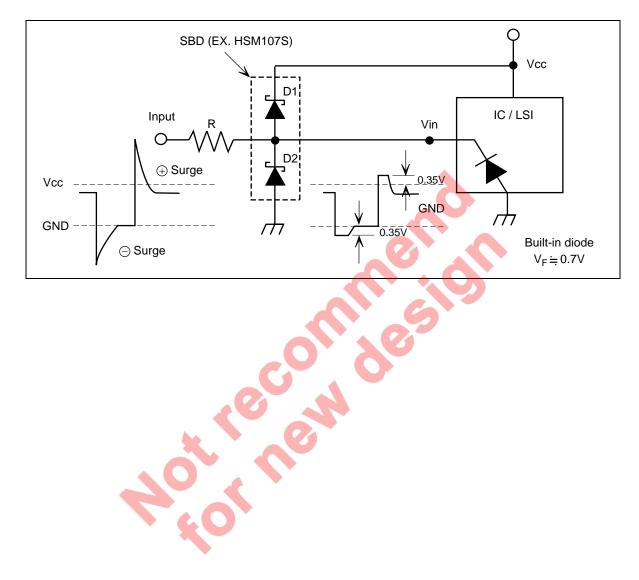
Main Characteristic





HSM107S

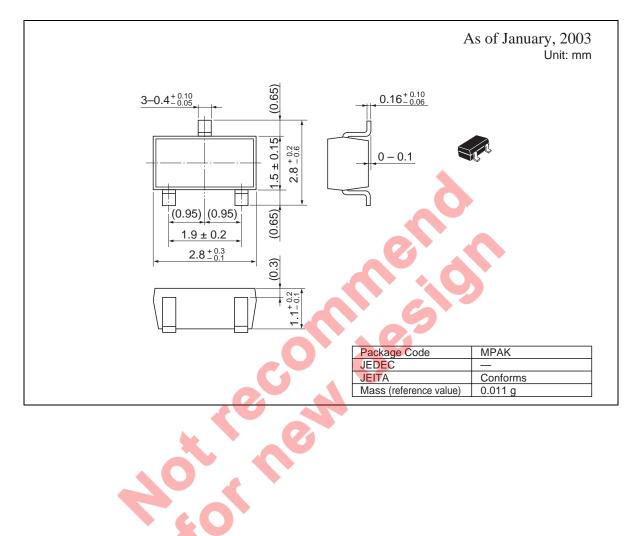
Example of application circuite





HSM107S

Package Dimensions





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April 1st, 2010 Renesas Electronics Corporation

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