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Kind regards,

Team Nexperia



2PB709ARL; 2PB709ASL 45 V, 100 mA PNP general-purpose transistors Rev. 01 – 12 November 2008 Pro

Product data sheet

1. Product profile

1.1 General description

PNP general-purpose transistors in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

Table	1.	Product	overview
100010			010111011

Type number ^[1]	Package	Package		
	NXP	JEDEC		
2PB709ARL	SOT23	TO-236AB	2PD601ARL	
2PB709ASL			2PD601ASL	
2PB709ARL/DG	SOT23	TO-236AB	2PD601ARL/DG	
2PB709ASL/DG			2PD601ASL/DG	

[1] /DG: halogen-free

1.2 Features

- General-purpose transistors
- Two current gain selections
- AEC-Q101 qualified
- Small SMD plastic package

1.3 Applications

General-purpose switching and amplification

1.4 Quick reference data

Table 2.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	-45	V
I _C	collector current		-	-	-100	mA
h _{FE}	DC current gain	$V_{CE} = -10 \text{ V};$ $I_C = -2 \text{ mA}$				
	h _{FE} group R		210	-	340	
	h _{FE} group S		290	-	460	



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2. Pinning information

Table 3.	Pinning		
Pin	Description	Simplified outline G	raphic symbol
1	base		_
2	emitter		3
3	collector		
			sym013

3. Ordering information

Type number ^[1]	Package	Package				
	Name	Description	Version			
2PB709ARL	-	plastic surface-mounted package; 3 leads	SOT23			
2PB709ASL						
2PB709ARL/DG						
2PB709ASL/DG						

4. Marking

Table 5. Marking codes	
Type number	Marking code ^[1]
2PB709ARL	SN*
2PB709ASL	SL*
2PB709ARL/DG	SS*
2PB709ASL/DG	SZ*

[1] * = -: made in Hong Kong

* = p: made in Hong Kong

* = t: made in Malaysia

* = W: made in China

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5. Limiting values

Table 6.Limiting valuesIn accordance with the Absolute Maximum Rating System (IEC 60134).							
Symbol	Parameter	Conditions	Min	Max	Unit		
V _{CBO}	collector-base voltage	open emitter	-	-45	V		
V _{CEO}	collector-emitter voltage	open base	-	-45	V		
V_{EBO}	emitter-base voltage	open collector	-	-6	V		
I _C	collector current		-	-100	mA		
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-200	mA		
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms	-	-100	mA		
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	<u>[1]</u> -	250	mW		
Tj	junction temperature		-	150	°C		
T _{amb}	ambient temperature		-55	+150	°C		
T _{stg}	storage temperature		-65	+150	°C		

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

6. Thermal characteristics

Table 7.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	<u>[1]</u> _	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

7. Characteristics

Table 8. Characteristics

 $T_{amb} = 25 \circ C$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	$V_{CB} = -45 \text{ V}; \text{ I}_{E} = 0 \text{ A}$	-	-	-10	nA
		$\label{eq:V_CB} \begin{array}{l} V_{CB} = -45 \ V; \ I_{E} = 0 \ A; \\ T_{j} = 150 \ ^{\circ}C \end{array}$	-	-	-5	μΑ
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_C = 0 \text{ A}$	-	-	-10	nA
h _{FE}	DC current gain	V_{CE} = -10 V; I_C = -2 mA				
	h _{FE} group R		210	-	340	
	h _{FE} group S		290	-	460	
V _{CEsat}	collector-emitter saturation voltage	$I_{C} = -100 \text{ mA};$ $I_{B} = -10 \text{ mA}$	<u>[1]</u> _	-	-500	mV

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
f _T	transition frequency	$V_{CE} = -10 \text{ V}; I_{C} = -1 \text{ mA};$ f = 100 MHz				
	h _{FE} group R		70	-	-	MHz
	h _{FE} group S		80	-	-	MHz
C _c	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = i_e = 0 \text{ A};$ f = 1 MHz	-	-	5	pF

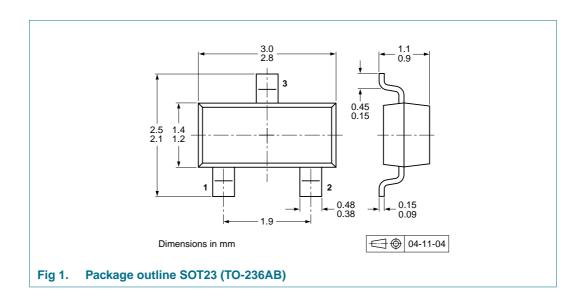
[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.

8. Test information

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline



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10. Packing information

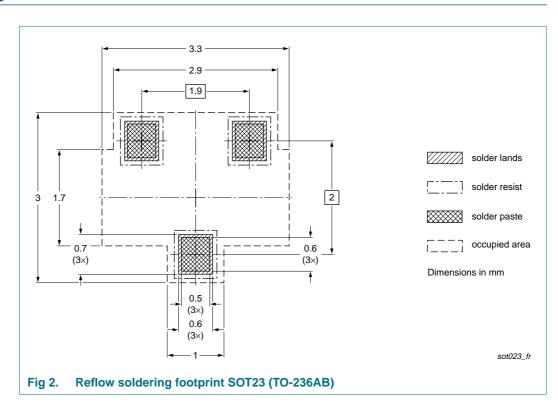
Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number ^[2]	Package	Package Description		Packing quantity		
			3000	10000		
2PB709ARL	SOT23	4 mm pitch, 8 mm tape and reel	-215	-235		
2PB709ASL						
2PB709ARL/DG						
2PB709ASL/DG						

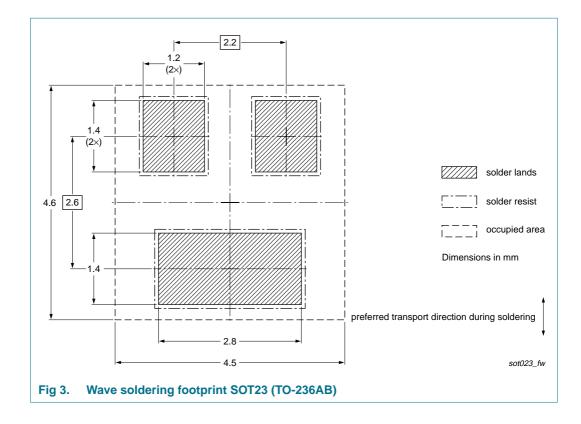
- [1] For further information and the availability of packing methods, see <u>Section 14</u>.
- [2] /DG: halogen-free

11. Soldering



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12. Revision history

Table 10. Revision hist	ory			
Document ID	Release date	Data sheet status	Change notice	Supersedes
2PB709AXL_1	20081112	Product data sheet	-	-

45 V, 100 mA PNP general-purpose transistors

13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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NXP Semiconductors

2PB709ARL; 2PB709ASL

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