

## **Marketing Bulletin**

DATE:	April 20 <sup>th</sup> , 2010

- TO: All Sales Personnel
- FROM: Isaac Gonzalez
- **RE:** Product Termination

To all concerned parties,

This bulletin is to notify all customers of the discontinuation of the following Ecliptek series effective April 20<sup>th</sup>, 2010:

Series EC13	Description RoHS Compliant (Pb-free) 3.3V 14 Pin DIP Metal Thru-Hole LVCMOS/TTL Oscillator	Recommended Replacement
EC13HS	RoHS Compliant (Pb-free) 3.3V 8 Pin DIP Metal Thru-Hole LVCMOS/TTL Oscillator	EH13HS

In compliance with our End of Life (EOL) policy, this will serve as advanced notice of product termination. New orders will not be accepted after September 1<sup>st</sup>, 2010, with delivery to conclude by December 1<sup>st</sup>, 2010.

If there are any questions pertaining to this bulletin, please feel free to contact me. Thank you again for your cooperation.

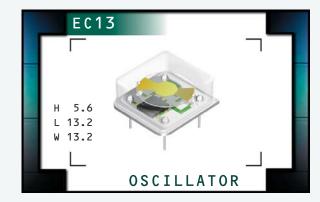
Best Regards,

Isaac Gonzalez Configuration Manager Ecliptek Corporation

## **EC13 Series**

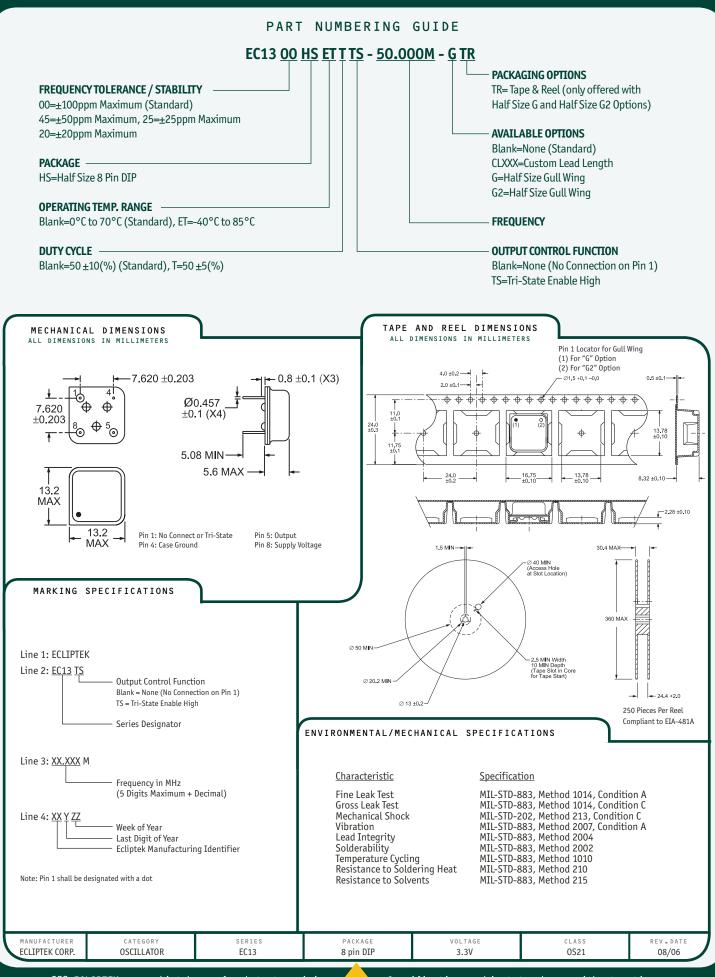
- RoHS Compliant (Pb-free)
- LVCMOS/TTL output
- 3.3V supply voltage
- 8 pin DIP package
- Stability to ±20ppm
- Custom lead length, gull wing options available





## ELECTRICAL SPECIFICATIONS

Frequency Range (MHz)		0.250MHz to 125.000MHz
Operating Temperature Range	0°C to 70°C	
		-40°C to 85°C
Storage Temperature Range		-55°C to 125°C
Supply Voltage (V <sub>DD</sub> )		$3.3V_{DC}\pm0.3V_{DC}$
Input Current	0.250MHz to 24.000MHz	10mA Maximum
	24.001MHz to 70.000MHz	25mA Maximum
	70.001MHz to 125.000MHz	45mA Maximum
Frequency Tolerance / Stability	Inclusive of all conditions: Calibration Tolerance at 25°C,	±100ppm, ±50ppm, ±25ppm, or
	Frequency Stability over the Operating Temperature Range,	±20ppm Maximum (0°C to 70°C Only)
	Supply Voltage Change, Output Load Change, First Year	
	Aging at 25°C, Shock, and Vibration	
Output Voltage Logic High (V <sub>он</sub> )	w/ TTL Load	2.4V <sub>DC</sub> Minimum
	w/ HCMOS Load	2.7V <sub>DC</sub> Minimum
Output Voltage Logic Low (V <sub>0L</sub> )	w/ TTL Load	0.4V <sub>DC</sub> Maximum
	w/ LVCMOS Load	0.5V <sub>DC</sub> Maximum
Rise Time / Fall Time	10% to 90% of Waveform w/LVCMOS Load or	10 nSeconds Max. ≤ 24.000MHz
	$0.4V_{\text{DC}}$ to $2.4V_{\text{DC}}$ w/TTL Load	10 nSeconds Max. ≤ 24.000MHz
	10% to 90% of Waveform w/LVCMOS Load	6 nSeconds Max. 24.000MHz to 70.000MHz
	10% to 90% of Waveform w/LVCMOS Load	4 nSeconds Max. 70.001MHz to 125.000MHz
Duty Cycle	at 50% of Waveform	50 ±10(%) (Standard) or 50 ±5(%) (Optional
Load Drive Capability	≤24.000MHz	2TTL or 15pF LVCMOS Load
	> 24.000MHz	15pF LVCMOS Load
Tri-State Input Voltage	V <sub>IH</sub> :No Connection	Enables Output
	$V_{IH}:\geq 2.2V_{DC}$	Enables Output
	$V_{IL}:\leq 0.8V_{DC}$	Disables Output: High Impedance
Aging (at 25°C)		±5ppm / year Maximum
		10 mSeconds Maximum
Start Up Time		
Start Up Time Period Jitter: Absolute		±100pSeconds Maximum



800-ECLIPTEK www.ecliptek.com for latest revision

Specifications subject to change without notice.