LM101A/LM201A/LM301A Operational Amplifiers



LM101A/LM201A/LM301A Operational Amplifiers General Description

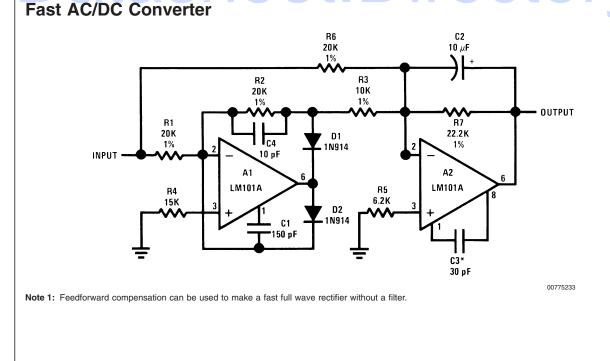
The LM101A series are general purpose operational amplifiers which feature improved performance over industry standards like the LM709. Advanced processing techniques make possible an order of magnitude reduction in input currents, and a redesign of the biasing circuitry reduces the temperature drift of input current. Improved specifications include:

- Offset voltage 3 mV maximum over temperature (LM101A/LM201A)
- Input current 100 nA maximum over temperature (LM101A/LM201A)
- Offset current 20 nA maximum over temperature (LM101A/LM201A)
- Guaranteed drift characteristics
- Offsets guaranteed over entire common mode and supply voltage ranges
- Slew rate of 10V/µs as a summing amplifier
- This amplifier offers many features which make its application nearly foolproof: overload protection on the input

and output, no latch-up when the common mode range is exceeded, and freedom from oscillations and compensation with a single 30 pF capacitor. It has advantages over internally compensated amplifiers in that the frequency compensation can be tailored to the particular application. For example, in low frequency circuits it can be overcompensated for increased stability margin. Or the compensation can be optimized to give more than a factor of ten improvement in high frequency performance for most applications.

In addition, the device provides better accuracy and lower noise in high impedance circuitry. The low input currents also make it particularly well suited for long interval integrators or timers, sample and hold circuits and low frequency waveform generators. Further, replacing circuits where matched transistor pairs buffer the inputs of conventional IC op amps, it can give lower offset voltage and a drift at a lower cost.

The LM101A is guaranteed over a temperature range of -55° C to $+125^{\circ}$ C, the LM201A from -25° C to $+85^{\circ}$ C, and the LM301A from 0°C to $+70^{\circ}$ C.



Absolute Maximum Ratings (Note 2)

Distributors for availability and specifications.

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/

	LM101A/LM201A	LM301A		
Supply Voltage	±22V	±18V		
Differential Input Voltage	±30V	±30V		
Input Voltage (Note 3)	±15V	±15V		
Output Short Circuit Duration (Note 4)	Continuous	Continuous		
Operating Ambient Temp. Range	-55°C to +125°C (LM101A)	0°C to +70°C		
	–25°C to +85°C (LM201A)			
T _J Max				
H-Package	150°C	100°C		
N-Package	150°C	100°C		
J-Package	150°C	100°C		
Power Dissipation at $T_A = 25^{\circ}C$				
H-Package (Still Air)	500 mW	300 mW		
(400 LF/Min Air Flow)	1200 mW	700 mW		
N-Package	900 mW	500 mW		
J-Package	1000 mW	650 mW		
Thermal Resistance (Typical) θ_{iA}				
H-Package (Still Air)	165°C/W	165°C/W		
(400 LF/Min Air Flow)	67°C/W	67°C/W		
N Package	135°C/W	135°C/W		
J-Package	110°C/W	110°CmW		
(Typical) θ _{iC}				
H-Package	25°C/W	25°C/W		
Storage Temperature Range	−65°C to +150°C	–65°C to +150°C		
Lead Temperature (Soldering, 10 sec.)				
Metal Can or Ceramic	300°C	300°C		
Plastic	260°C	260°C		
ESD Tolerance (Note 7)	2000V	2000V		

Electrical Characteristics (Note 5)

 $T_A = T_J$

Parameter	Conditions		LM101A/LM201A			LM301A			Units
			Min	Тур	Max	Min	Тур	Max	
Input Offset Voltage	$T_A = 25^{\circ}C, R_S \le 50 \text{ k}\Omega$			0.7	2.0		2.0	7.5	mV
Input Offset Current	T _A = 25°C			1.5	10		3.0	50	nA
Input Bias Current	T _A = 25°C			30	75		70	250	nA
Input Resistance	$T_A = 25^{\circ}C$		1.5	4.0		0.5	2.0		MΩ
Supply Current	$T_A = 25^{\circ}C$	$V_{\rm S} = \pm 20V$		1.8	3.0				mA
		$V_{\rm S} = \pm 15 V$					1.8	3.0	mA
Large Signal Voltage Gain	$T_{A} = 25^{\circ}C, V_{S} = \pm 15V$	•	50	160		25	160		V/mV
	$V_{OUT} = \pm 10V, R_L \ge 2$	<Ω							
Input Offset Voltage	$R_{S} \le 50 \text{ k}\Omega$				3.0			10	mV
Average Temperature Coefficient	$R_{S} \le 50 \text{ k}\Omega$			3.0	15		6.0	30	µV/°C
of Input Offset Voltage									
Input Offset Current					20			70	nA
Average Temperature Coefficient	$25^{\circ}C \le T_A \le T_{MAX}$			0.01	0.1		0.01	0.3	nA/°C
of Input Offset Current	$T_{MIN} \le T_A \le 25^{\circ}C$			0.02	0.2		0.02	0.6	nA/°C
Input Bias Current					0.1			0.3	μA

Electrical Characteristics (Note 5) (Continued)

Parameter	Conditions		LM101A/LM201A			LM301A			Units
				Тур	Max	Min	Тур	Max	
Supply Current	$T_A = T_{MAX}, V_S = \pm 20V$			1.2	2.5				mA
Large Signal Voltage Gain	$V_{S} = \pm 15V, V_{OUT} = \pm 10V$ $R_{L} \ge 2k$		25			15			V/mV
Output Voltage Swing $V_{S} = \pm 15V$	$V_{\rm S} = \pm 15 V$	$R_L = 10 \ k\Omega$	±12	±14		±12	±14		V
		$R_L = 2 k\Omega$	±10	±13		±10	±13		V
Input Voltage Range	$V_{S} = \pm 20V$		±15						V
$V_{S} = \pm 15V$				+15, –13		±12	+15, -13		V
Common-Mode Rejection Ratio	$R_{S} \le 50 \text{ k}\Omega$		80	96		70	90		dB
Supply Voltage Rejection Ratio	$R_{S} \le 50 \text{ k}\Omega$		80	96		70	96		dB

Note 2: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating ratings indicate for which the device is functional, but do no guarantee specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which guarantee specific limits. This assumes that the device is within the Operating Ratings. Specifications are not guaranteed for parameters where no limit is given, however, the typical value is a good indication of device performance.

Note 3: For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.

Note 4: Continuous short circuit is allowed for case temperatures to 125°C and ambient temperatures to 75°C for LM101A/LM201A, and 70°C and 55°C respectively for LM301A.

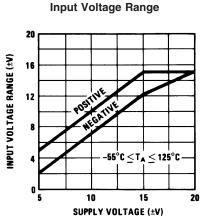
Note 5: Unless otherwise specified, these specifications apply for C1 = 30 pF, $\pm 5V \le V_S \le \pm 20V$ and $-55^{\circ}C \le T_A \le +125^{\circ}C$ (LM101A), $\pm 5V \le V_S \le \pm 20V$ and $-25^{\circ}C \le T_A \le +85^{\circ}C$ (LM201A), $\pm 5V \le V_S \le \pm 15V$ and $0^{\circ}C \le T_A \le +70^{\circ}C$ (LM301A).

Note 6: Refer to RETS101AX for LM101A military specifications and RETS101X for LM101 military specifications.

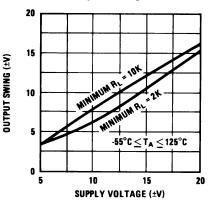
Note 7: Human body model, 100 pF discharged through 1.5 k Ω .

Typical Performance Characteristics

LM101A/LM201A

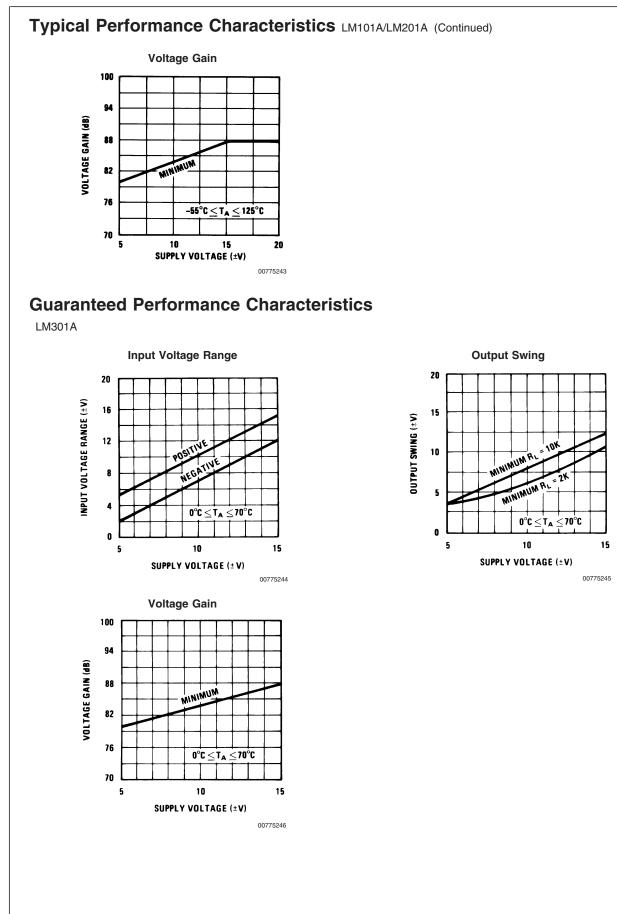


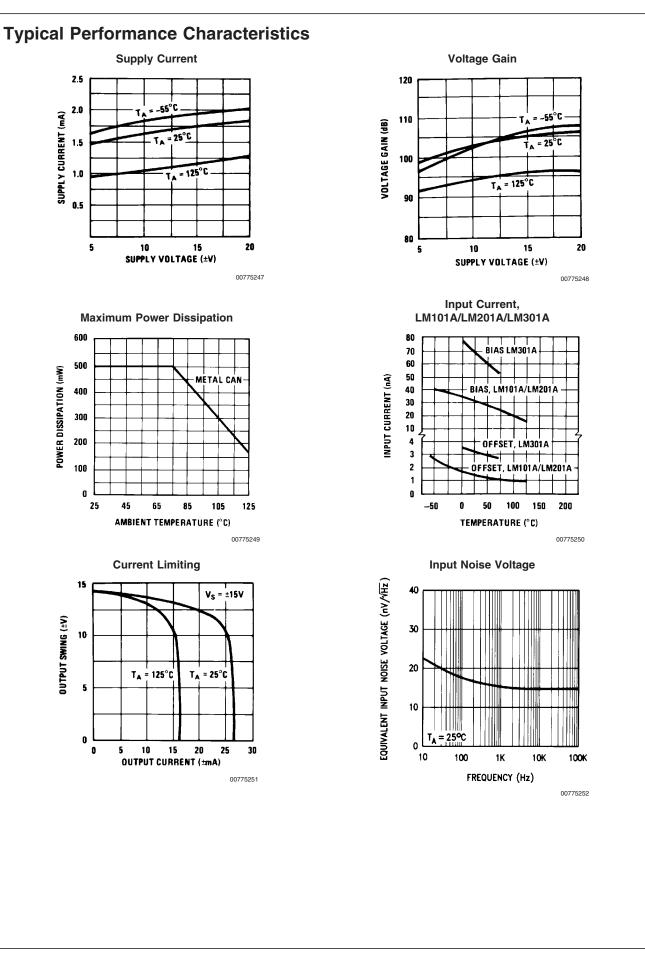
00775241

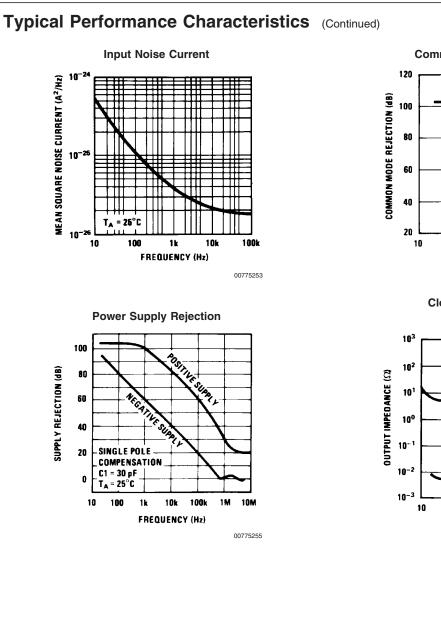


Output Swing

00775242



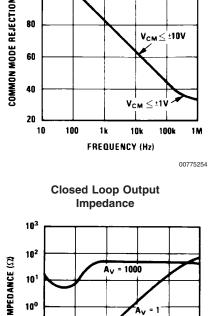




Common Mode Rejection

Rs= 1 **K**Ω

T_A = 25°C.



00775256

1M

100k

SINGLE POLE

C1 = 30 pF T_A = 25°C

10k

FREQUENCY (Hz)

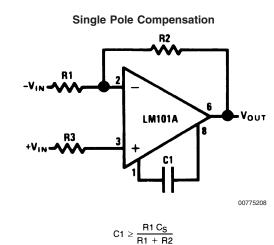
100

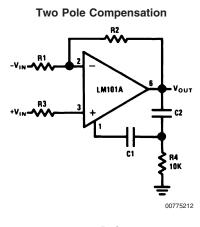
1k

lout = ±5 mA

COMPENSATION

Typical Performance Characteristics for Various Compensation Circuits (Note 9)

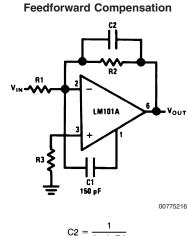




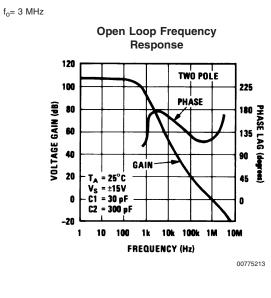




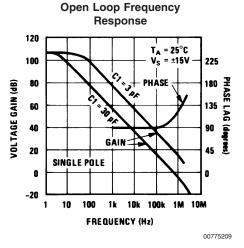
C_S= 30 pF C2 = 10 C1



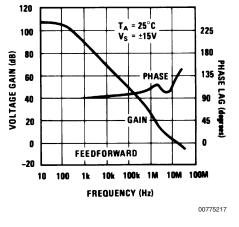


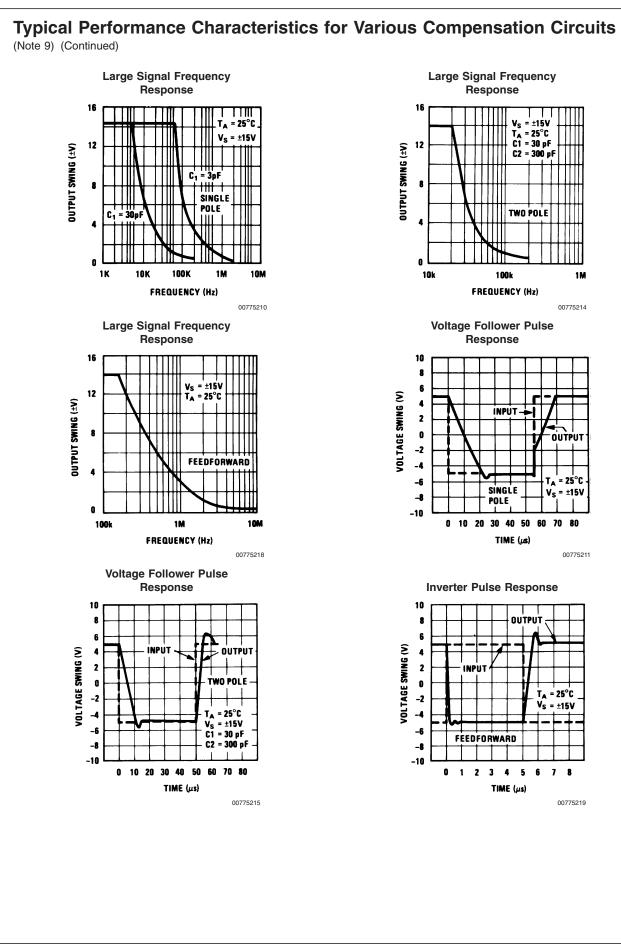


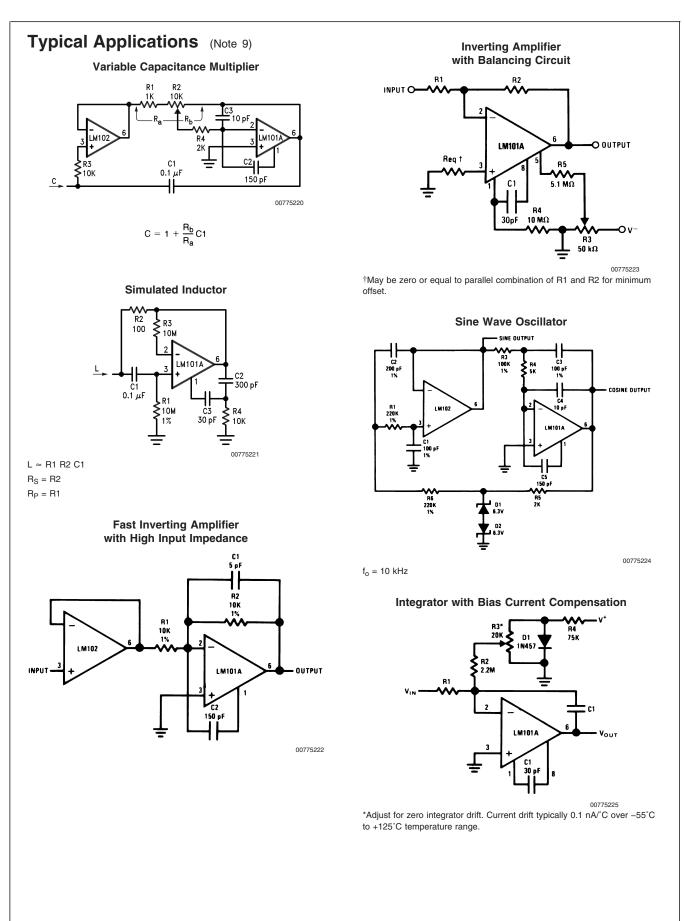


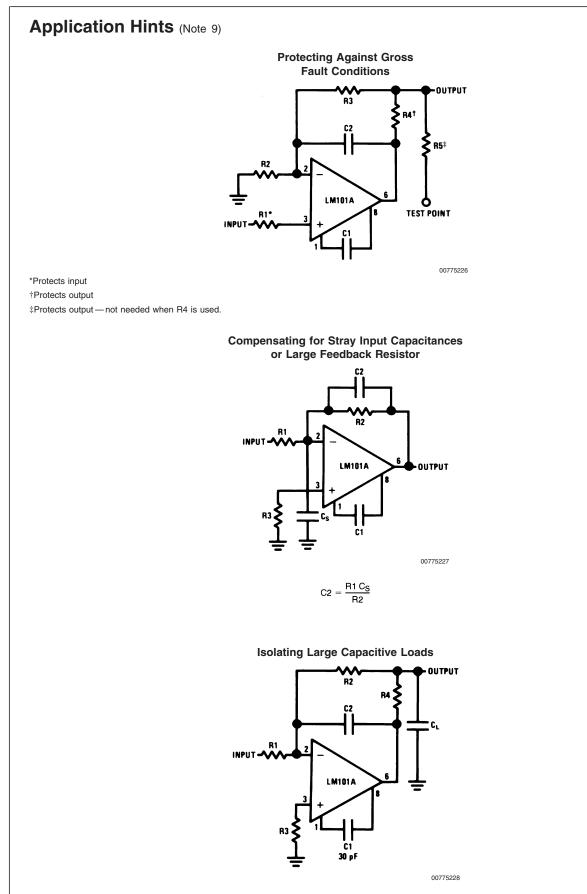












Although the LM101A is designed for trouble free operation, experience has indicated that it is wise to observe certain

precautions given below to protect the devices from abnormal operating conditions. It might be pointed out that the advice given here is applicable to practically any IC op amp, although the exact reason why may differ with different devices.

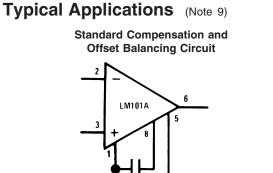
When driving either input from a low-impedance source, a limiting resistor should be placed in series with the input lead to limit the peak instantaneous output current of the source to something less than 100 mA. This is especially important when the inputs go outside a piece of equipment where they could accidentally be connected to high voltage sources. Large capacitors on the input (greater than 0.1 μ F) should be treated as a low source impedance and isolated with a resistor. Low impedance sources do not cause a problem unless their output voltage exceeds the supply voltage. However, the supplies go to zero when they are turned off, so the isolation is usually needed.

The output circuitry is protected against damage from shorts to ground. However, when the amplifier output is connected to a test point, it should be isolated by a limiting resistor, as test points frequently get shorted to bad places. Further, when the amplifer drives a load external to the equipment, it is also advisable to use some sort of limiting resistance to preclude mishaps.

Precautions should be taken to insure that the power supfor the integrated circuit never become plies reversed-even under transient conditions. With reverse voltages greater than 1V, the IC will conduct excessive current, fusing internal aluminum interconnects. If there is a possibility of this happening, clamp diodes with a high peak current rating should be installed on the supply lines. Reversal of the voltage between V⁺ and V⁻ will always cause a problem, although reversals with respect to ground may also give difficulties in many circuits.

The minimum values given for the frequency compensation capacitor are stable only for source resistances less than 10 k Ω , stray capacitances on the summing junction less than 5 pF and capacitive loads smaller than 100 pF. If any of these conditions are not met, it becomes necessary to overcompensate the amplifier with a larger compensation capacitor. Alternately, lead capacitors can be used in the feedback network to negate the effect of stray capacitance and large feedback resistors or an RC network can be added to isolate capacitive loads.

Although the LM101A is relatively unaffected by supply bypassing, this cannot be ignored altogether. Generally it is necessary to bypass the supplies to ground at least once on every circuit card, and more bypass points may be required if more than five amplifiers are used. When feed-forward compensation is employed, however, it is advisable to bypass the supply leads of each amplifier with low inductance capacitors because of the higher frequencies involved.



C1

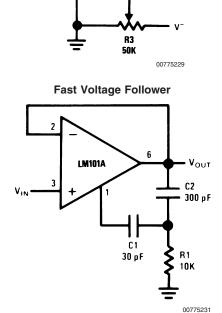
30 p F

R2 ξ

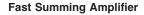
5.1M

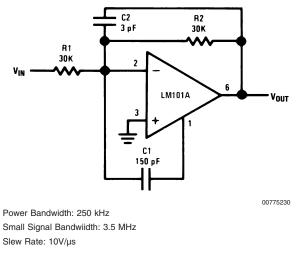
R1

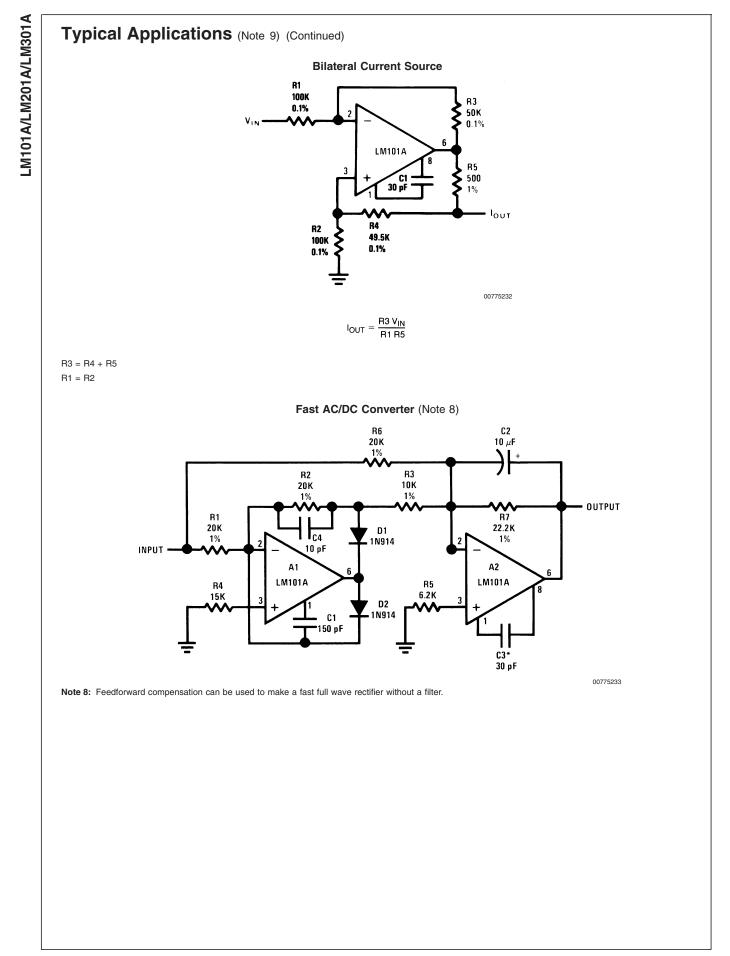
10M

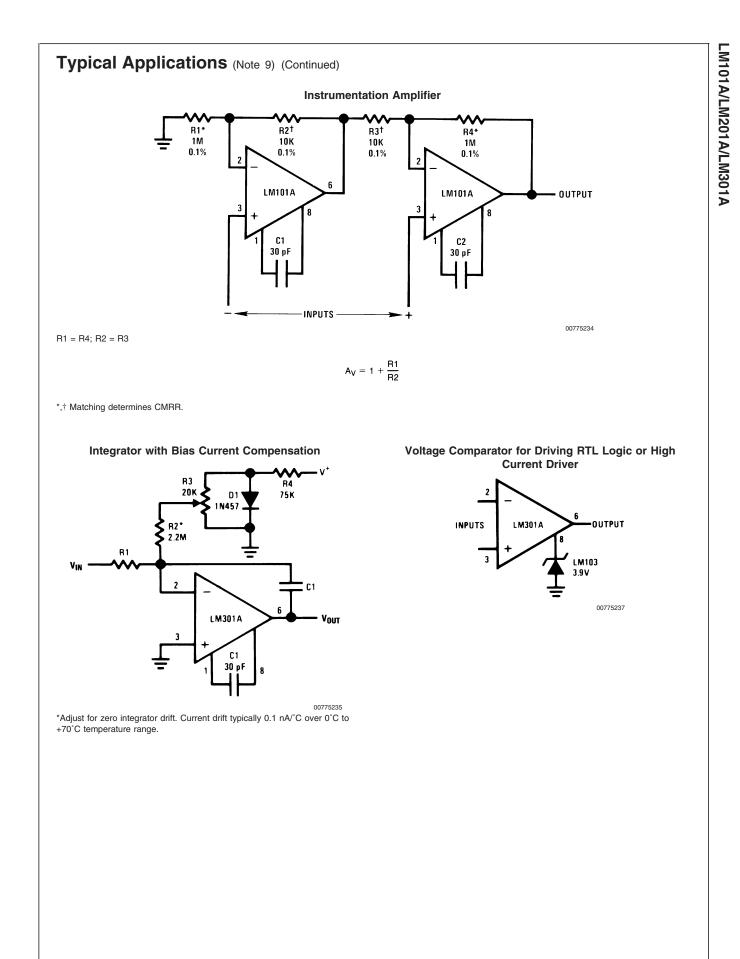


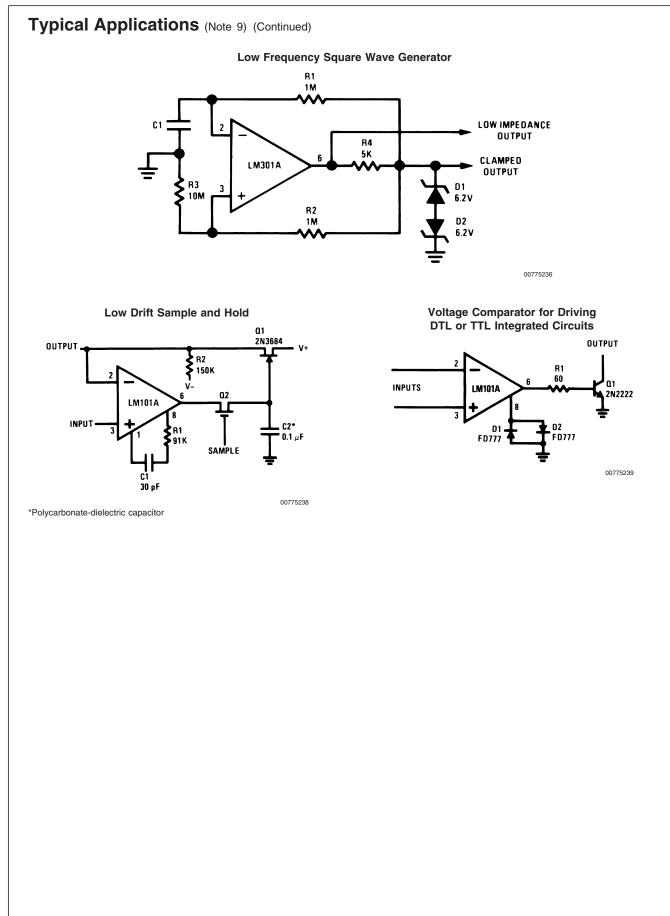
Power Bandwidth: 15 kHz Slew Rate: 1V/µs

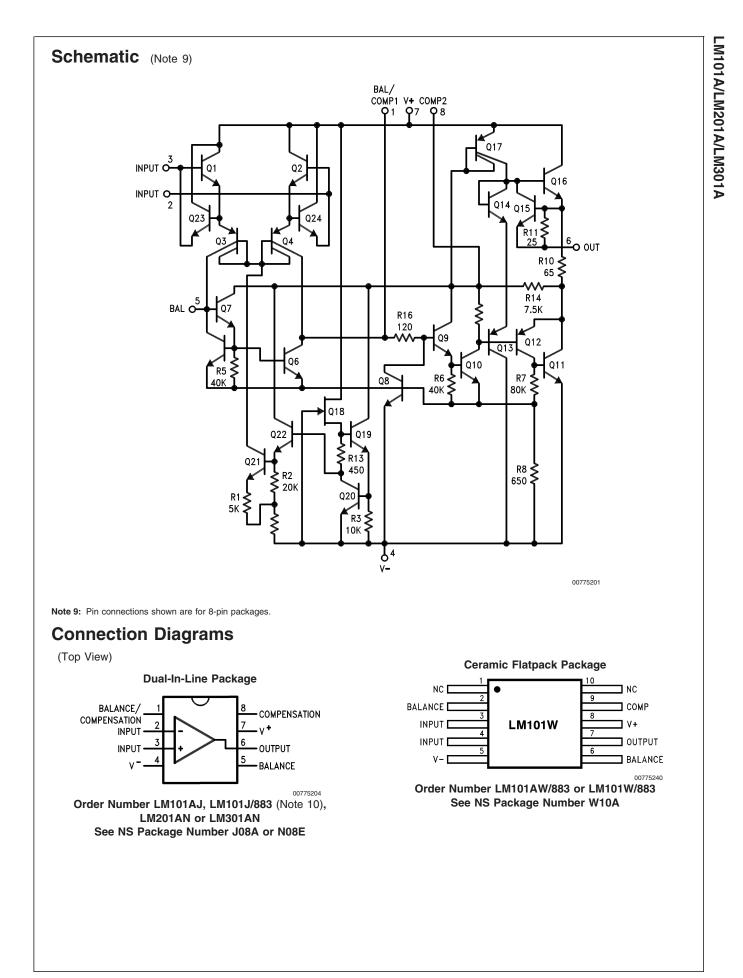


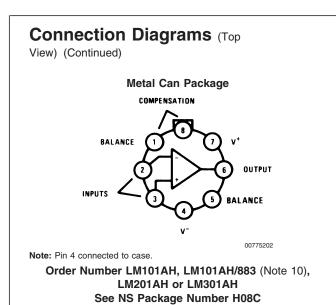




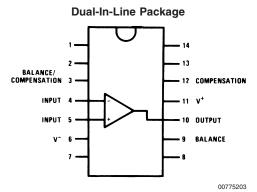






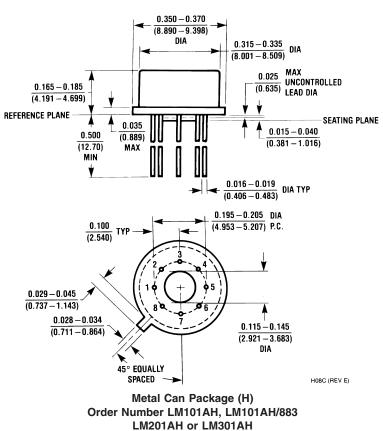


Note 10: Available per JM38510/10103.



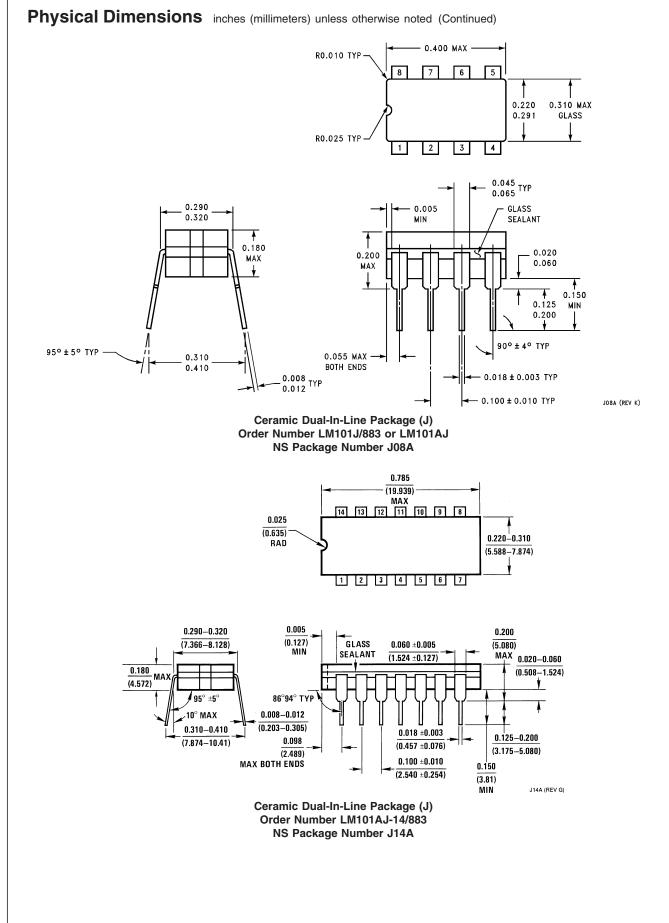
Order Number LM101AJ-14/883 (Note 10) See NS Package Number J14A

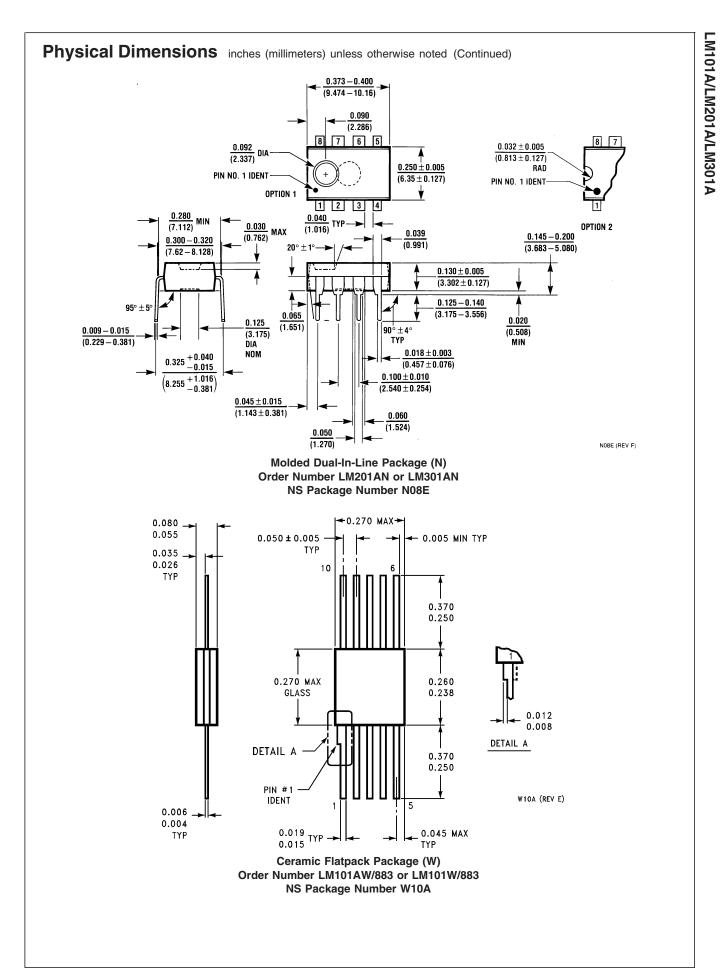
Physical Dimensions inches (millimeters) unless otherwise noted



NS Package Number H08C







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Notes