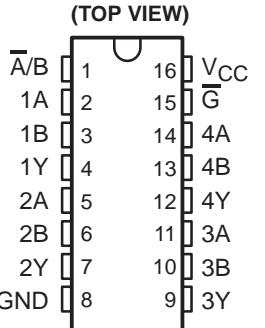


**SN54HC257, SN54HC258, SN74HC257, SN74HC258**  
**QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MUXES**  
**WITH 3-STATE OUTPUTS**

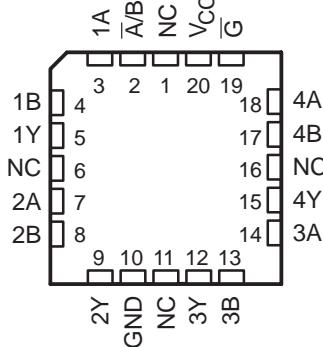
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- Wide Operating Voltage Range of 2 V to 6 V
- High-Current Inverting Outputs Drive Up To 15 LSTTL Loads
- Low Power Consumption, 80- $\mu$ A Max  $I_{CC}$
- 'HC257 . . . Typical  $t_{pd} = 9$  ns
- 'HC258 . . . Typical  $t_{pd} = 12$  ns
- $\pm 6$ -mA Output Drive at 5 V
- Low Input Current of 1  $\mu$ A Max
- Provides Bus Interface from Multiple Sources in High-Performance Systems

SN54HC257, SN54HC258 . . . J PACKAGE  
SN74HC257, SN74HC258 . . . D, N, NS, OR PW PACKAGE



SN54HC257, SN54HC258 . . . FK PACKAGE  
(TOP VIEW)



NC – No internal connection

#### description/ordering information

#### ORDERING INFORMATION

TA	PACKAGE <sup>†</sup>	ORDERABLE PART NUMBER	TOP-SIDE MARKING
-40°C to 85°C	PDIP – N	Tube of 25	SN74HC257N SN74HC258N
		Tube of 40	SN74HC257D
	SOIC – D	Reel of 2500	SN74HC257DR
		Reel of 250	SN74HC257DT
		Tube of 40	SN74HC258D
		Reel of 2500	SN74HC258DR
	SOP – NS	Reel of 2000	SN74HC257NSR SN74HC258NSR
		Tube of 90	HC257 HC258
	TSSOP – PW	Reel of 2000	SN74HC257PWR
		Reel of 250	SN74HC257PWT
		Tube of 90	SN74HC258PW
		Reel of 2000	SN74HC258PWR
		Reel of 250	SN74HC258PWT
		Tube of 25	HC257 HC258
-55°C to 125°C	CDIP – J	SNJ54HC257J SNJ54HC258J	SNJ54HC257J SNJ54HC258J
		SNJ54HC257FK SNJ54HC258FK	SNJ54HC257FK SNJ54HC258FK
	LCCC – FK	Tube of 55	

<sup>†</sup> Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at [www.ti.com/sc/package](http://www.ti.com/sc/package).

Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

**TEXAS INSTRUMENTS**

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On products compliant to MIL-PRF-38535, all parameters are tested unless otherwise noted. On all other products, production processing does not necessarily include testing of all parameters.

**SN54HC257, SN54HC258, SN74HC257, SN74HC258**  
**QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MUXES**  
**WITH 3-STATE OUTPUTS**

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**description/ordering information (continued)**

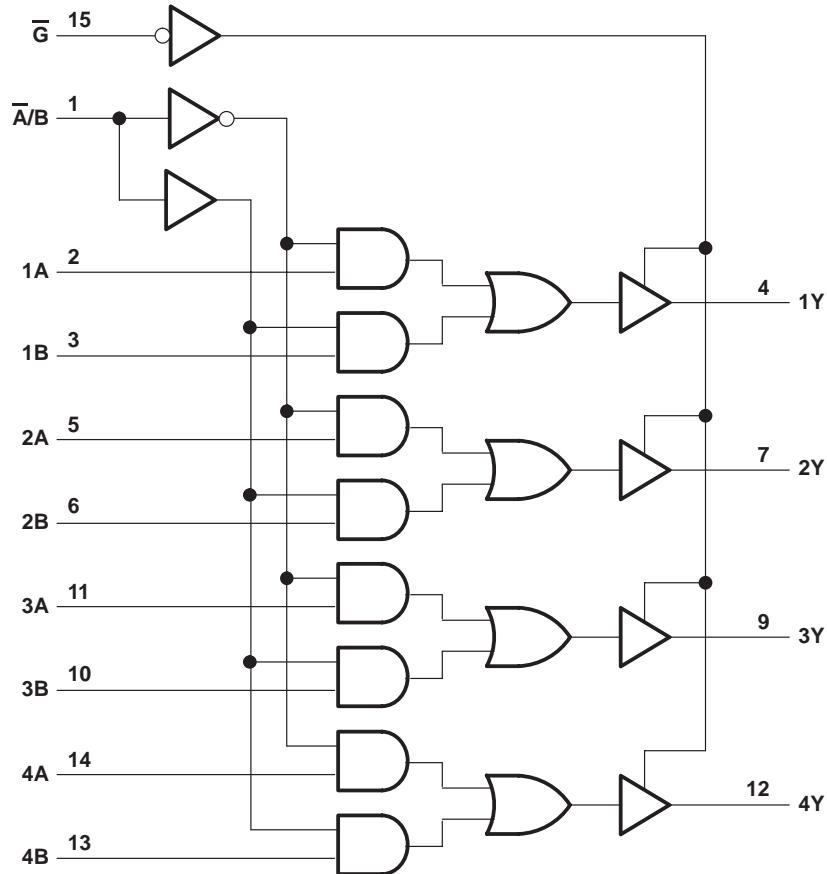
These devices are designed to multiplex signals from 4-bit data sources to 4-output data lines in bus-organized systems. The 3-state outputs do not load the data lines when the output-enable ( $\bar{G}$ ) input is at a high logic level.

To ensure the high-impedance state during power up or power down,  $\bar{G}$  should be tied to  $V_{CC}$  through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

FUNCTION TABLE

$\bar{G}$	INPUTS		OUTPUT Y		
	$\bar{A}/B$	A	B	'HC257	'HC258
H	X	X	X	Z	Z
L	L	L	X	L	H
L	L	H	X	H	L
L	H	X	L	L	H
L	H	X	H	H	L

**'HC257 logic diagram (positive logic)**

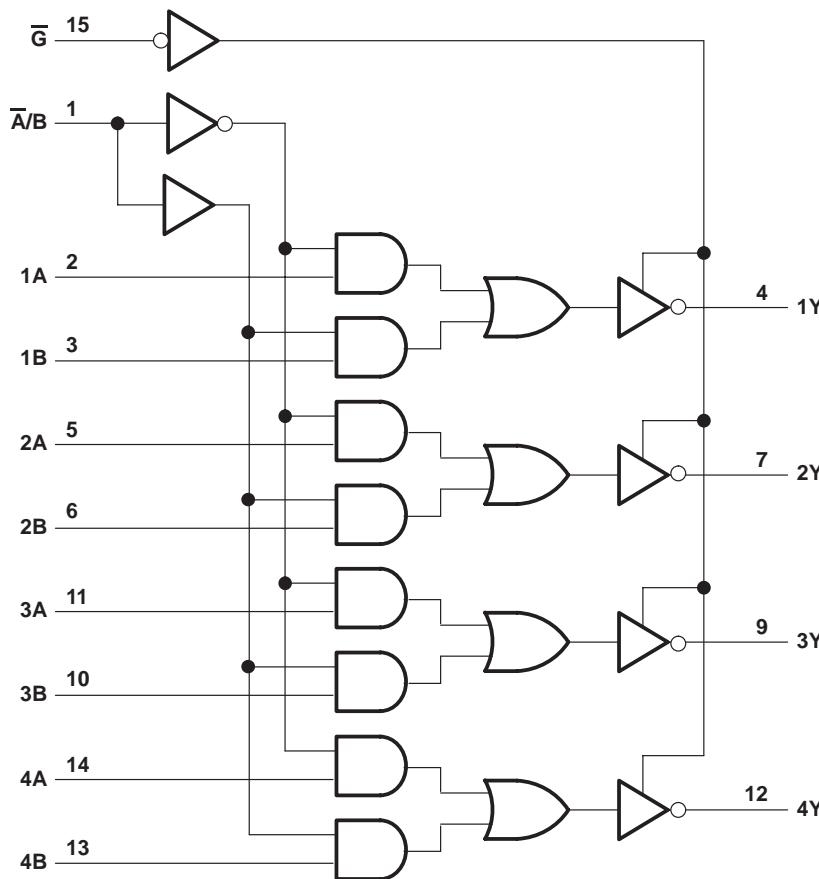


Pin numbers shown are for the D, J, N, NS, and PW packages.

**SN54HC257, SN54HC258, SN74HC257, SN74HC258**  
**QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MUXES**  
**WITH 3-STATE OUTPUTS**

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**'HC258 logic diagram (positive logic)**



Pin numbers shown are for the D, J, N, NS, and PW packages.

**absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>**

Supply voltage range, $V_{CC}$ .....	-0.5 V to 7 V
Input clamp current, $I_{IK}$ ( $V_I < 0$ or $V_I > V_{CC}$ ) .....	$\pm 20$ mA
Output clamp current, $I_{OK}$ ( $V_O < 0$ or $V_O > V_{CC}$ ) .....	$\pm 20$ mA
Continuous output current, $I_O$ ( $V_O = 0$ to $V_{CC}$ ) .....	$\pm 35$ mA
Continuous current through $V_{CC}$ or GND .....	$\pm 70$ mA
Package thermal impedance, $\theta_{JA}$ (see Note 1):	
D package .....	73°C/W
N package .....	67°C/W
NS package .....	64°C/W
PW package .....	108°C/W
Storage temperature range, $T_{stg}$ .....	-65°C to 150°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The package thermal impedance is calculated in accordance with JESD 51-7.

**SN54HC257, SN54HC258, SN74HC257, SN74HC258**  
**QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MUXES**  
**WITH 3-STATE OUTPUTS**

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**recommended operating conditions (see Note 2)**

			SN54HC257, SN54HC258			SN74HC257, SN74HC258			UNIT	
			MIN	NOM	MAX	MIN	NOM	MAX		
V <sub>CC</sub>	Supply voltage			2	5	6	2	5	6	V
V <sub>IH</sub>	High-level input voltage			V <sub>CC</sub> = 2 V	1.5		1.5			V
				V <sub>CC</sub> = 4.5 V	3.15		3.15			
				V <sub>CC</sub> = 6 V	4.2		4.2			
V <sub>IL</sub>	Low-level input voltage			V <sub>CC</sub> = 2 V		0.3		0.5		V
				V <sub>CC</sub> = 4.5 V		0.9		1.35		
				V <sub>CC</sub> = 6 V		1.2		1.8		
V <sub>I</sub>	Input voltage			0	V <sub>CC</sub>	0	V <sub>CC</sub>	0	V <sub>CC</sub>	V
V <sub>O</sub>	Output voltage			0	V <sub>CC</sub>	0	V <sub>CC</sub>	0	V <sub>CC</sub>	V
Δt/Δv	Input transition rise/fall time			V <sub>CC</sub> = 2 V		1000		1000		ns
				V <sub>CC</sub> = 4.5 V		500		500		
				V <sub>CC</sub> = 6 V		400		400		
T <sub>A</sub>	Operating free-air temperature			-55		125	-40		85	°C

NOTE 2: All unused inputs of the device must be held at V<sub>CC</sub> or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.

**electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)**

PARAMETER	TEST CONDITIONS	V <sub>CC</sub>	T <sub>A</sub> = 25°C			SN54HC257, SN54HC258		SN74HC257, SN74HC258	UNIT	
			MIN	TYP	MAX	MIN	MAX	MIN		
V <sub>OH</sub>	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -20 μA	2 V	1.9	1.998	1.9		1.9	V	
			4.5 V	4.4	4.499	4.4		4.4		
			6 V	5.9	5.999	5.9		5.9		
		I <sub>OH</sub> = -6 mA	4.5 V	3.98	4.3	3.7		3.84		
			6 V	5.48	5.8	5.2		5.34		
		I <sub>OL</sub> = 20 μA	2 V	0.002	0.1	0.1		0.1		
			4.5 V	0.001	0.1	0.1		0.1		
			6 V	0.001	0.1	0.1		0.1		
			I <sub>OL</sub> = 6 mA	4.5 V	0.17	0.26	0.4	0.33		
			I <sub>OL</sub> = 7.8 mA	6 V	0.15	0.26	0.4	0.33		
I <sub>I</sub>	V <sub>I</sub> = V <sub>CC</sub> or 0		6 V	±0.1	±100	±1000		±1000	nA	
I <sub>OZ</sub>	V <sub>O</sub> = V <sub>CC</sub> or 0		6 V	±0.01	±0.5	±10		±5	μA	
I <sub>CC</sub>	V <sub>I</sub> = V <sub>CC</sub> or 0, I <sub>O</sub> = 0		6 V		8	160		80	μA	
C <sub>i</sub>		2 V to 6 V		3	10	10		10	pF	

**SN54HC257, SN54HC258, SN74HC257, SN74HC258**  
**QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MUXES**  
**WITH 3-STATE OUTPUTS**

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**switching characteristics over recommended operating free-air temperature range,  $C_L = 50 \text{ pF}$  (unless otherwise noted) (see Figure 1)**

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC}$	$T_A = 25^\circ\text{C}$			SN54HC257		SN74HC257		UNIT
				MIN	TYP	MAX	MIN	MAX	MIN	MAX	
$t_{pd}$	A or B	Any Y	2 V	50	100	150	150	125	125	125	ns
			4.5 V	10	20	30	30	25	25	25	
			6 V	9	17	25	25	21	21	21	
	$\overline{A}/B$	Any Y	2 V	50	100	150	150	125	125	125	
			4.5 V	10	20	30	30	25	25	25	
			6 V	9	17	25	25	21	21	21	
$t_{en}$	$\overline{G}$	Any Y	2 V	75	150	225	225	190	190	190	ns
			4.5 V	15	30	45	45	38	38	38	
			6 V	13	26	38	38	32	32	32	
$t_{dis}$	$\overline{G}$	Any Y	2 V	75	150	225	225	190	190	190	ns
			4.5 V	15	30	45	45	38	38	38	
			6 V	13	26	38	38	32	32	32	
$t_t$		Any Y	2 V	28	60	90	90	75	75	75	ns
			4.5 V	8	12	18	18	15	15	15	
			6 V	6	10	15	15	13	13	13	

**switching characteristics over recommended operating free-air temperature range,  $C_L = 150 \text{ pF}$  (unless otherwise noted) (see Figure 1)**

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC}$	$T_A = 25^\circ\text{C}$			SN54HC257		SN74HC257		UNIT
				MIN	TYP	MAX	MIN	MAX	MIN	MAX	
$t_{pd}$	A or B	Any Y	2 V	75	150	245	245	190	190	190	ns
			4.5 V	15	30	45	45	38	38	38	
			6 V	13	26	38	38	32	32	32	
	$\overline{A}/B$	Any Y	2 V	75	150	245	245	190	190	190	
			4.5 V	15	30	45	45	38	38	38	
			6 V	13	26	38	38	32	32	32	
$t_{en}$	$\overline{G}$	Any Y	2 V	100	200	300	300	250	250	250	ns
			4.5 V	24	40	60	60	50	50	50	
			6 V	18	34	51	51	43	43	43	
$t_t$		Any Y	2 V	45	210	315	315	265	265	265	ns
			4.5 V	17	42	63	63	53	53	53	
			6 V	13	36	53	53	45	45	45	

**SN54HC257, SN54HC258, SN74HC257, SN74HC258**  
**QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MUXES**  
**WITH 3-STATE OUTPUTS**

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switching characteristics over recommended operating free-air temperature range,  $C_L = 50 \text{ pF}$  (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC}$	$T_A = 25^\circ\text{C}$			SN54HC258	SN74HC258	UNIT
				MIN	TYP	MAX	MIN	MAX	
$t_{pd}$	A or B	Any Y	2 V	60	100	150	125		ns
			4.5 V	13	20	30	25		
			6 V	12	17	25	21		
	$\overline{A}/B$	Any Y	2 V	60	115	175	145		
			4.5 V	13	23	35	29		
			6 V	12	20	30	25		
$t_{en}$	$\overline{G}$	Any Y	2 V	70	150	225	190		ns
			4.5 V	15	30	45	38		
			6 V	13	26	38	32		
$t_{dis}$	$\overline{G}$	Any Y	2 V	75	150	225	190		ns
			4.5 V	15	30	45	38		
			6 V	13	26	38	32		
$t_t$		Any Y	2 V	28	60	90	75		ns
			4.5 V	8	12	18	15		
			6 V	6	10	15	13		

switching characteristics over recommended operating free-air temperature range,  $C_L = 150 \text{ pF}$  (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC}$	$T_A = 25^\circ\text{C}$			SN54HC258	SN74HC258	UNIT
				MIN	TYP	MAX	MIN	MAX	
$t_{pd}$	A or B	Any Y	2 V	95	150	245	190		ns
			4.5 V	23	30	45	38		
			6 V	21	26	38	32		
	$\overline{A}/B$	Any Y	2 V	95	165	240	210		
			4.5 V	23	33	48	42		
			6 V	21	28	41	36		
$t_{en}$	$\overline{G}$	Any Y	2 V	100	200	300	250		ns
			4.5 V	24	40	60	50		
			6 V	18	34	51	43		
$t_t$		Any Y	2 V	45	210	315	265		ns
			4.5 V	17	42	63	53		
			6 V	13	36	53	45		

operating characteristics,  $T_A = 25^\circ\text{C}$

PARAMETER	TEST CONDITIONS	TYP	UNIT
$C_{pd}$ Power dissipation capacitance per multiplexer	No load	40	pF

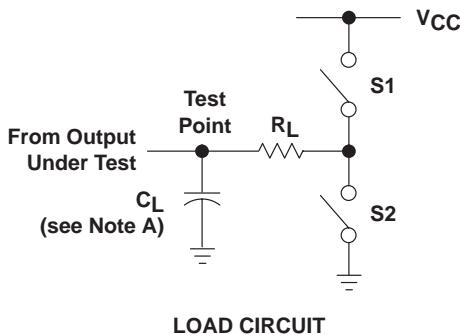


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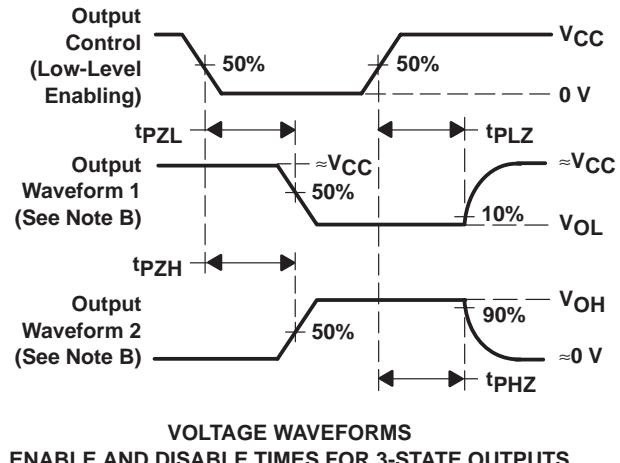
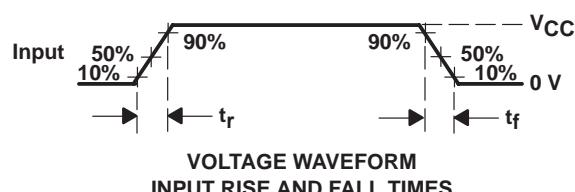
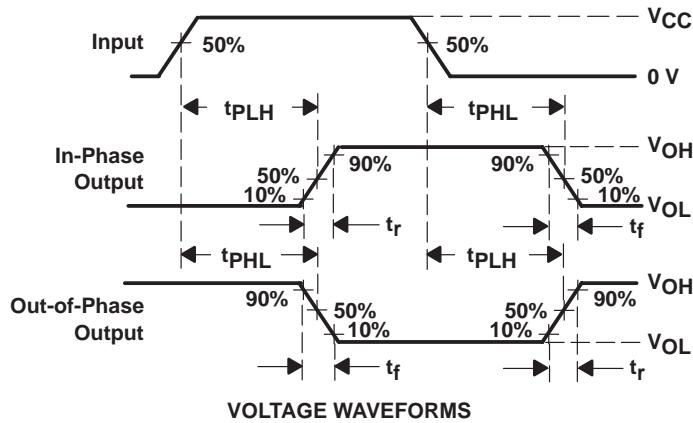
**SN54HC257, SN54HC258, SN74HC257, SN74HC258**  
**QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MUXES**  
**WITH 3-STATE OUTPUTS**

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**PARAMETER MEASUREMENT INFORMATION**



PARAMETER	R <sub>L</sub>	C <sub>L</sub>	S1	S2
t <sub>en</sub>	1 kΩ	50 pF or 150 pF	Open	Closed
			Closed	Open
t <sub>dis</sub>	1 kΩ	50 pF	Open	Closed
			Closed	Open
t <sub>pd</sub> or t <sub>t</sub>	--	50 pF or 150 pF	Open	Open



- NOTES:
- A. C<sub>L</sub> includes probe and test-fixture capacitance.
  - B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
  - C. Phase relationships between waveforms were chosen arbitrarily. All input pulses are supplied by generators having the following characteristics: PRR ≤ 1 MHz, Z<sub>O</sub> = 50 Ω, t<sub>r</sub> = 6 ns, t<sub>f</sub> = 6 ns.
  - D. The outputs are measured one at a time with one input transition per measurement.
  - E. t<sub>PZL</sub> and t<sub>PHZ</sub> are the same as t<sub>dis</sub>.
  - F. t<sub>PZL</sub> and t<sub>PZH</sub> are the same as t<sub>en</sub>.
  - G. t<sub>PLH</sub> and t<sub>PHL</sub> are the same as t<sub>pd</sub>.

**Figure 1. Load Circuit and Voltage Waveforms**

**PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
85124012A	ACTIVE	LCCC	FK	20	1	TBD	Call TI	Level-NC-NC-NC
8512401EA	ACTIVE	CDIP	J	16	1	TBD	Call TI	Level-NC-NC-NC
SN54HC257J	ACTIVE	CDIP	J	16	1	TBD	Call TI	Level-NC-NC-NC
SN74HC257D	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC257DE4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC257DR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC257DRE4	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC257DT	ACTIVE	SOIC	D	16	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC257DTE4	ACTIVE	SOIC	D	16	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC257N	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
SN74HC257NE4	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
SN74HC257NSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC257NSRE4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC257PW	ACTIVE	TSSOP	PW	16	90	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC257PWG4	ACTIVE	TSSOP	PW	16	90	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC257PWLE	OBsolete	TSSOP	PW	16		TBD	Call TI	Call TI
SN74HC257PWR	ACTIVE	TSSOP	PW	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC257PWRG4	ACTIVE	TSSOP	PW	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC257PWT	ACTIVE	TSSOP	PW	16	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC257PWTE4	ACTIVE	TSSOP	PW	16	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC258D	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC258DE4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC258DR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC258DRE4	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC258DT	ACTIVE	SOIC	D	16	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC258DTE4	ACTIVE	SOIC	D	16	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC258N	ACTIVE	PDIP	N	16	25	Pb-Free	CU NIPDAU	Level-NC-NC-NC

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
(RoHS)								
SN74HC258NE4	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
SN74HC258NSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC258NSRE4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC258PW	ACTIVE	TSSOP	PW	16	90	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC258PWE4	ACTIVE	TSSOP	PW	16	90	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC258PWR	ACTIVE	TSSOP	PW	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC258PWRE4	ACTIVE	TSSOP	PW	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC258PWT	ACTIVE	TSSOP	PW	16	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74HC258PWTE4	ACTIVE	TSSOP	PW	16	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SNJ54HC257FK	ACTIVE	LCCC	FK	20	1	TBD	Call TI	Level-NC-NC-NC
SNJ54HC257J	ACTIVE	CDIP	J	16	1	TBD	Call TI	Level-NC-NC-NC

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

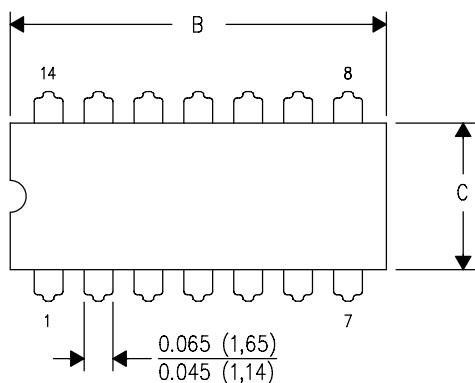
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In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

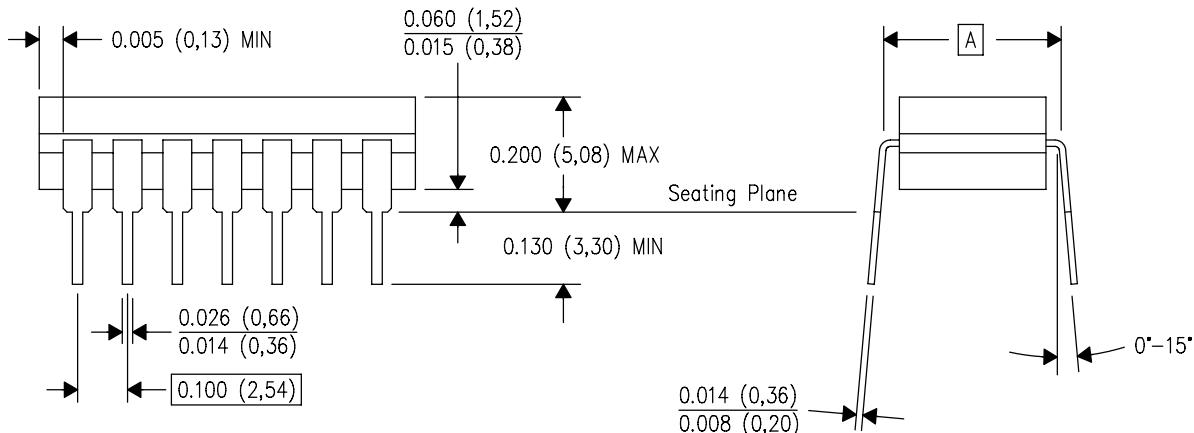
J (R-GDIP-T\*\*)

14 LEADS SHOWN

CERAMIC DUAL IN-LINE PACKAGE



PINS **\nDIM	14	16	18	20
A	0.300 (7,62) BSC	0.300 (7,62) BSC	0.300 (7,62) BSC	0.300 (7,62) BSC
B MAX	0.785 (19,94)	.840 (21,34)	0.960 (24,38)	1.060 (26,92)
B MIN	—	—	—	—
C MAX	0.300 (7,62)	0.300 (7,62)	0.310 (7,87)	0.300 (7,62)
C MIN	0.245 (6,22)	0.245 (6,22)	0.220 (5,59)	0.245 (6,22)



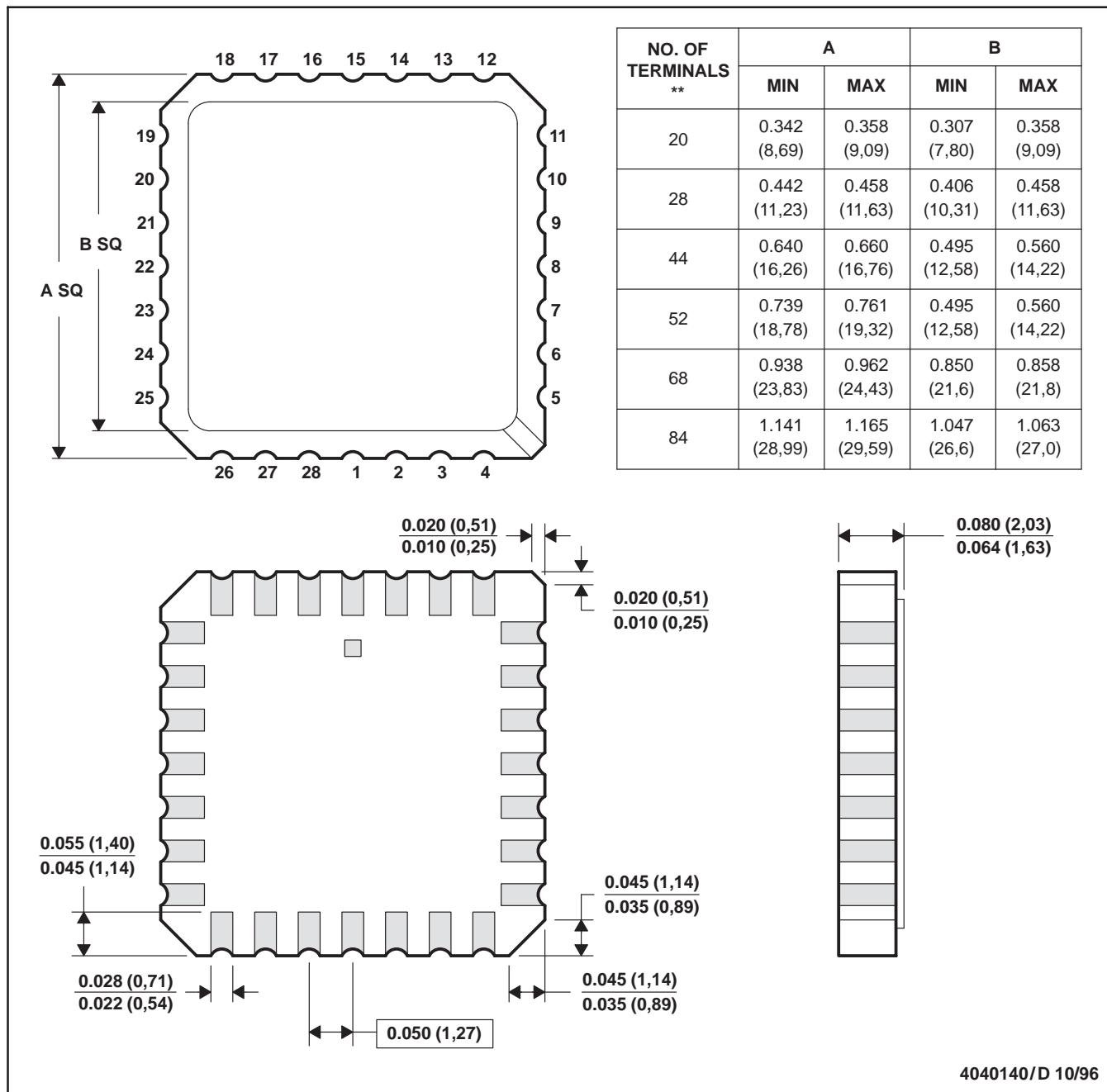
4040083/F 03/03

- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. This package is hermetically sealed with a ceramic lid using glass frit.
  - D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
  - E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

## FK (S-CQCC-N\*\*)

## LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

C. This package can be hermetically sealed with a metal lid.

D. The terminals are gold plated.

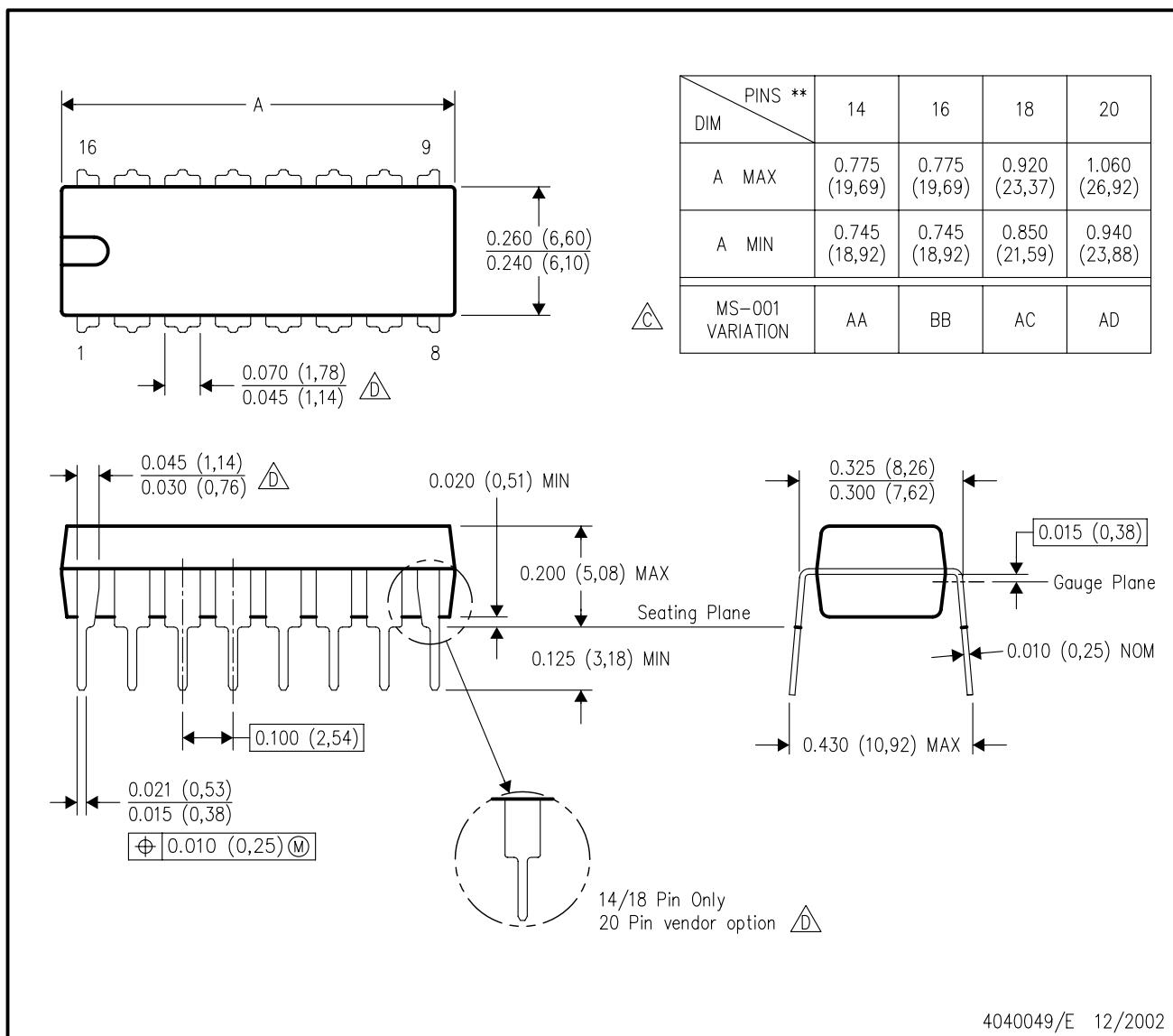
E. Falls within JEDEC MS-004

4040140/D 10/96

## N (R-PDIP-T\*\*)

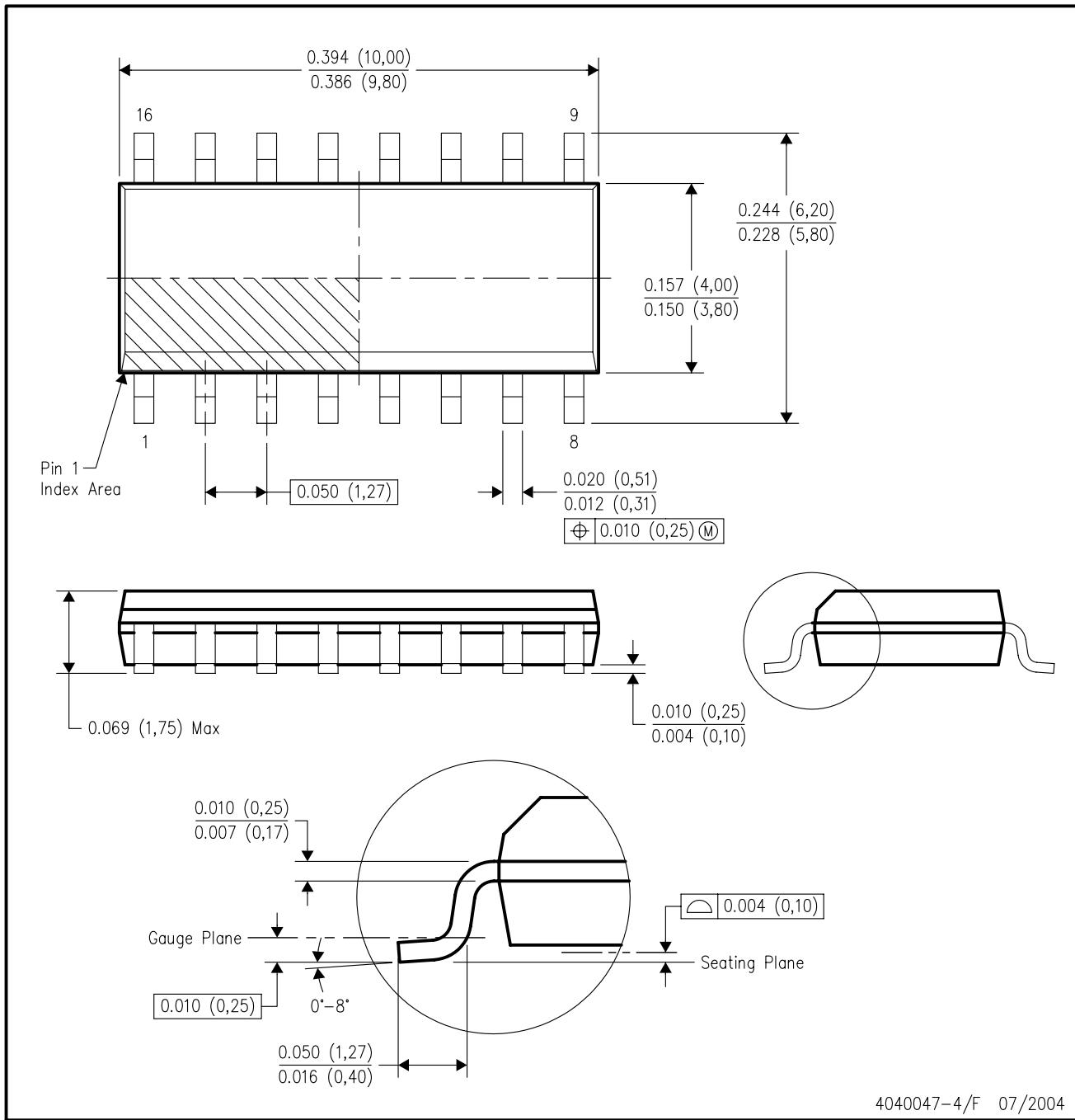
16 PINS SHOWN

## PLASTIC DUAL-IN-LINE PACKAGE



## D (R-PDSO-G16)

## PLASTIC SMALL-OUTLINE PACKAGE



4040047-4/F 07/2004

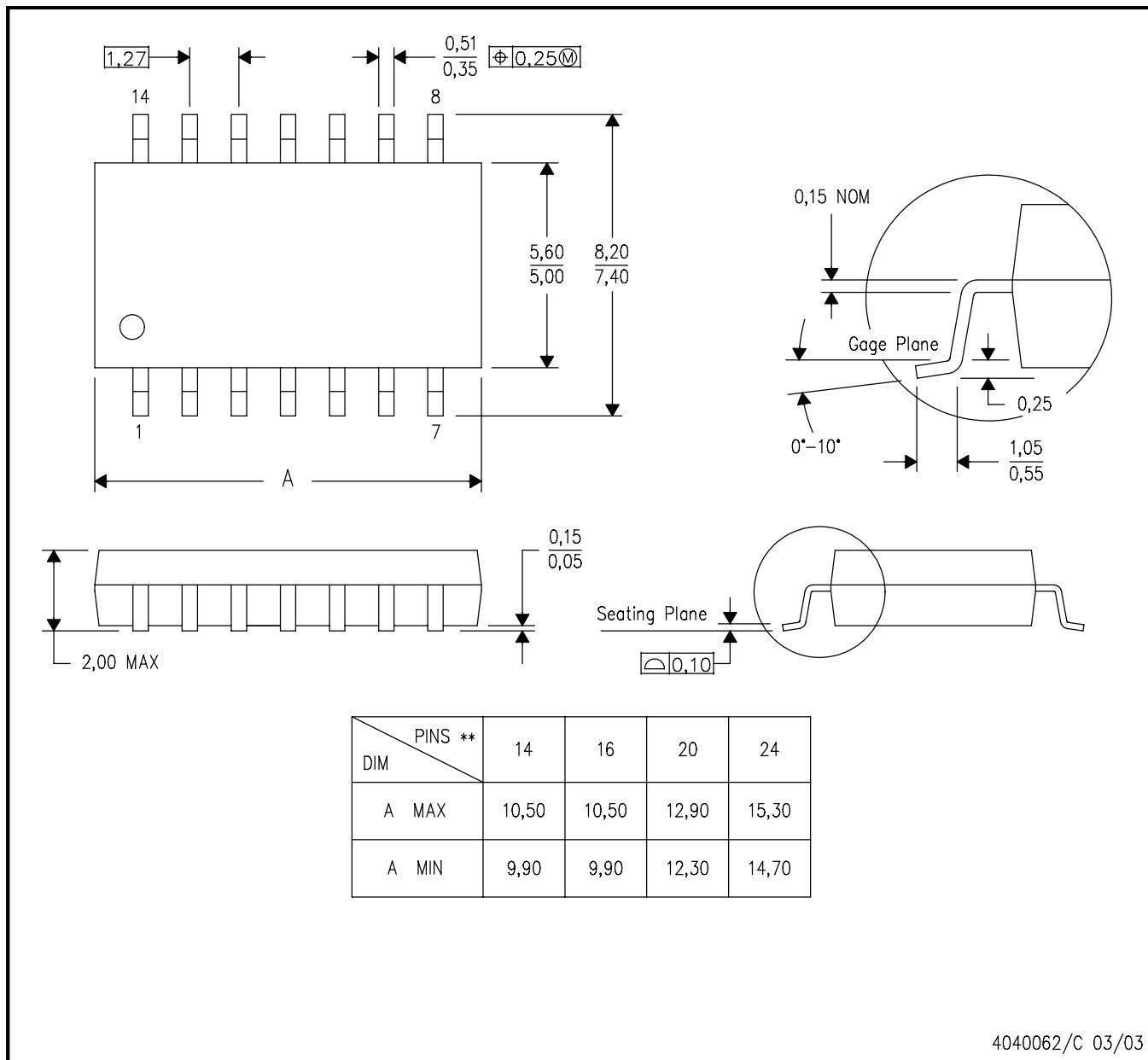
- NOTES:
- All linear dimensions are in inches (millimeters).
  - This drawing is subject to change without notice.
  - Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
  - Falls within JEDEC MS-012 variation AC.

## MECHANICAL DATA

**NS (R-PDSO-G\*\*)**

**14-PINS SHOWN**

**PLASTIC SMALL-OUTLINE PACKAGE**

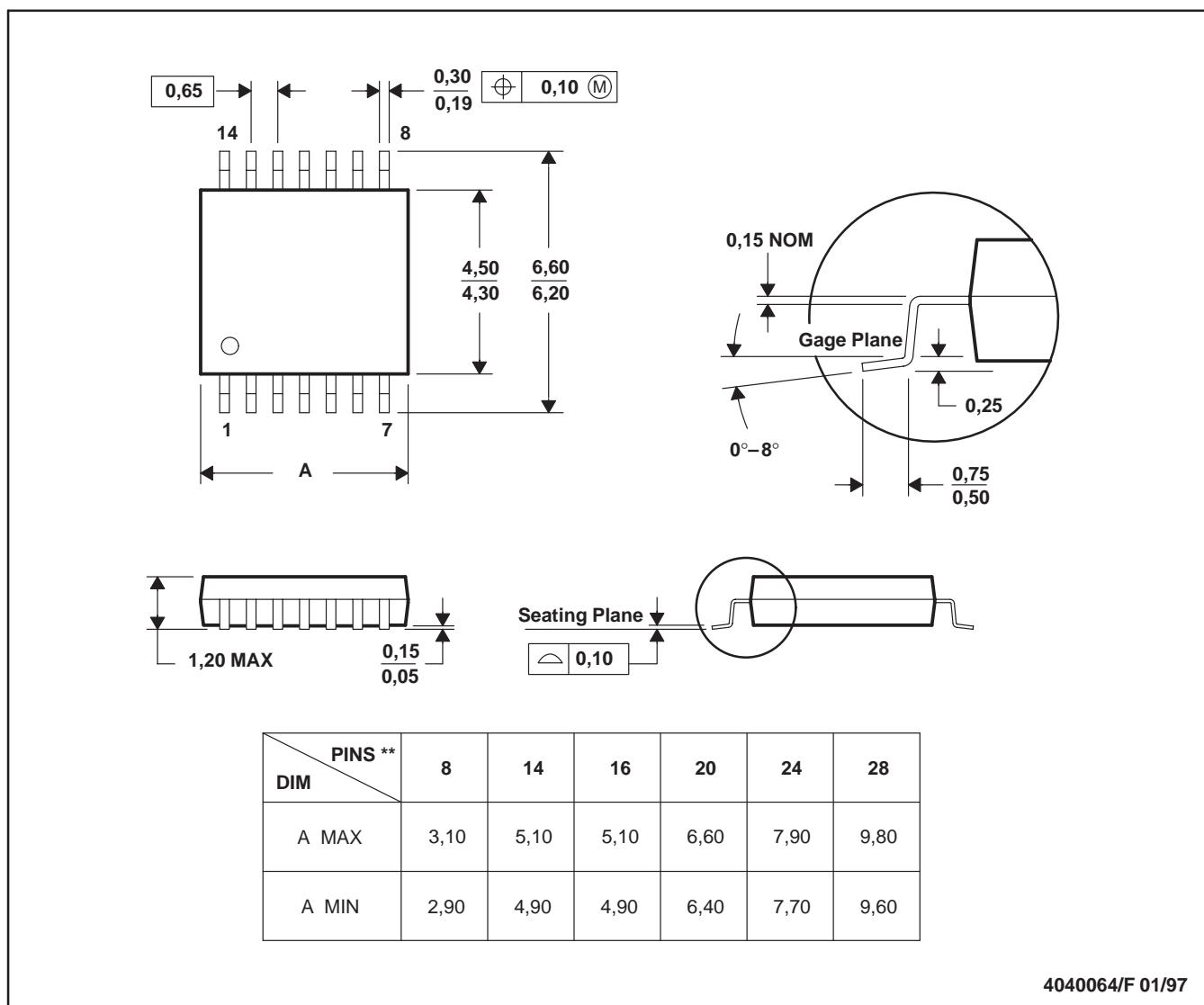


- NOTES: A. All linear dimensions are in millimeters.  
 B. This drawing is subject to change without notice.  
 C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

## PW (R-PDSO-G\*\*)

## PLASTIC SMALL-OUTLINE PACKAGE

14 PINS SHOWN



- NOTES:
- All linear dimensions are in millimeters.
  - This drawing is subject to change without notice.
  - Body dimensions do not include mold flash or protrusion not to exceed 0,15.
  - Falls within JEDEC MO-153

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Interface	interface.ti.com	Digital Control	<a href="http://www.ti.com/digitalcontrol">www.ti.com/digitalcontrol</a>
Logic	logic.ti.com	Military	<a href="http://www.ti.com/military">www.ti.com/military</a>
Power Mgmt	power.ti.com	Optical Networking	<a href="http://www.ti.com/opticalnetwork">www.ti.com/opticalnetwork</a>
Microcontrollers	microcontroller.ti.com	Security	<a href="http://www.ti.com/security">www.ti.com/security</a>
		Telephony	<a href="http://www.ti.com/telephony">www.ti.com/telephony</a>
		Video & Imaging	<a href="http://www.ti.com/video">www.ti.com/video</a>
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**SN74HC257, Status: ACTIVE**

Quadruple 2-Line To 1-Line Data Selectors/Multiplexers With 3-State Outputs

 View ROHS Compliant Devices clear gif

View RoHS Compliant Devices

 clear gif Features Quality & Pb-Free Data Related Products Tools & Software Samples Pricing/Packaging Inventory Symbols/Footprints Technical Documents Applications Notes Simulation Models Reference Designs**Refine Your Selection**

- Logic: Data Selectors/Multiplexers

**Support**

- KnowledgeBase
- Contact Technical Support
- TI Cross Reference
- Training
- Part Marking Lookup
- Part Number Nomenclature

**Datasheet****SN54HC257, SN54HC258, SN74HC257, SN74HC258 (Rev.****B)** (sn74hc257.pdf, 478 KB)15 Sep 2003 [Download](#)

	<b>SN54HC257</b>	<b>SN74HC257</b>
<b>Voltage Nodes(V)</b>	6, 5, 2	6, 5, 2
<b>Vcc range(V)</b>	2.0 to 6.0	2.0 to 6.0
<b>Input Level</b>	CMOS	CMOS
<b>Output Level</b>	CMOS	CMOS
<b>Output Drive(mA)</b>		-6/6
<b>Output</b>	3S	3S
<b>From</b>	2	2
<b>To</b>	1	1
	<a href="#">Samples</a>	<a href="#">Samples</a>
	<a href="#">Inventory</a>	<a href="#">Inventory</a>

**Product Information** Features Save this to your personal library

Wide Operating Voltage Range of 2 V to 6 V  
 High-Current Inverting Outputs Drive up to 15 LSTTL Loads  
 Low Power Consumption, 80- $\mu$ A Max I<sub>CC</sub>  
 'HC257 ...Typical t<sub>pd</sub> = 9 ns  
 'HC258 ...Typical t<sub>pd</sub> = 12 ns  
 $\pm$ 6-mA Output Drive at 5 V  
 Low Input Current of 1  $\mu$ A Max  
 Provides Bus Interface from Multiple Sources in High-Performance Systems

 Description

These devices are designed to multiplex signals from 4-bit data sources to 4-output data lines in bus-organized systems. The 3-state outputs do not load the data lines when the output-enable (G\l) input is at a high logic level.

To ensure the high-impedance state during power up or power down, (G\l) should be tied to V<sub>CC</sub> through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

### Pricing/Packaging/CAD Design Tools/Samples

				Price	Packaging			CAD Design Tools	Samples
Device	Status	Temp (°C)	Budget Price (\$US)   QTY	Industry Standard (TI Pkg)   Pins	Top Side Marking	Standard Pack Quantity	Footprints		Samples
SN74HC257D	ACTIVE	-40 to 85	0.23   1KU	SOIC (D)   16	View	40		<input type="checkbox"/>	Purchase Samples
SN74HC257DE4	ACTIVE	-40 to 85	0.23   1KU	SOIC (D)   16	View	40		<input type="checkbox"/>	Purchase Samples
SN74HC257DR	ACTIVE	-40 to 85	0.23   1KU	SOIC (D)   16	View	2500		<input type="checkbox"/>	Contact TI Distributor or Sales Office
SN74HC257DRE4	ACTIVE	-40 to 85	0.23   1KU	SOIC (D)   16	View	2500		<input type="checkbox"/>	Request Free Samples
SN74HC257DT	ACTIVE	-40 to 85	0.27   1KU	SOIC (D)   16	View	250		<input type="checkbox"/>	Purchase Samples
SN74HC257DTE4	ACTIVE	-40 to 85	0.27   1KU	SOIC (D)   16	View	250		<input type="checkbox"/>	Purchase Samples
SN74HC257N	ACTIVE	-40 to 85	0.24   1KU	PDIP (N)   16	View	25		<input type="checkbox"/>	Contact TI Distributor or Sales Office
SN74HC257NE4	ACTIVE	-40 to 85	0.24   1KU	PDIP (N)   16	View	25		<input type="checkbox"/>	Request Free Samples
SN74HC257NSR	ACTIVE	-40 to 85	0.23   1KU	SO (NS)   16	View	2000		<input type="checkbox"/>	Contact TI Distributor or Sales Office
SN74HC257NSRE4	ACTIVE	-40 to 85	0.23   1KU	SO (NS)   16	View	2000		<input type="checkbox"/>	Purchase Samples
SN74HC257PW	ACTIVE	-40 to 85	0.23   1KU	TSSOP (PW)   16	View	90		<input type="checkbox"/>	Purchase Samples
SN74HC257PWG4	ACTIVE	-40 to 85	0.25   1KU	TSSOP (PW)   16	View	90		<input type="checkbox"/>	Purchase Samples
SN74HC257PWLE	OBsolete	-40 to 85		TSSOP (PW)   16	View			<input type="checkbox"/>	Not Available
SN74HC257PWR	ACTIVE	-40 to 85	0.23   1KU	TSSOP (PW)   16	View	2000		<input type="checkbox"/>	Contact TI Distributor or Sales Office
SN74HC257PWRG4	ACTIVE	-40 to 85	0.25   1KU	TSSOP (PW)   16	View	2000		<input type="checkbox"/>	Purchase Samples
SN74HC257PWT	ACTIVE	-40 to 85	0.30   1KU	TSSOP (PW)   16	View	250		<input type="checkbox"/>	Purchase Samples
SN74HC257PWTE4	ACTIVE	-40 to 85	0.30   1KU	TSSOP (PW)   16	View	250		<input type="checkbox"/>	Purchase Samples

### Inventory

	TI Inventory Status			Reported Distributor Inventory			
SN74HC257D	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	3838   21 Dec	10 Weeks	Americas	DigiKey	>1k	<input type="text"/>
		3000   22 Dec			Newark InOne	>1k	<input type="text"/>
		8107   5 Jan		Europe	Arrow Southern Europe	>1k	<input type="text"/>
					Avnet-SILICA	400	<input type="text"/>
					EBV Elektronik	120	<input type="text"/>
SN74HC257DE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase

[View all Distributors](#)

[Choose a Region](#)



	0*	3838   21 Dec	10 Weeks	None Reported <a href="#">View Distributors</a>			
		3000   22 Dec					
		8107   5 Jan					
<b>SN74HC257DR</b>	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	<b>In Stock</b>	<b>In Progress QTY   Date</b>	<b>Lead Time</b>	<b>Region</b>	<b>Company</b>	<b>In Stock</b>	<b>Purchase</b>
	0*	7500   23 Dec	10 Weeks	Americas	DigiKey	107	
		7202   9 Jan					
<b>SN74HC257DRE4</b>	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	<b>In Stock</b>	<b>In Progress QTY   Date</b>	<b>Lead Time</b>	<b>Region</b>	<b>Company</b>	<b>In Stock</b>	<b>Purchase</b>
	0*	7500   23 Dec	10 Weeks	None Reported <a href="#">View Distributors</a>			
		7202   9 Jan					
<b>SN74HC257DT</b>	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	<b>In Stock</b>	<b>In Progress QTY   Date</b>	<b>Lead Time</b>	<b>Region</b>	<b>Company</b>	<b>In Stock</b>	<b>Purchase</b>
	0*	3813   21 Dec	10 Weeks	None Reported <a href="#">View Distributors</a>			
		>10k   5 Jan					
<b>SN74HC257DTE4</b>	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	<b>In Stock</b>	<b>In Progress QTY   Date</b>	<b>Lead Time</b>	<b>Region</b>	<b>Company</b>	<b>In Stock</b>	<b>Purchase</b>
	0*	3813   21 Dec	10 Weeks	None Reported <a href="#">View Distributors</a>			
		>10k   5 Jan					
<b>SN74HC257N</b>	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	<b>In Stock</b>	<b>In Progress QTY   Date</b>	<b>Lead Time</b>	<b>Region</b>	<b>Company</b>	<b>In Stock</b>	<b>Purchase</b>
	3200*	1000   19 Dec	10 Weeks	Americas	Avnet	>1k	
		800   20 Dec			DigiKey	986	
		5000   28 Dec			Newark InOne	155	
		4748   16 Jan		Europe	Arrow Northern Europe	>1k	
					Avnet-SILICA	650	
					EBV Elektronik	>1k	
					Rutronik	>1k	
<b>SN74HC257NE4</b>	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	<b>In Stock</b>	<b>In Progress QTY   Date</b>	<b>Lead Time</b>	<b>Region</b>	<b>Company</b>	<b>In Stock</b>	<b>Purchase</b>
	3200*	1000   19 Dec	10 Weeks	None Reported <a href="#">View Distributors</a>			
		800   20 Dec					
		5000   28 Dec					
		4748   16 Jan					
<b>SN74HC257NSR</b>	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	<b>In Stock</b>	<b>In Progress QTY   Date</b>	<b>Lead Time</b>	<b>Region</b>	<b>Company</b>	<b>In Stock</b>	<b>Purchase</b>

	0*	2000   16 Dec	10 Weeks	Americas	DigiKey	>1k	
		260   13 Jan					
		685   20 Jan					
		617   27 Jan					
		930   3 Feb					
SN74HC257NSRE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	2000   16 Dec	10 Weeks	None Reported	<a href="#">View Distributors</a>		
		260   13 Jan					
		685   20 Jan					
		617   27 Jan					
		930   3 Feb					
SN74HC257PW	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	2700*	>10k   3 Apr	12 Weeks	None Reported	<a href="#">View Distributors</a>		
SN74HC257PWG4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	1440*	>10k   3 Apr	12 Weeks	None Reported	<a href="#">View Distributors</a>		
SN74HC257PWR	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*		16 Weeks	None Reported	<a href="#">View Distributors</a>		
SN74HC257PWRG4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*	>10k   3 Apr	16 Weeks	None Reported	<a href="#">View Distributors</a>		
SN74HC257PWT	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*		16 Weeks	None Reported	<a href="#">View Distributors</a>		
SN74HC257PWTE4	As of 9:11 AM GMT, 29 Nov 2005			As of 9:11 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY   Date	Lead Time	Region	Company	In Stock	Purchase
	0*		16 Weeks	None Reported	<a href="#">View Distributors</a>		

\* Our information is updated daily, so please check back with us soon if this does not meet your needs. You may also contact your [TI Authorized Distributor](#), including those [listed above](#), for real time stock information.

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## Quality & Lead (Pb)-Free Data

	Product Content				MTBF/FIT Rate
Device	Eco Plan*	Lead/Ball Finish	MSL Rating/Peak Reflow	Details	Details
SN74HC257D <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>
SN74HC257DE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>
SN74HC257DR <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>
SN74HC257DRE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>
SN74HC257DT <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>
SN74HC257DTE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>
SN74HC257N <input type="checkbox"/>	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC	<a href="#">View</a>	<a href="#">View</a>
SN74HC257NE4 <input type="checkbox"/>	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC	<a href="#">View</a>	<a href="#">View</a>
SN74HC257NSR <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>
SN74HC257NSRE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>
SN74HC257PW <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>
SN74HC257PWG4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>
SN74HC257PWR <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>
SN74HC257PWRG4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>
SN74HC257PWT <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>
SN74HC257PWTE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	<a href="#">View</a>	<a href="#">View</a>

\* The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please click on the Product Content Details "View" link in the table above for the latest availability information and additional product content details.

If the information you are requesting is not available online at this time, contact one of our [Product Information Centers](#) regarding the availability of this information.

## Technical Documents

### Datasheets

[Keep track of what's new](#)

**SN54HC257, SN54HC258, SN74HC257, SN74HC258 (Rev. B)** ([sn74hc257.pdf](#), 478 KB)

15 Sep 2003 [Download](#)

### Application Notes

**Semiconductor Packing Material Electrostatic Discharge (ESD) Protection** ([szza047.htm](#), 9 KB)

08 Jul 2004 [Abstract](#)

**Shelf-Life Evaluation of Lead-Free Component Finishes** ([szza046.htm](#), 9 KB)

24 May 2004 [Abstract](#)

**Understanding and Interpreting Standard-Logic Data Sheets (Rev. B)** ([szza036b.htm](#), 8 KB)

28 May 2003 [Abstract](#)

**TI IBIS File Creation, Validation, and Distribution Processes** ([szza034.htm](#), 9 KB)

29 Aug 2002 [Abstract](#)

**Selecting the Right Texas Instruments Signal Switch** ([szza030.htm](#), 9 KB)

07 Sep 2001 [Abstract](#)

**Implications of Slow or Floating CMOS Inputs (Rev. C)** ([scba004c.htm](#), 9 KB)

01 Feb 1998 [Abstract](#)

**CMOS Power Consumption and CPD Calculation (Rev. B)** ([scaa035b.htm](#), 9 KB)

01 Jun 1997 [Abstract](#)

**Designing With Logic (Rev. C)** ([sdya009c.htm](#), 9 KB)

01 Jun 1997 [Abstract](#)

**Live Insertion** ([sdya012.htm](#), 9 KB)

01 Oct 1996 [Abstract](#)

**Input and Output Characteristics of Digital Integrated Circuits** ([sdya010.htm](#), 9 KB)

01 Oct 1996 [Abstract](#)

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